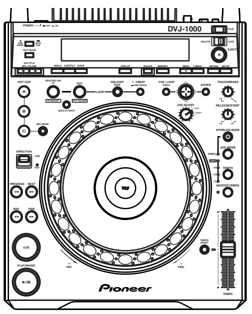
Pioneer sound.vision.soul

Service Manual



DVJ-1000

ORDER NO.
RRV3454

DVD PLAYER

DVJ-1000

THIS MANUAL IS APPLICABLE TO THE FOLLOWING MODEL(S) AND TYPE(S).

Model	Type	Power Requirement	Region No.	Remarks
DVJ-1000	KUCXJ	AC 120 V	1	
DVJ-1000	WYXJ5	AC 220 – 240 V	2	
DVJ-1000	TLXJ/RD	AC 110-120 V / 220-240 V	4	
DVJ-1000	TLFXJ	AC 110-120 V / 220-240 V	3	

For details, refer to "Important symbols for good services" on the next page.

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SAFETY INFORMATION

This service manual is intended for qualified service technicians ; it is not meant for the casual do-it-yourselfer. Qualified technicians have the necessary test equipment and tools, and have been trained to properly and safely repair complex products such as those covered by this manual.
Improperly performed repairs can adversely affect the safety and reliability of the product and may void the warranty. If you are not qualified to perform the repair of this product properly and safely, you should not risk trying to do so and refer the repair to a qualified service technician.

WARNING
This product contains lead in solder and certain electrical parts contain chemicals which are known to the state of California to causecancer, birth defects or other reproductive harm.

Health & Safety Code Section 25249.6 – Proposition 65

NOTICE
(FOR CANADIAN MODEL ONLY)
Fuse symbols (fast operating fuse) and/or (slow operating fuse) on PCB indicate that replacement parts must be of identical designation.

REMARQUE
(POUR MODÈLE CANADIEN SEULEMENT)
Les symboles de fusible (fusible de type rapide) et/ou (fusible de type lent) sur CCI indiquent que les pièces de remplacement doivent avoir la même désignation.

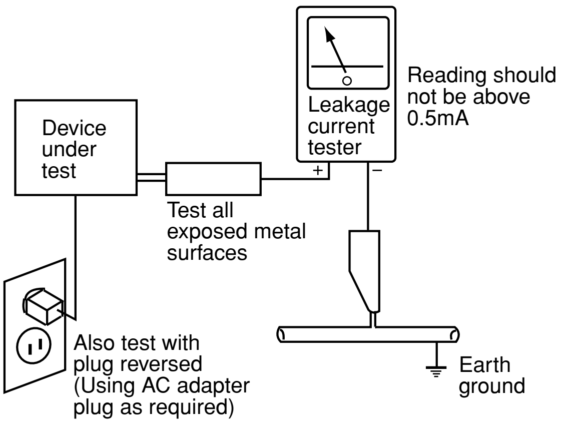
(FOR USA MODEL ONLY)

1. SAFETY PRECAUTIONS

The following check should be performed for the continued protection of the customer and service technician.

LEAKAGE CURRENT CHECK

Measure leakage current to a known earth ground (waterpipe, conduit, etc.) by connecting a leakage current tester such as Simpson Model 229-2 or equivalent between the earth ground and all exposed metal parts of the appliance (input/output terminals, screwheads, metal overlays, control shaft, etc.). Plug the AC line cord of the appliance directly into a 120V AC 60Hz outlet and turn the AC power switch on. Any current measured must not exceed 0.5mA.


Reading should not be above 0.5mA
Leakage current tester
Device under test
+
−
Test all exposed metal surfaces
Also test with plug reversed (Using AC adapter plug as required)
Earth ground

AC Leakage Test

ANY MEASUREMENTS NOT WITHIN THE LIMITS OUTLINED ABOVE ARE INDICATIVE OF A POTENTIAL SHOCK HAZARD AND MUST BE CORRECTED BEFORE RETURNING THE APPLIANCE TO THE CUSTOMER.

2. PRODUCT SAFETY NOTICE

Many electrical and mechanical parts in the appliance have special safety related characteristics. These are often not evident from visual inspection nor the protection afforded by them necessarily can be obtained by using replacement components rated for voltage, wattage, etc. Replacement parts which have these special safety characteristics are identified in this Service Manual.

Electrical components having such features are identified by marking with a ⚠ on the schematics and on the parts list in this Service Manual.

The use of a substitute replacement component which does not have the same safety characteristics as the PIONEER recommended replacement one, shown in the parts list in this Service Manual, may create shock, fire, or other hazards.

Product Safety is continuously under review and new instructions are issued from time to time. For the latest information, always consult the current PIONEER Service Manual. A subscription to, or additional copies of, PIONEER Service Manual may be obtained at a nominal charge from PIONEER.

IMPORTANT

THIS PIONEER APPARATUS CONTAINS LASER OF CLASS 1.
SERVICING OPERATION OF THE APPARATUS SHOULD BE DONE BY A SPECIALLY INSTRUCTED PERSON.

LASER DIODE CHARACTERISTICS

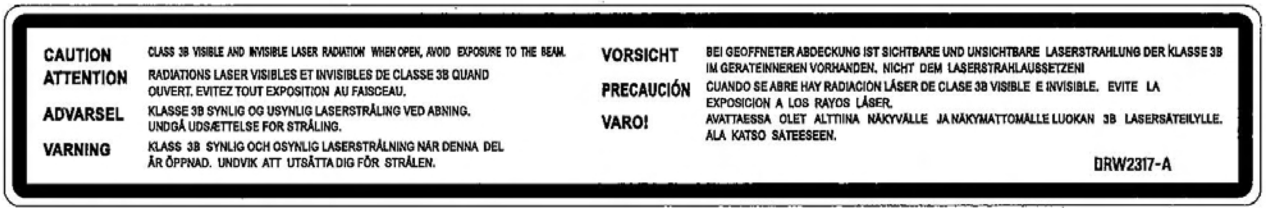
MAXIMUM OUTPUT POWER : 5 mW
WAVELENGTH : 780 – 785 nm

WARNING !

THE AEL (ACCESSIBLE EMISSION LEVEL) OF THE LASER POWER OUTPUT IS LESS THAN **CLASS 1** BUT THE LASER COMPONENT IS CAPABLE OF EMITTING RADIATION EXCEEDING THE LIMIT FOR **CLASS 1**.
A SPECIALLY INSTRUCTED PERSON SHOULD DO SERVICING OPERATION OF THE APPARATUS.

LABEL CHECK (for WYXJ5, KUCXJ and TLXJ/RD types)

for WYXJ5, KUCXJ and TLXJ/RD types



CAUTION CLASS 3B VISIBLE AND INVISIBLE LASER RADIATION WHEN OPEN, AVOID EXPOSURE TO THE BEAM.

ATTENTION RADIATIONS LASER VISIBLES ET INVISIBLES DE CLASSE 3B QUAND OUVERT. EVITEZ TOUT EXPOSITION AU FAISCEAU.

ADVARSEL KLASSE 3B SYNLIG OG USYNLIG LASERSTRÅLING VED ABNING. UNDGÅ UDSÆTTELSE FOR STRÅLING.

VARNING KLASS 3B SYNLIG OCH OSYNLIG LASERSTRÅLNING NÄR DENNA DEL ÄR ÖPPNAD. UNDVIK ATT UTSÄTTA DIG FÖR STRÅLEN.

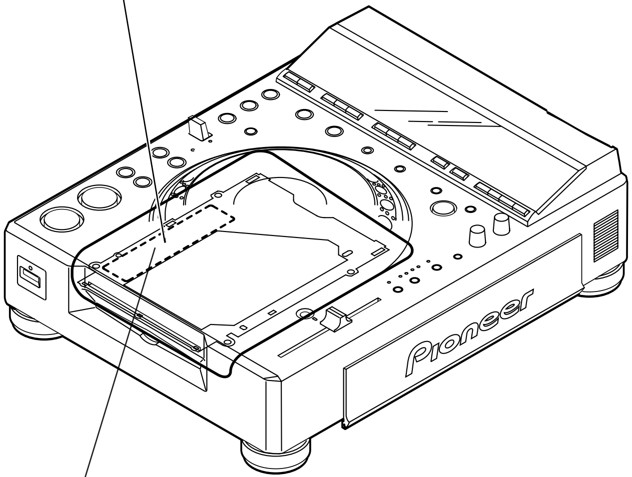
VORSICHT BEI GEOFFNETER ABDECKUNG IST SICHTBARE UND UNSICHTBARE LASERSTRAHLUNG DER KLASSE 3B IM GERATEINNEREN VORHANDEN. NICHT DEM LASERSTRAHLAUSSETZEN!

PRECAUCIÓN CUANDO SE ABRE HAY RADIACION LÁSER DE CLASE 3B VISIBLE E INVISIBLE. EVITE LA EXPOSICION A LOS RAYOS LÁSER.

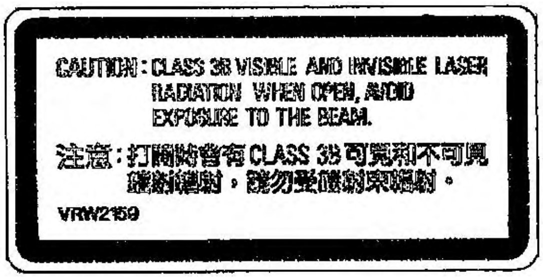
VARO! AVATTAESSA OLET ALTTIINA NÄKYVÄLLE JA NÄKYMATTOMÄLLE LUOKAN 3B LASERSÄTEILYLLE. ÄLÄ KATSO SÄTEESEEN.

DRW2317-A

(DRW2317)



for TLFXJ type



CAUTION : CLASS 3B VISIBLE AND INVISIBLE LASER RADIATION WHEN OPEN, AVOID EXPOSURE TO THE BEAM.

注意：打開時會有CLASS 3B可見和不可見雷射輻射，請勿受雷射束照射。

VRW2159

(VRW2159)

Additional Laser Caution

1. Laser Interlock Mechanism
   The position of the switch (S2401) for detecting loading completion is detected by the system microprocessor, and the design prevents laser diode oscillation when the switch is not in LPS1 terminal side (when the mechanism is not clamped and LPS1 signal is high level.)
   Thus, the interlock will no longer function if the switch is deliberately set to LPS1 terminal side. (if LPS1 signal is low level).
   In the test mode∗ the interlock mechanism will not function.
   Laser diode oscillation will continue, if pin 5 of AN22022A (IC7000) on the DVPL Assy is connected to GND, or else the terminals of Q7001(Q7002) are shorted to each other (fault condition).

2. When the cover is opened, close viewing of the objective lens with the naked eye will cause exposure to a Class 1 laser beam.

[Important Check Points for Good Servicing]

In this manual, procedures that must be performed during repairs are marked with the below symbol.
Please be sure to confirm and follow these procedures.

1. Product safety

Please conform to product regulations (such as safety and radiation regulations), and maintain a safe servicing environment by following the safety instructions described in this manual.

① Use specified parts for repair.

Use genuine parts. Be sure to use important parts for safety.

② Do not perform modifications without proper instructions.

Please follow the specified safety methods when modification(addition/change of parts) is required due to interferences such as radio/TV interference and foreign noise.

③ Make sure the soldering of repaired locations is properly performed.

When you solder while repairing, please be sure that there are no cold solder and other debris.
Soldering should be finished with the proper quantity. (Refer to the example)

④ Make sure the screws are tightly fastened.

Please be sure that all screws are fastened, and that there are no loose screws.

⑤ Make sure each connectors are correctly inserted.

Please be sure that all connectors are inserted, and that there are no imperfect insertion.

⑥ Make sure the wiring cables are set to their original state.

Please replace the wiring and cables to the original state after repairs.
In addition, be sure that there are no pinched wires, etc.

⑦ Make sure screws and soldering scraps do not remain inside the product.

Please check that neither solder debris nor screws remain inside the product.

⑧ There should be no semi-broken wires, scratches, melting, etc. on the coating of the power cord.

Damaged power cords may lead to fire accidents, so please be sure that there are no damages.
If you find a damaged power cord, please exchange it with a suitable one.

⑨ There should be no spark traces or similar marks on the power plug.

When spark traces or similar marks are found on the power supply plug, please check the connection and advise on secure connections and suitable usage. Please exchange the power cord if necessary.

⑩ Safe environment should be secured during servicing.

When you perform repairs, please pay attention to static electricity, furniture, household articles, etc. in order to prevent injuries.
Please pay attention to your surroundings and repair safely.

2. Adjustments

To keep the original performance of the products, optimum adjustments and confirmation of characteristics within specification.
Adjustments should be performed in accordance with the procedures/instructions described in this manual.

3. Lubricants, Glues, and Replacement parts

Use grease and adhesives that are equal to the specified substance.
Make sure the proper amount is applied.

4. Cleaning

For parts that require cleaning, such as optical pickups, tape deck heads, lenses and mirrors used in projection monitors, proper cleaning should be performed to restore their performances.

5. Shipping mode and Shipping screws

To protect products from damages or failures during transit, the shipping mode should be set or the shipping screws should be installed before shipment. Please be sure to follow this method especially if it is specified in this manual.

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1. SPECIFICATIONS

Specifications

1. General

System	DVD-Video, CD
Power requirements	AC 220 V to 240 V, 50 Hz/60 Hz
Power consumption	40 W
Operating temperature	+5 ˚C to +35 ˚C
Operating humidity	5 % to 85 % (There should be no condensation of moisture.)
Weight	5.4 kg
Dimensions	320 mm (W) 408 mm (D) 109 mm (H)

2. Video Output

Composite output (2)	
Output level	1 V p-p (75 Ω)
Jack	RCA jack(1), BNC jack(1)
S-Video output	
Y (luminance)	1 V p-p (75 Ω)
C (color)	286 mV p-p (75 Ω) (NTSC) 300 mV p-p (75 Ω) (PAL)
Jack	4-pin mini DIN

3. Preview Video Output

Composite output	
Output level	1 V p-p (75 Ω)
Jack	RCA jack
S-Video output	
Y (luminance)	1 V p-p (75 Ω)
C (color)	286 mV p-p (75 Ω) (NTSC) 300 mV p-p (75 Ω) (PAL)
Jack	4-pin mini DIN

4. Audio Output

Audio output (2 channels)	
Output level	2 V rms (1 kHz, 0 dB)
Channels	2
Jack	RCA jack
Digital audio output	
Frequency response (CD)	4 Hz to 20 kHz
Frequency response (DVD, 96 kHz)	4 Hz to 44 kHz
Signal-to-noise ratio	115 dB or more (JEITA)
Distortion	0.004 % (JEITA)
Digital output	
Jack	RCA jack

5. Other jacks

CONTROL jack	Mini phone jack (ø3.5 mm)
SYNC IN jack	BNC jack

6. Accessories

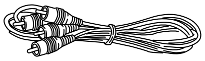
Audio cable	1
Video cable	1
Control cord	1
Power cord	1
Forced eject pin (housed in a groove in the bottom panel)	1
Operating instructions	1

Note:

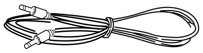
Specifications and design are subject to possible modification without notice.

Accessories

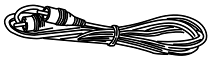
Audio Cable
(VDE1064) L=1.5m



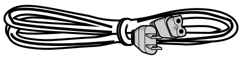
Control Cord
(XDE3063) L=1 m



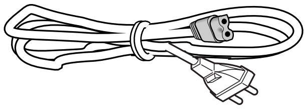
Video Cable
(VDE1065) L=1.5m



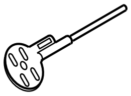
Power Cord
(ADG7021 : KUCXJ)
(ADG7097 : TLFXJ)



(ADG1154 : WYXJ5,TLFXJ
TLXJ/RD)



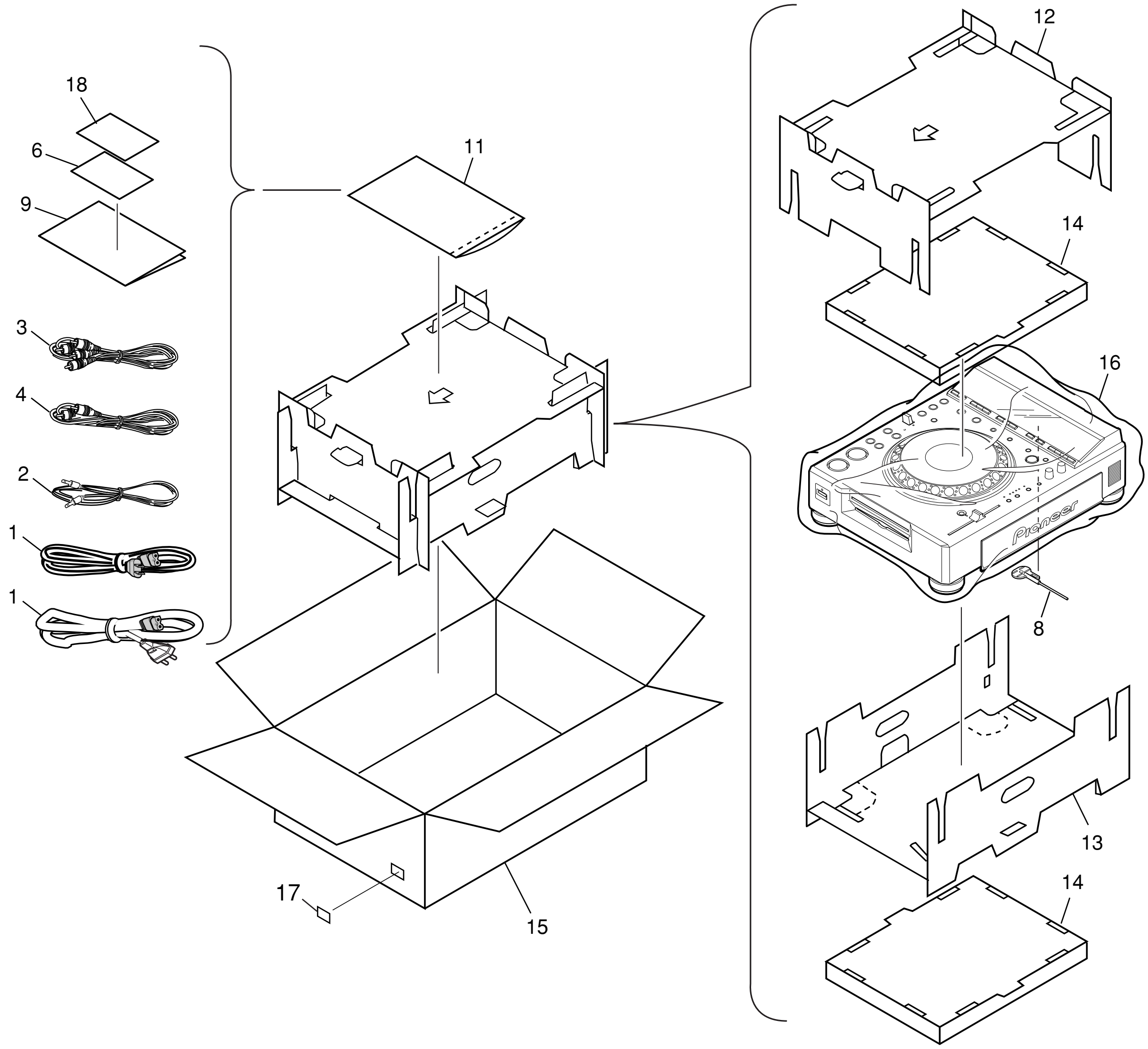
Forced Eject Pin
(housed in a groove in
the bottom panel)
(DEX1021)



2. EXPLODED VIEWS AND PARTS LIST

NOTES:
- *Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.*
- *The ⚠ mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.*
- *Screws adjacent to ▼ mark on product are used for disassembly.*
- *For the applying amount of lubricants or glue, follow the instructions in this manual. (In the case of no amount instructions, apply as you think it appropriate.)*

2.1 PACKING



PACKING parts List

Mark	No.	Description	Part No.
⚠	1	AC Power Cord	See Contrast table(2)
	2	Control Cord (L= 1m)	XDE3063
	3	Audio Cable (L= 1.5m)	VDE1064
	4	Video Cable (L=1.5m)	VDE1065
	5	• • • • •	
NSP	6	Warranty Card	See Contrast table(2)
	7	• • • • •	
	8	Forced Eject Pin	DEX1021
	9	Operating Instructions(English)	See Contrast table(2)
	10	• • • • •	

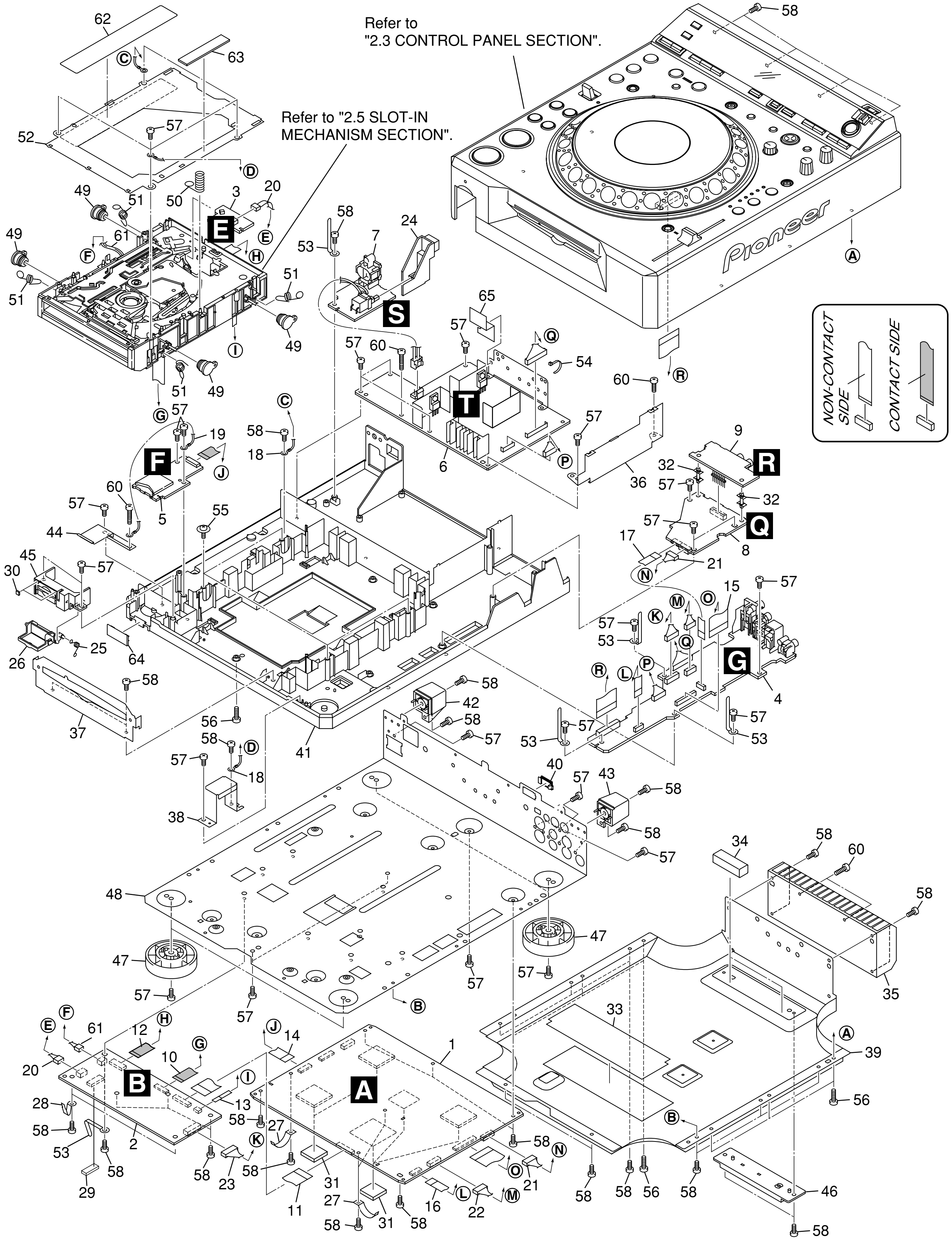
Mark	No.	Description	Part No.
NSP	11	Polyethylene Bag (230 x 340 x 0.06)	AHG7117
	12	Pad (A)	DHA1716
	13	Pad (B)	DHA1717
	14	Pad (C)	DHA1718
	15	Packing Case	See Contrast table(2)
	16	Mirror mat Sheet	AHG7010
	17	Label	See Contrast table(2)
NSP	18	User Card	DRM1262

(2) CONTRAST TABLE

DVJ-1000/KUCXJ, /WYXJ5, /TLXJ/RD and TLFXJ types are constructed the same except for the following:

Mark	No.	Symbol and Description	DVJ-1000 /KUCXJ	DVJ-1000 /WYXJ5	DVJ-1000 /TLXJ/RD	DVJ-1000 /TLFXJ
⚠	1	AC Power Cord	ADG7021	ADG1154	ADG1154	ADG1154
⚠	1	AC Power Cord	Not used	Not used	Not used	ADG7097
NSP	6	Warranty Card	ARY7043	Not used	Not used	Not used
	9	Operating Instructions (English)(KUCXJ)	DRB1413	Not used	Not used	Not used
	9	Operating Instructions (WYXJ5)(English, French, German, Italian, Dutch, Spanish)	Not used	DRB1414	Not used	Not used
	9	Operating Instructions (TLFXJ) (English, Chinese)	Not used	Not used	Not used	DRB1415
	9	Operating Instructions (TLXJ/RD) (English, Spanish)	Not used	Not used	DRB1416	Not used
	15	Packing Case	DHG2636	DHG2637	DHG2639	DHG2638
NSP	17	Label	DRW2311	VRW1629	VRW1629	VRW1629

2.2 EXTERIOR SECTION


Refer to
"2.3 CONTROL PANEL SECTION".
Refer to "2.5 SLOT-IN
MECHANISM SECTION".
NON-CONTACT
SIDE
CONTACT SIDE
Pioneer
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EXTERIOR SECTION parts List

Mark	No.	Description	Part No.
	1	MAIN Assy	DWG1630
	2	DVPL Assy	DWX2627
	3	SLMB Assy	DWS1395
	4	MJCB Assy	DWG1633
	5	SDCB Assy	DWX2630
⚠	6	POWER SUPPLY Assy	DWR1436
⚠	7	PSWB Assy	See Contrast table(2)
	8	AUDB Assy	DWG1638
	9	AJCB Assy	DWG1639
	10	26P FFC/30V	DDD1337
	11	FFC/30V (40p)	DDD1338
	12	FFC/60V (13p)	DDD1339
	13	FFC/60V (4p)	DDD1340
	14	FFC/60V (13p)	DDD1341
	15	FFC/60V (27p)	DDD1342
	16	FFC/60V (10p)	DDD1343
	17	FFC/60V (9p)	DDD1344
	18	Earth Lead Unit	DDF1032
	19	Earth Lead Unit	DDF1033
	20	Connector Assy	PF03PP-B35
	21	Connector Assy	DKP3778
	22	6P Connector Assy	PF06PP6D15
	23	8P Connector Assy	PF08PP-D25
	24	Power Knob	DNK4635
	25	Door Spring	DBH1565
	26	Card Door	DNK4651
	27	Earth Plate 1(CU)	DBK1300
	28	Earth Plate 2(CU)	DBK1301
NSP	29	Silicon Rubber D5 L	DEB1456
	30	Door Cushion	DEB1780
	31	Insulation Sheet	DEC2694
	32	PC Support	DEC2736
	33	Shield Cushion	DEC2905
	34	Gasket L40	DEC2979

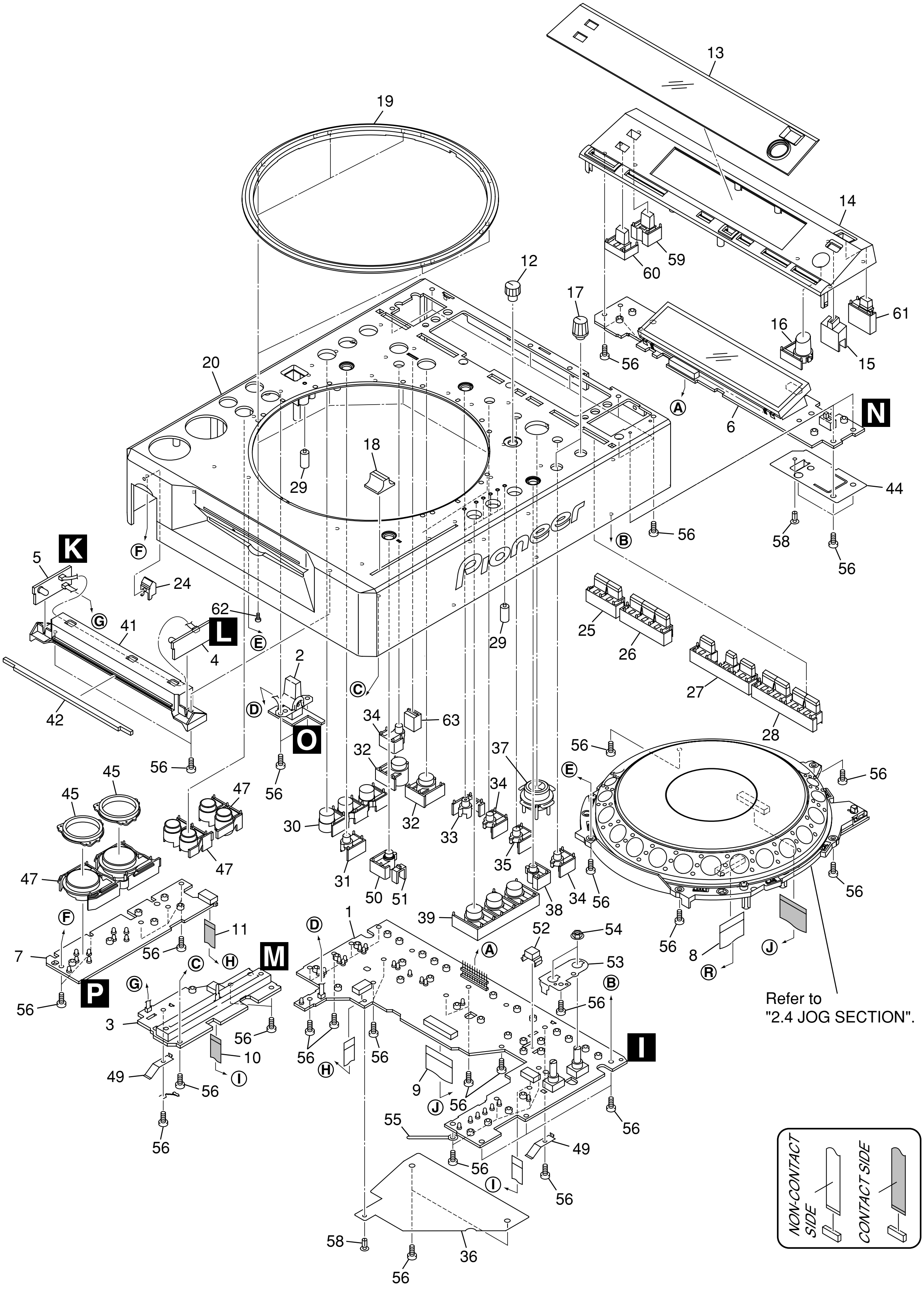
Mark	No.	Description	Part No.
	35	Heat Sink	DNG1098
NSP	36	Shield Plate 2	DNH2723
NSP	37	Front Plate	DNH2724
NSP	38	Earth Plate	DNH2726
	39	Heat Plate	See Contrast table(2)
	40	Blind Cap	DNK4218
NSP	41	Chassis	DNK4591
	42	Rear Protector (R)	DNK4636
	43	Rear Protector (L)	DNK4637
	44	Card Plate	DBK1296
	45	Door Holder	DNK4652
	46	Push Rod Holder	DNK4671
	47	Insulator Assembly	DXA1904
	48	Shield Plate 1 Assy	See Contrast table(2)
	49	Damper	CNV6011
	50	Earth Spring	DBH1398
	51	Float Spring G11	DBH1526
	52	Mecha Plate	DNH2339
	53	Cord Clamper (STEEL)	RNH-184
	54	Binder	ZCA-SKB90BK
	55	DM Screw (FTC)	DBA1260
	56	Screw	BPZ30P120FTB
	57	Screw	BPZ30P080FTB
	58	Screw	BBZ30P080FTC
	59	Screw	BPZ30P080FZK
	60	Screw	BBZ30P140FTC
	61	2P Connector	PF02PY-B37
	62	Laser Caution (7L)	See Contrast table(2)
	63	Guard Cushion	DEC2984
	64	Door Plate	DNK4650
	65	Shield Sheet Assy	DXB1941

(2) CONTRAST TABLE

DVJ-1000/KUCXJ, /WYXJ5, /TLXJ/RD and TLFXJ types are constructed the same except for the following:

Mark	No.	Symbol and Description	DVJ-1000 /KUCXJ	DVJ-1000 /WYXJ5	DVJ-1000 /TLXJ/RD	DVJ-1000 /TLFXJ
⚠	7	PSWB ASSY	DWS1392	DWS1396	DWS1396	DWS1396
	39	Heat Plate	DNH2744	DNH2749	DNH2750	DNH2750
	48	Shield Plate 1 Assy	DXB1883	DXB1892	DXB1894	DXB1893
	62	Laser Caution	DRW2317	DRW2317	DRW2317	VRW2159

2.3 CONTROL PANEL SECTION


13
19
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60
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16
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56
20
6
N
18
29
44
56
58
56
5
K
F
24
B
62
L
41
E
4
29
25
26
2
C
27
28
42
D
34
63
O
56
32
37
56
45
56
34
56
E
45
47
56
30
32
33
47
35
47
31
50
51
38
34
56
39
F
1
52
54
11
D
A
7
56
H
M
53
8
56
C
G
B
56
R
J
P
56
3
56
I
10
56
49
I
H
56
9
J
56
56
56
55
49
56
56
I
58
36
56
NON-CONTACT SIDE
CONTACT SIDE

Refer to "2.4 JOG SECTION".

CONTROL PANEL SECTION parts List

Mark	No.	Description	Part No.
	1	DIPB Assy	See Contrast table(2)
	2	RSWB Assy	DWS1391
	3	SLDB Assy	DWS1393
	4	LED 1 Assy	DWG1634
	5	LED 2 Assy	DWG1635
	6	DFLB Assy	DWG1632
	7	KSWB Assy	DWS1394
	8	22P FFC/60V	DDD1345
	9	25P FFC/60V	DDD1346
	10	10P FFC/60V	DDD1347
	11	9P FFC/60V	DDD1348
	12	Adjust Knob	DAC2350
	13	Display Panel(PMMA)	DNK4601
	14	Top Panel	DNK4599
	15	Slide SW Knob	DNK4592
	16	Eject Knob	DAC2335
	17	Rotary Knob	DAA1182
	18	Slide Knob	DAC2376
	19	Jog Ring	DNK4600
	20	Control Panel	DNK4598
	21	• • • •	
	22	• • • •	
	23	• • • •	
	24	Card Lens	DNK4596
	25	SETKnob 1	DAC2331
	26	SETKnob 2	DAC2332
	27	SETKnob 3	DAC2333
	28	SETKnob 4	DAC2334
	29	TEMPO Lens	DNK4593
	30	SET Knob (H. C.)	DAC2321
	31	REC Knob	DAC2322
	32	SET Knob (LOOP)	DAC2324

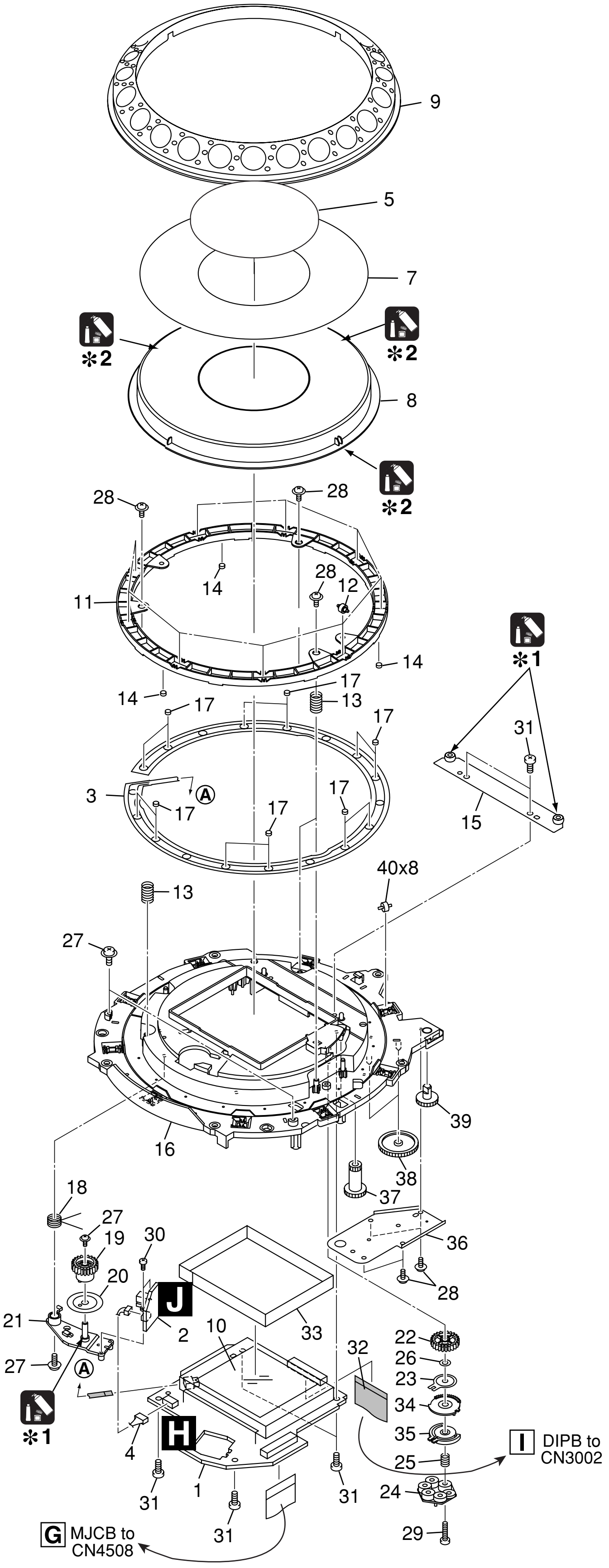
Mark	No.	Description	Part No.
	33	RELOOP Knob	DAC2325
	34	ENTER Knob	DAC2326
	35	CALL Knob	DAC2323
	36	Shield Sheet 1	DEC2899
	37	Cross Key	DAC2327
	38	Change Knob	DAC2328
	39	SET Knob (TEMPO)	DAC2329
	40	• • • •	
	41	Reflector	DNK4597
	42	Front Lens	DNK4595
	43	• • • •	
	44	Shield Sheet 2	DEC2909
	45	Ring Lens	DNK3880
	46	• • • •	
	47	SET Knob (PLAY) Assy	DXB1912
	48	• • •	
	49	Earth Plate (CU)	DBK1224
	50	RESET Knob	DAC2330
	51	TEMPO RESET Lens	DNK4708
	52	Cross Key Holder	DNK4798
	53	VR Stay	DNF1663
	54	Flange Nut M9	DBN1008
	55	Cord Clamper (steel)	RNH-184
	56	Screw	BPZ30P080FTB
	57	• • • •	
	58	Rivet (PLASTIC)	RBM-003
	59	TIMX Knob	DAC2336
	60	TEXT Knob	DAC2338
	61	STOP Knob	DAC2354
	62	Screw	OBA8038
	63	LOOP MODE Lens	DNK4594

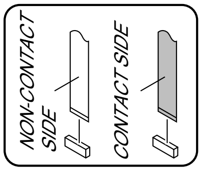
(2) CONTRAST TABLE

DVJ-1000/KUCXJ, /WYXJ5, /TLXJ/RD and TLFXJ types are constructed the same except for the following:

Mark	No.	Symbol and Description	DVJ-1000 /KUCXJ	DVJ-1000 /WYXJ5	DVJ-1000 /TLXJ/RD	DVJ-1000 /TLFXJ
	1	DIPB ASSY	DWG1631	DWG1640	DWG1642	DWG1641

2.4 JOG SECTION


9
5
7
8
✻2
✻2
✻2
28
28
28
12
11
14
14
14
17
17
17
17
17
17
13
13
3
Ⓐ
✻1
31
15
40x8
27
16
39
38
37
36
28
18
27
19
30
20
J
21
2
10
33
32
22
26
23
34
35
25
24
29
27
Ⓐ
✻1
4
H
1
31
31
31
I DIPB to CN3002
G MJCB to CN4508


NON-CONTACT SIDE
CONTACT SIDE

✻**1.** Lubricating oil
GYA1001 (ZLB-PN397B)

✻**2.** Lubricants : (ZLB-HPD1600)

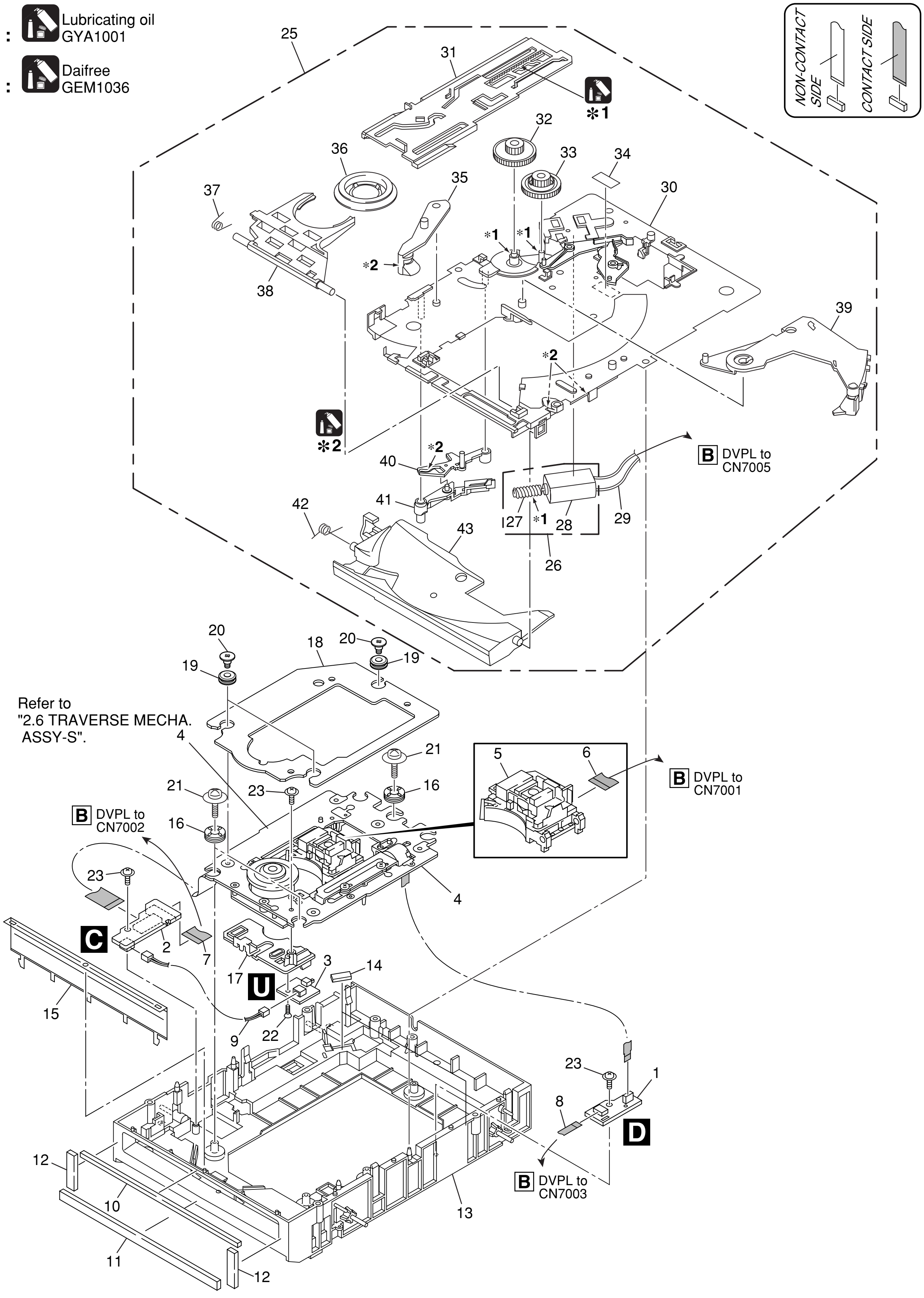
JOG SECTION parts List

Mark	No.	Description	Part No.
	1	JFLB Assy	DWG1636
	2	JOGB Assy	DWG1637
	3	Sheet SW	DSX1065
	4	Connector Assy	PF04PP-B07
	5	Jog Panel (PMMA)	DAH2182
	6	• • • •	
	7	JOG Plate	DAH2438
	8	JOG A	DNK4586
	9	JOG B	DNK4557
	10	Protector Sheet	DEC2945
	11	SW Ring	DNK4070
	12	Roller A Assy	DXB1825
	13	SW Spring 25	DBH1514
	14	Ring Cushion L24/2.0	DEC2958
	15	JOG Stay Assy	DXB1876
	16	JOG Holder 1000	DNK4558
	17	SW Cushion HH48/2	DEC2538
	18	Arm Spring	DBH1566
	19	Gear	DNK4560
	20	ENCORDER Plate	DEC2889
	21	Gear Arm	DNK4559
	22	Load Gear A	DNK4562
	23	Smoother	DNK4561
	24	Adjust Plate	DNK4178
	25	Gear Spring 200	DBH1525
	26	Washer	WA52D120D025
	27	Screw	IPZ30P100FTC
	28	Screw (FE)	DBA1265
	29	Screw	BPZ20P100FTC
	30	Screw	BPZ20P060FTC
	31	Screw	BPZ30P080FTB
	32	25P FFC / 60V	DDD1346
	33	FL Sheet	DEC2946
	34	CAM Plate	DNK4567
	35	PRESS Plate	DNK4566
	36	GEAR Plate	DNH2713
	37	Link Gear 3	DNK4565
	38	Link Gear 2	DNK4564
	39	Link Gear 1	DNK4563
	40	Roller B Assy	DXB1877

2.5 SLOT-IN MECHANISM SECTION

∗1 : Lubricating oil GYA1001

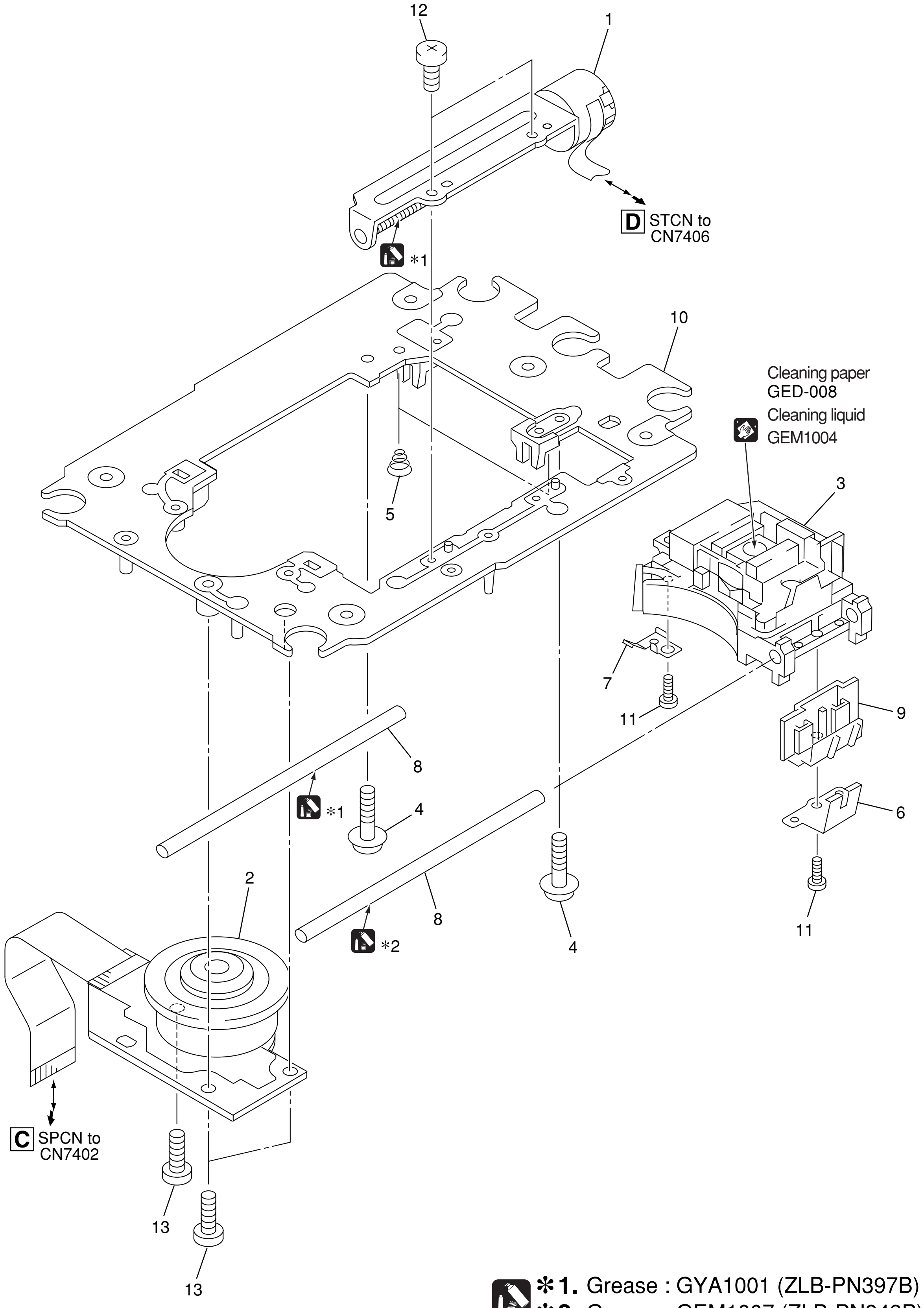
∗2 : Daifree GEM1036


NON-CONTACT SIDE
CONTACT SIDE
25
31
∗1
32
33
34
36
35
37
30
∗1
∗1
∗2
38
39
∗2
∗2
∗2
40
B DVPL to CN7005
41
42
27
∗1
28
29
43
26
20
18
20
19
19
Refer to "2.6 TRAVERSE MECHA. ASSY-S".
4
21
5
6
B DVPL to CN7001
23
16
21
B DVPL to CN7002
16
23
4
C
2
7
3
14
17
U
15
9
22
23
1
8
D
12
B DVPL to CN7003
10
13
11
12

SLOT-IN MECHANISM SECTION parts List

Mark	No.	Description	Part No.
	1	STCN Assy	DWX2628
	2	SPCN Assy	DWX2629
	3	INSW Assy	DWS1398
	4	Traverse Mechanism G11 Assy-S	DXX2566
NSP	5	Pickup Assy	OWY8071
	6	26P Flexible Cable/30V	DDD1337
	7	13P Flexible Cable/60V	DDD1339
	8	4P Flexible Cable/60V	DDD1340
	9	Connector Assy	DKP3769
	10	Vessel Cushion A	DEC2852
	11	Vessel Cushion B	DEC2853
	12	Vessel Cushion C	DEC2854
	13	Float Base (G11) Assy	DXB1793
	14	Spacer POR (T3)	DEB1566
	15	Front Sheet	DED1132
	16	Float Rubber D3	DEB1404
	17	Inside SW Base	DNK4236
	18	Weight G11	DNH2775
NSP	19	DRA Rubber G5	DEB1454
	20	DRA Screw R (G7)	DBA1264
	21	Float Fastener	DBA1286
	22	Screw	VBA1062
	23	Screw	IPZ20P060FTC
	24	• • • • •	
NSP	25	Slot-in Mechanism G11 assy	DXA2085
	26	Loading Motor Assy-S	DEA1008
NSP	27	Worm Gear	DNK3910
NSP	28	DC Motor	DXM1229
	29	Connector Assy	PF02PY-B37
	30	Loading Base Assy-S	DEA1022
	31	Main Cam	DNK3407
	32	Driver Gear	DNK3912
	33	Loading Gear	DNK3911
	34	SW Lever Spacer	DEC2420
	35	Loading Lever Assy	DXB1880
	36	Clamper D4 Assy	DXA2043
	37	Clamper Spring	DBH1374
	38	Clamper Arm	DNK3576
	39	Eject Lever	DNK3684
	40	Lever AP	DNK3835
	41	Lever BP	DNK3836
	42	Guide Spring	DBH1375
	43	Disc Guide	DNK3478

2.6 TRAVERSE MECHA ASSY-S SECTION


12
1
A
D STCN to
CN7406
*1
10
B
Cleaning paper
GED-008
Cleaning liquid
GEM1004
3
5
C
7
11
9
8
*1
4
6
D
2
8
*2
4
11
C SPCN to
CN7402
E
13
13

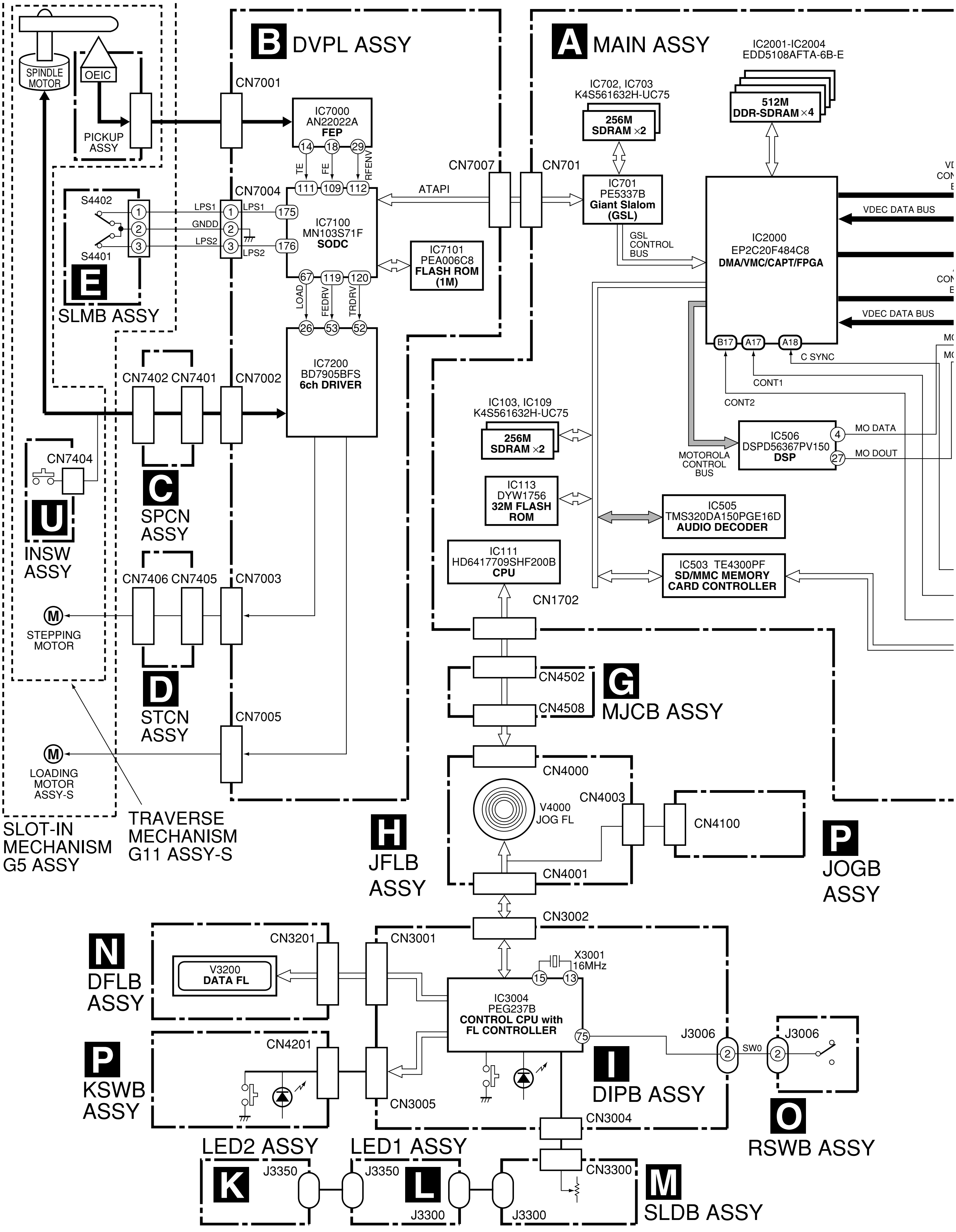
*1. Grease : GYA1001 (ZLB-PN397B)
*2. Grease : GEM1007 (ZLB-PN948P)

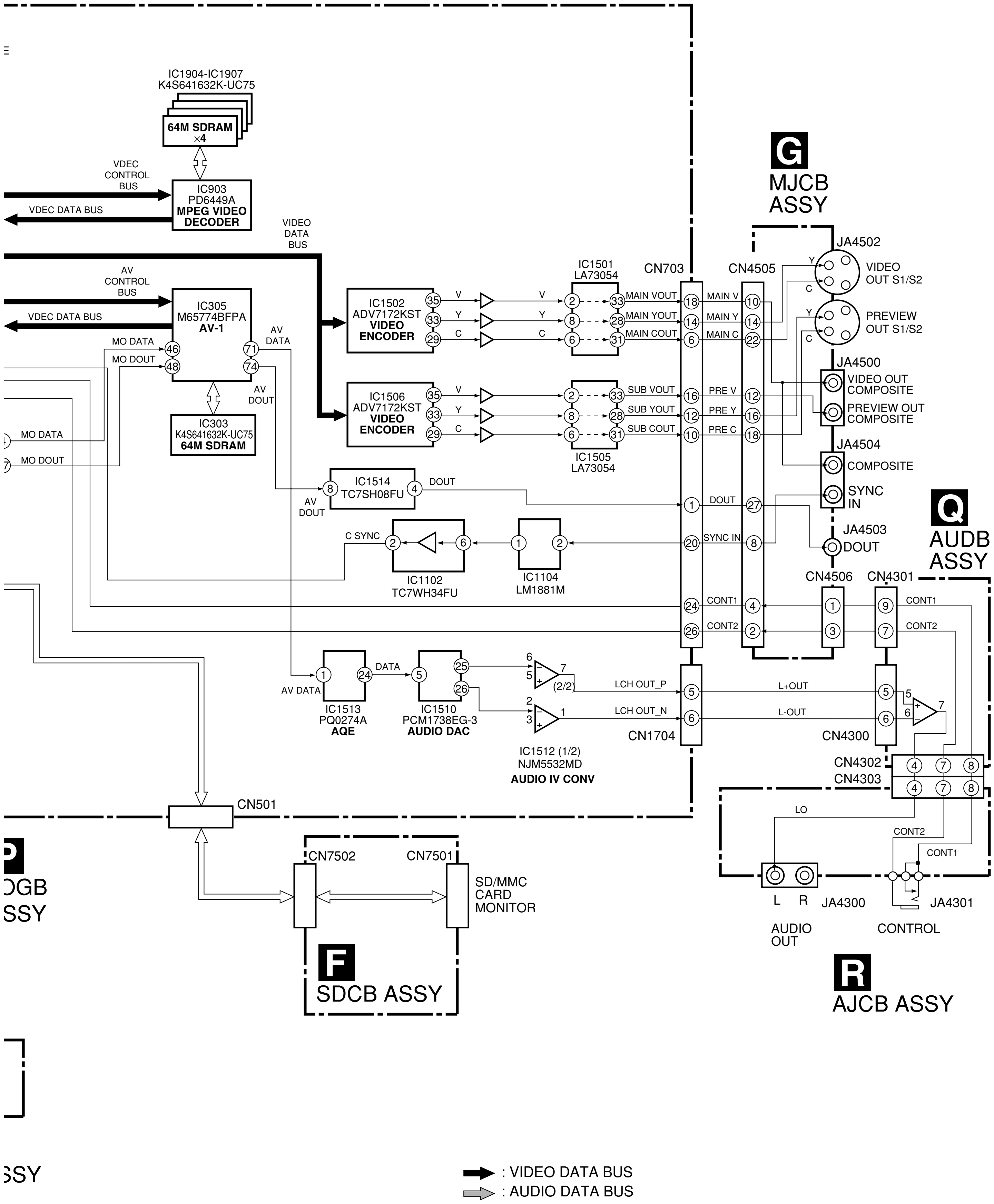
TRAVERSE MECHA ASSY-S SECTION parts List

Mark	No.	Description	Part No.
NSP	1	Spindle Motor	DXM1227
NSP	2	Stepping Motor	DXM1231
NSP	3	Pickup Assy-S	OXX8071
NSP	4	Skew Screw	DBA1263
NSP	5	Skew Spring	DBH1437
NSP	6	Joint Spring	DBK1261
NSP	7	Leaf Spring G11	DBK1262
NSP	8	Guide Bar (S)	DLA1918
	9	Joint	DNK3858
NSP	10	Mecha.Chassis G11	DNK4307
	11	Tapping Screw 04	VBA1092
	12	Screw	BPZ20P080FTC
	13	Screw	BPZ26P080FTC

3. BLOCK DIAGRAM AND SCHEMATIC DIAGRAM

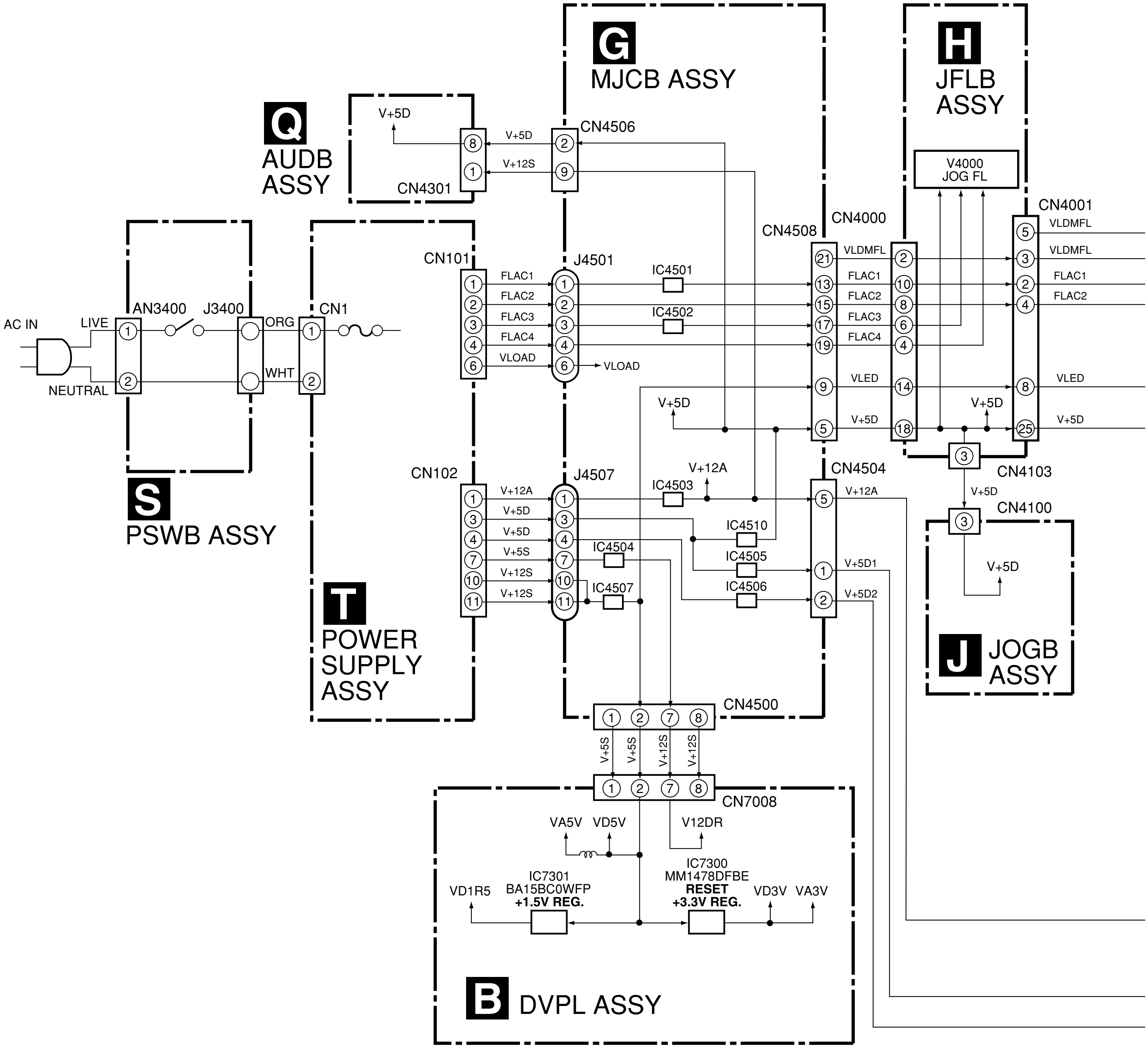
3.1 BLOCK DIAGRAM (1/2)

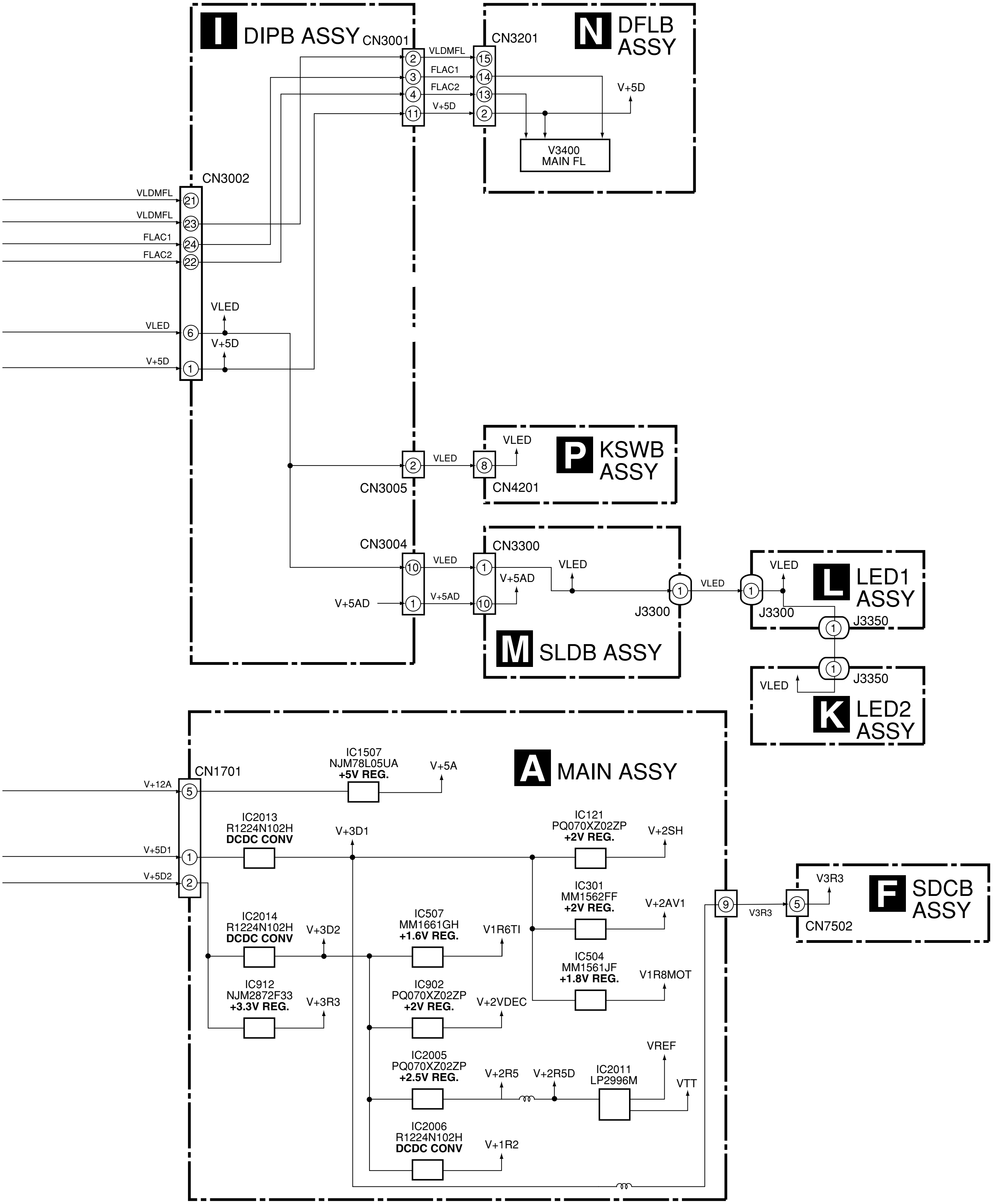

SPINDLE MOTOR
OEIC
PICKUP ASSY
S4402
S4401
E
SLMB ASSY
LPS1
GNDD
LPS2
B DVPL ASSY
CN7001
IC7000
AN22022A
FEP
TE
FE
RFENV
CN7004
IC7100
MN103S71F
SODC
IC7101
PEA006C8
FLASH ROM
(1M)
LOAD
FEDRV
TRDRV
IC7200
BD7905BFS
6ch DRIVER
CN7007
ATAPI
CN7402 CN7401
CN7002
CN7404
U
INSW ASSY
C
SPCN ASSY
CN7406 CN7405
CN7003
STEPPING MOTOR
D
STCN ASSY
CN7005
LOADING MOTOR ASSY-S
SLOT-IN MECHANISM G5 ASSY
TRAVERSE MECHANISM G11 ASSY-S
A MAIN ASSY
IC702, IC703
K4S561632H-UC75
256M SDRAM ×2
IC2001-IC2004
EDD5108AFTA-6B-E
512M DDR-SDRAM ×4
CN701
IC701
PE5337B
Giant Slalom
(GSL)
GSL CONTROL BUS
IC2000
EP2C20F484C8
DMA/VMC/CAPT/FPGA
VDEC DATA BUS
VDEC DATA BUS
C SYNC
CONT1
CONT2
IC103, IC109
K4S561632H-UC75
256M SDRAM ×2
IC506
DSPD56367PV150
DSP
MO DATA
MO DOUT
MOTOROLA CONTROL BUS
IC113
DYW1756
32M FLASH ROM
IC505
TMS320DA150PGE16D
AUDIO DECODER
IC111
HD6417709SHF200B
CPU
IC503 TE4300PF
SD/MMC MEMORY CARD CONTROLLER
CN1702
CN4502
CN4508
G
MJCB ASSY
CN4000
V4000
JOG FL
CN4003
CN4100
CN4001
H
JFLB ASSY
P
JOGB ASSY
CN3002
N
DFLB ASSY
CN3201
V3200
DATA FL
CN3001
X3001
16MHz
IC3004
PEG237B
CONTROL CPU with FL CONTROLLER
CN4201
P
KSWB ASSY
CN3005
J3006
SW0
J3006
I
DIPB ASSY
O
RSWB ASSY
CN3004
LED2 ASSY
LED1 ASSY
K
J3350
J3350
L
J3300
J3300
CN3300
M
SLDB ASSY


E
IC1904-IC1907
K4S641632K-UC75
64M SDRAM
×4
VDEC
CONTROL
BUS
IC903
PD6449A
MPEG VIDEO
DECODER
VDEC DATA BUS
VIDEO
DATA
BUS
AV
CONTROL
BUS
IC305
M65774BFPA
AV-1
VDEC DATA BUS
MO DATA
MO DOUT
46
48
71
74
AV
DATA
AV
DOUT
IC303
K4S641632K-UC75
64M SDRAM
MO DATA
MO DOUT
IC1502
ADV7172KST
VIDEO
ENCODER
35
33
29
V
Y
C
IC1506
ADV7172KST
VIDEO
ENCODER
IC1501
LA73054
IC1505
LA73054
2
8
6
33
28
31
MAIN VOUT
MAIN YOUT
MAIN COUT
SUB VOUT
SUB YOUT
SUB COUT
CN703
18
14
6
16
12
10
1
20
24
26
CN4505
MAIN V
MAIN Y
MAIN C
PRE V
PRE Y
PRE C
10
14
22
12
16
18
27
8
4
2
G
MJCB
ASSY
JA4502
VIDEO
OUT S1/S2
PREVIEW
OUT S1/S2
JA4500
VIDEO OUT
COMPOSITE
PREVIEW OUT
COMPOSITE
JA4504
COMPOSITE
SYNC
IN
JA4503
DOUT
Q
AUDB
ASSY
IC1514
TC7SH08FU
8
4
DOUT
AV
DOUT
DOUT
C SYNC
IC1102
TC7WH34FU
IC1104
LM1881M
SYNC IN
CN4506
CN4301
CONT1
CONT2
1
3
9
7
IC1513
PQ0274A
AQE
AV DATA
DATA
IC1510
PCM1738EG-3
AUDIO DAC
25
26
IC1512 (1/2)
NJM5532MD
AUDIO IV CONV
(2/2)
LCH OUT_P
LCH OUT_N
CN1704
5
6
L+OUT
L-OUT
CN4300
CN4302
CN4303
4
7
8
LO
CONT2
CONT1
L
R
JA4300
JA4301
AUDIO
OUT
CONTROL
R
AJCB ASSY
CN501
CN7502
CN7501
SD/MMC
CARD
MONITOR
F
SDCB ASSY
P
OGB
SSY
SSY
: VIDEO DATA BUS
: AUDIO DATA BUS

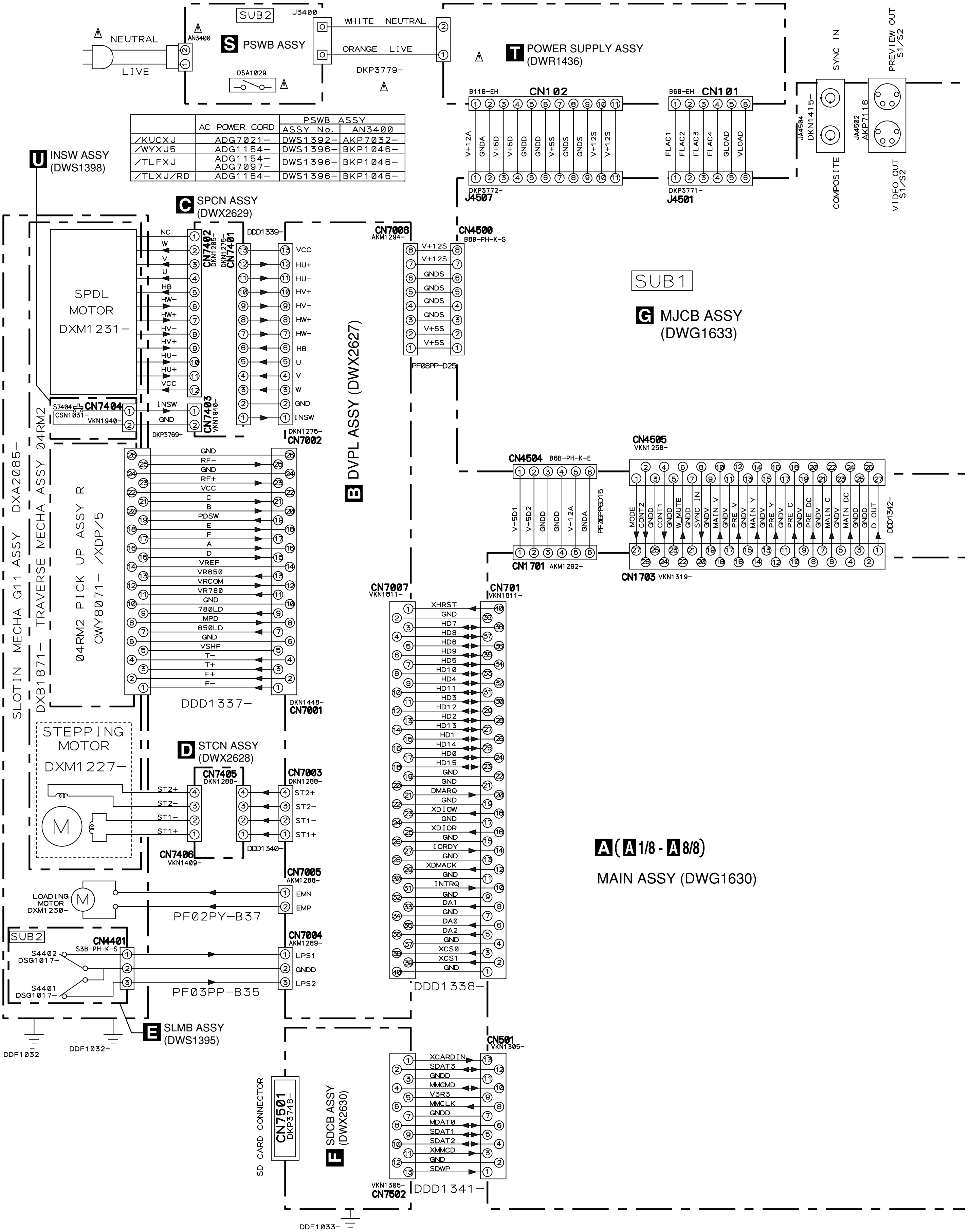
3.2 BLOCK DIAGRAM (2/2)

• POWER


G
MJCB ASSY
H
JFLB ASSY
V4000
JOG FL
Q
AUDB ASSY
V+5D
CN4301
V+5D
V+12S
CN4506
CN4000
CN4001
CN4508
VLDMFL
FLAC1
FLAC2
FLAC3
FLAC4
VLED
V+5D
CN101
J4501
IC4501
IC4502
VLOAD
AC IN
LIVE
NEUTRAL
AN3400
J3400
ORG
WHT
CN1
S
PSWB ASSY
T
POWER SUPPLY ASSY
CN102
J4507
V+12A
V+5D
V+5S
V+12S
IC4503
IC4504
IC4507
IC4510
IC4505
IC4506
CN4504
V+5D1
V+5D2
CN4103
CN4100
J
JOGB ASSY
CN4500
CN7008
VA5V
VD5V
V12DR
IC7301
BA15BC0WFP
+1.5V REG.
IC7300
MM1478DFBE
RESET
+3.3V REG.
VD1R5
VD3V
VA3V
B
DVPL ASSY

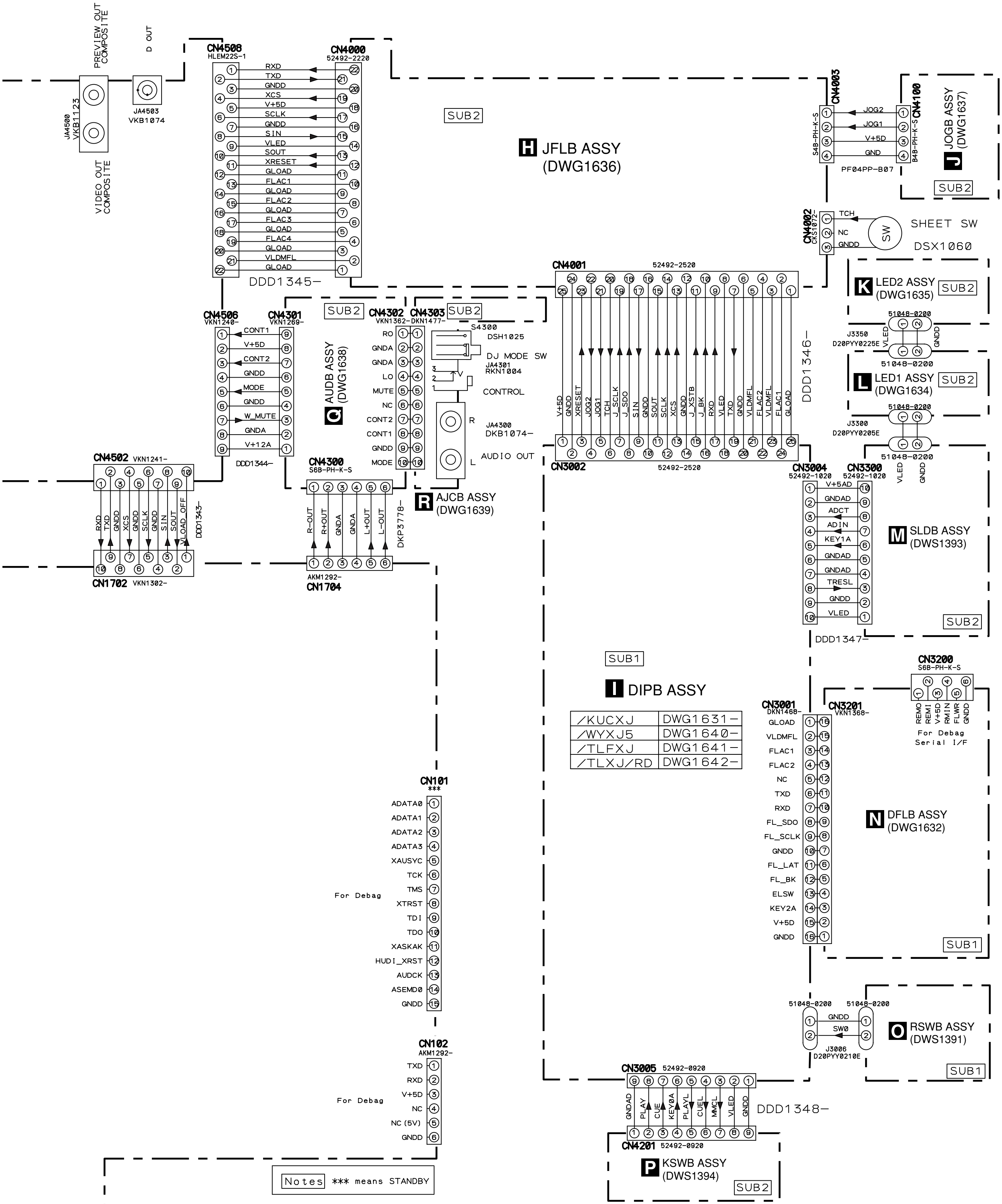

I DIPB ASSY
CN3001
VLDMFL
FLAC1
FLAC2
V+5D
2
3
4
11
CN3201
15
14
13
2
N DFLB ASSY
V+5D
V3400
MAIN FL
CN3002
VLDMFL
VLDMFL
FLAC1
FLAC2
21
23
24
22
VLED
VLED
6
V+5D
V+5D
1
P KSWB ASSY
VLED
CN3005
2
VLED
8
CN4201
CN3004
10
VLED
V+5AD
1
V+5AD
CN3300
1
V+5AD
10
VLED
J3300
1
VLED
M SLDB ASSY
L LED1 ASSY
VLED
1
J3300
J3350
1
K LED2 ASSY
1
J3350
VLED
A MAIN ASSY
CN1701
V+12A
5
V+5D1
1
V+5D2
2
IC1507
NJM78L05UA
+5V REG.
V+5A
IC2013
R1224N102H
DCDC CONV
V+3D1
IC121
PQ070XZ02ZP
+2V REG.
V+2SH
IC301
MM1562FF
+2V REG.
V+2AV1
IC2014
R1224N102H
DCDC CONV
V+3D2
IC507
MM1661GH
+1.6V REG.
V1R6TI
IC504
MM1561JF
+1.8V REG.
V1R8MOT
IC912
NJM2872F33
+3.3V REG.
V+3R3
IC902
PQ070XZ02ZP
+2V REG.
V+2VDEC
IC2005
PQ070XZ02ZP
+2.5V REG.
V+2R5
V+2R5D
IC2011
LP2996M
VREF
VTT
IC2006
R1224N102H
DCDC CONV
V+1R2
9
V3R3
5
V3R3
CN7502
F SDCB ASSY

3.3 OVERALL WIRING DIAGRAM


A
B
C
D
E
F
NEUTRAL
LIVE
AN3400
SUB2
S PSWB ASSY
J3400
DSA1029
WHITE NEUTRAL
ORANGE LIVE
DKP3779-
T POWER SUPPLY ASSY (DWR1436)
B11B-EH CN102
B6B-EH CN101
V+12A GNDA V+5D V+5D GNDD GNDD V+5S GNDS GNDS V+12S V+12S
FLAC1 FLAC2 FLAC3 FLAC4 GLOAD VLOAD
DKP3772- J4507
DKP3771- J4501
J4504 DKN1415-
J4502 AKP7116
SYNC IN
COMPOSITE
PREVIEW OUT S1/S2
VIDEO OUT S1/S2
U INSW ASSY (DWS1398)
C SPCN ASSY (DWX2629)
SPDL MOTOR DXM1231-
CN7402 DKN1205-
CN7401 DKN1275-
DDD1339-
DKN1275- CN7002
CN7403 VKN1940-
S7404 CSN1031- CN7404 VKN1940-
DKP3769-
NC W V U HB HW- HW+ HV- HV+ HU- HU+ VCC
INSW GND
SLOTIN MECHA G11 ASSY DXA2085-
DXB1871- TRAVERSE MECHA ASSY 04RM2
04RM2 PICK UP ASSY R OWY8071- /XDP/5
DDD1337-
DKN1448- CN7001
GND RF- GND RF+ VCC C B PDSW E F A D VREF VR650 VRCOM VR780 GND 780LD MPD 650LD GND VSHF T- T+ F+ F-
B DVPL ASSY (DWX2627)
CN7008 AKM1294-
CN4500 B8B-PH-K-S
V+12S V+12S GNDS GNDS GNDS GNDS V+5S V+5S
PF08PP-D25
SUB1
G MJCB ASSY (DWG1633)
STEPPING MOTOR DXM1227-
D STCN ASSY (DWX2628)
CN7405 DKN1288-
CN7406 VKN1409-
DDD1340-
CN7003 DKN1288-
ST2+ ST2- ST1- ST1+
LOADING MOTOR DXM1230-
CN7005 AKM1288-
EMN EMP
PF02PY-B37
SUB2
CN4401 S3B-PH-K-S
S4402 DSG1017-
S4401 DSG1017-
CN7004 AKM1289-
LPS1 GNDD LPS2
PF03PP-B35
E SLMB ASSY (DWS1395)
DDF1032
DDF1032-
CN4504 B6B-PH-K-E
V+5D1 V+5D2 GNDD GNDD V+12A GNDA
PF06PP6D15
CN1701 AKM1292-
CN4505 VKN1258-
MODE CONT2 GNDD CONT1 GNDD W_MUTE GNDD SYNC IN GNDV MAIN V GNDV PRE V GNDV MAIN Y GNDV PRE Y GNDV PRE C GNDV PRE DC GNDV MAIN C GNDV MAIN DC GNDD GNDD D OUT
DDD1342-
CN1703 VKN1319-
CN7007 VKN1811-
CN701 VKN1811-
XHRST GND HD7 HD8 HD6 HD9 HD5 HD10 HD4 HD11 HD3 HD12 HD2 HD13 HD1 HD14 HD0 HD15 GND GND DMARQ GND XDIOW GND XDIOR GND IORDY GND XDMACK GND INTRQ GND DA1 GND DA0 DA2 GND XCS0 XCS1 GND
DDD1338-
A (A 1/8 - A 8/8)
MAIN ASSY (DWG1630)
CN501 VKN1305-
XCARDIN SDAT3 GNDD MMCMD V3R3 MMCLK GNDD MDAT0 SDAT1 SDAT2 XMMCD GND SDWP
SD CARD CONNECTOR
CN7501 DKP3748-
F SDCB ASSY (DWX2630)
VKN1305- CN7502
DDD1341-
DDF1033-

	AC POWER CORD	PSWB ASSY	
		ASSY No.	AN3400
/KUCXJ	ADG7021-	DWS1392-	AKP7032-
/WYXJ5	ADG1154-	DWS1396-	BKP1046-
/TLFXJ	ADG1154- ADG7097-	DWS1396-	BKP1046-
/TLXJ/RD	ADG1154-	DWS1396-	BKP1046-

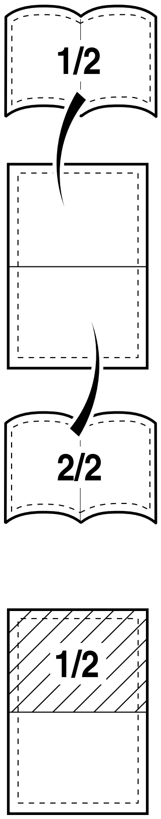
The ⚠ mark found on some cmponent parts should be replaced with same parts (safety regulation authorized) of identical designation.

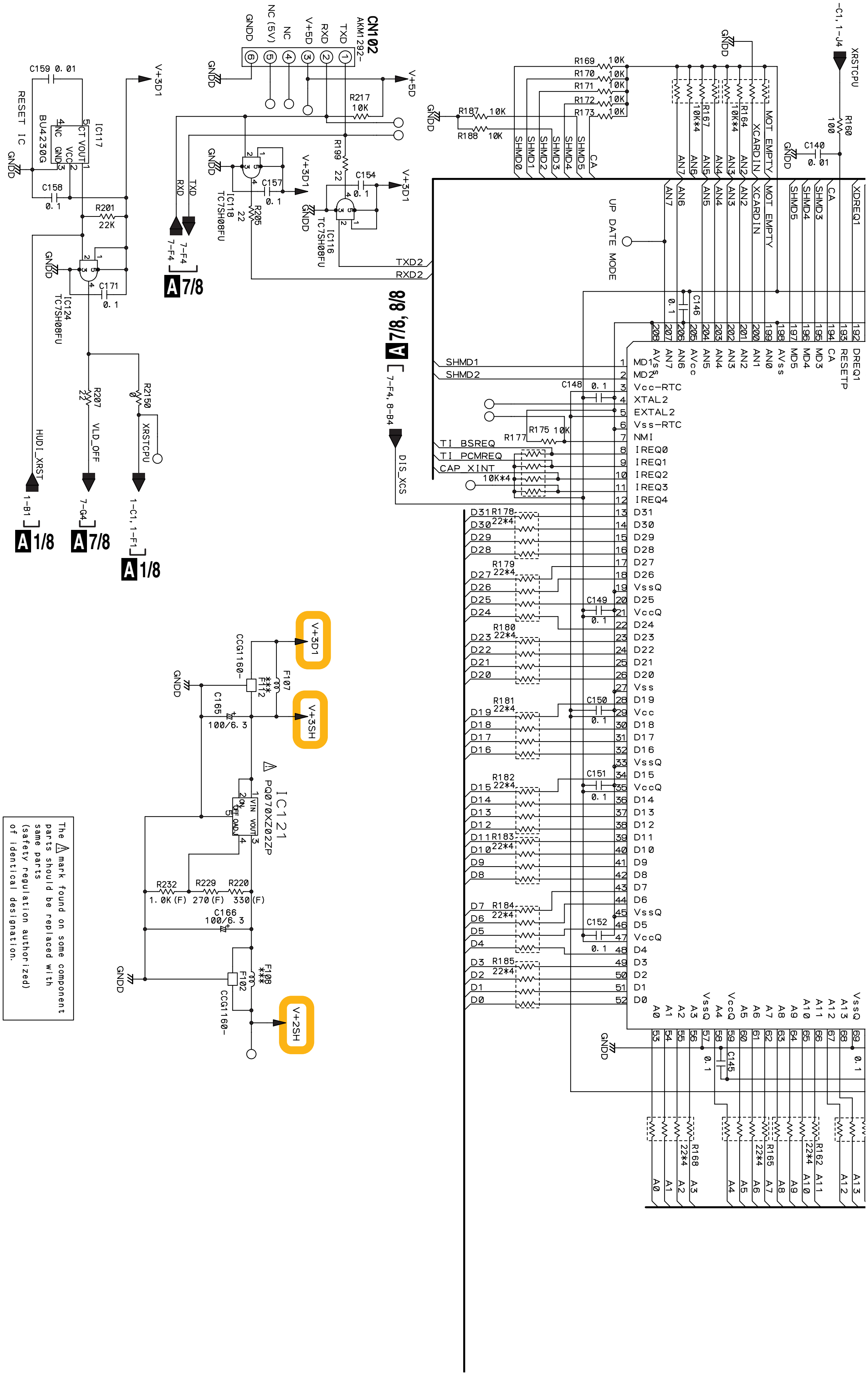

PREVIEW OUT COMPOSITE
D OUT
VIDEO OUT COMPOSITE
JA4500 VKB1123
JA4503 VKB1074
CN4508 HLEM22S-1
CN4000 52492-2220
RXD
TXD
GNDD
XCS
V+5D
SCLK
SIN
VLED
SOUT
XRESET
GLOAD
FLAC1
FLAC2
FLAC3
FLAC4
VLDMFL
DDD1345-
SUB2
H JFLB ASSY (DWG1636)
CN4003
CN4100
S4B-PH-K-S
B4B-PH-K-S
JOG2
JOG1
GND
PF04PP-B07
J JOGB ASSY (DWG1637)
CN4002 CKS1072-
TCH
NC
SW
SHEET SW
DSX1060
CN4001 52492-2520
K LED2 ASSY (DWG1635)
51048-0200
J3350 D20PYY0225E
L LED1 ASSY (DWG1634)
J3300 D20PYY0205E
CN4506 VKN1240-
CN4301 VKN1269-
CONT1
CONT2
MODE
W_MUTE
GNDA
V+12A
DDD1344-
CN4302 VKN1362-
CN4303 DKN1477-
S4300 DSH1025
DJ MODE SW
JA4301 RKN1004
CONTROL
JA4300 DKB1074-
AUDIO OUT
R
L
RO
LO
MUTE
Q AUDB ASSY (DWG1638)
CN4502 VKN1241-
VLOAD_OFF
DDD1343-
CN4300 S6B-PH-K-S
R-OUT
R+OUT
L+OUT
L-OUT
DKP3778-
R AJCB ASSY (DWG1639)
CN1702 VKN1302-
AKM1292-
CN1704
V+5D GNDD XRESET JOG2 JOG1 TCH J_SCLK J_SDO SIN GNDD SOUT SCLK XCS GNDD J_XSTB J_BK RXD VLED TXD GNDD VLDMFL FLAC2 VLDMFL FLAC1 GLOAD
DDD1346-
CN3002 52492-2520
CN3004 52492-1020
CN3300 52492-1020
V+5AD
GNDAD
ADCT
ADIN
KEY1A
TRESL
M SLDB ASSY (DWS1393)
DDD1347-
SUB1
I DIPB ASSY
/KUCXJ DWG1631-
/WYXJ5 DWG1640-
/TLFXJ DWG1641-
/TLXJ/RD DWG1642-
CN3200 S6B-PH-K-S
CN3201 VKN1366-
REMO REMI V+5D RMIN FLWR GNDD
For Debag Serial I/F
CN3001 DKN1468-
FL_SDO
FL_SCLK
FL_LAT
FL_BK
ELSW
KEY2A
N DFLB ASSY (DWG1632)
CN101 ***
ADATA0 ADATA1 ADATA2 ADATA3 XAUSYC TCK TMS XTRST TDI TDO XASKAK HUDI_XRST AUDCK ASEMD0 GNDD
For Debag
CN102 AKM1292-
TXD RXD V+5D NC NC (5V) GNDD
J3006 D20PYY0210E
GNDD SW0
O RSWB ASSY (DWS1391)
CN3005 52492-0920
GNDAD PLAY CUE KEY0A PLAYL CUEL MACL VLED GNDD
DDD1348-
CN4201 52492-0920
P KSWB ASSY (DWS1394)
Notes *** means STANDBY

- When ordering service parts, be sure to refer to "EXPLODED VIEWS and PARTS LIST" or "PCB PARTS LIST".
- The ⚠ mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.
- ▭ : The power supply is shown with the marked box.

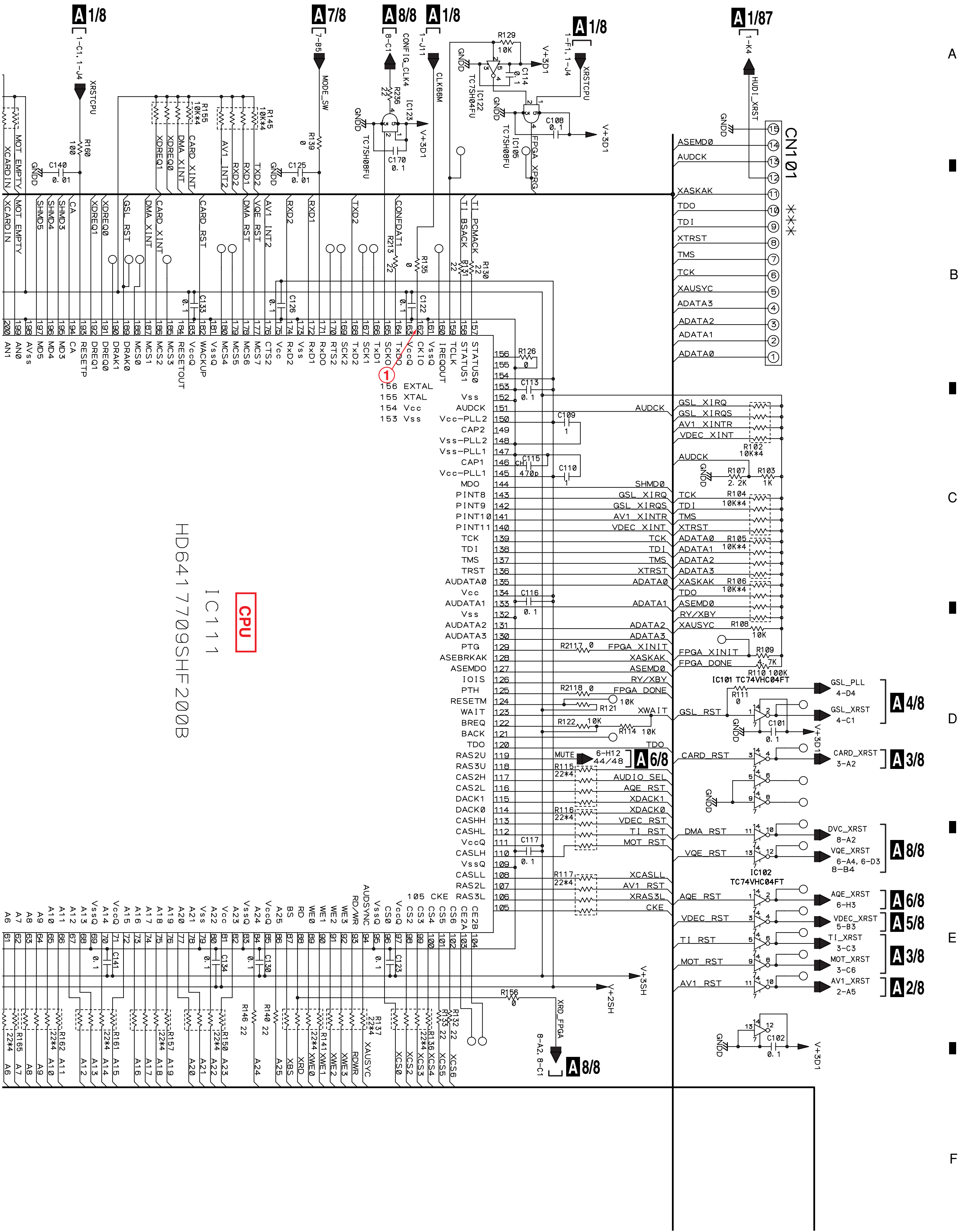
A 1/8 MAIN ASSY (DWG1630)

Large size SCH diagram



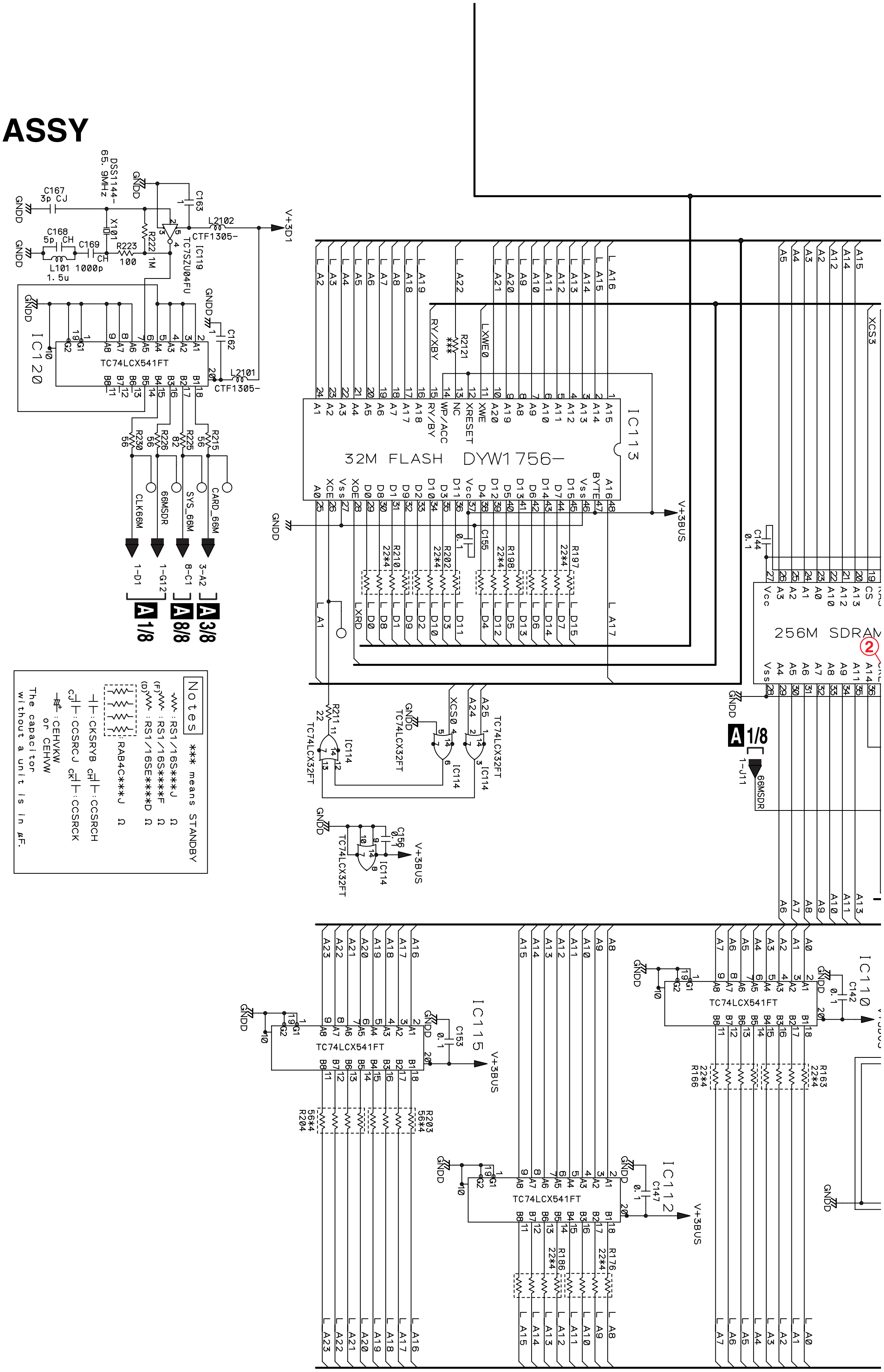

CN102
AKM1292-
TXD
RXD
V+5D
NC
NC (5V)
GNDD
V+5D
V+3D1
R217 10K
R199 22
C154 0.1
IC116 TC7SH08FU
IC118 TC7SH08FU
R205 22
C157 0.1
IC117 BU4230G
RESET IC
C159 0.01
C158 0.1
R201 22K
IC124 TC7SH08FU
C171 0.1
R207 22
R2150 0
HUDI_XRST
VLD_OFF
XRSTCPU
1-B1
7-G4
1-C1, 1-F1
A 1/8
A 7/8
A 1/8
7-F4
7-F4
A 7/8
A 7/8, 8/8
7-F4, 8-B4
DIS_XCS
TXD2
RXD2
UP DATE MODE
R169 10K
R170 10K
R171 10K
R172 10K
R173 10K
R187 10K
R188 10K
R167 10K*4
R164 10K*4
R160 100
C140 0.01
XRSTCPU
A 1/8
1-C1, 1-J4
SHMD0
SHMD1
SHMD2
SHMD3
SHMD4
SHMD5
CA
AN7
AN6
AN5
AN4
AN3
AN2
XCARDIN
MOT EMPTY
XDREQ1
C146 0.1
C148 0.1
R175 10K
R177
TI BSREQ
TI PCMREQ
CAP XINT
10K*4
MD1
MD2
Vcc-RTC
XTAL2
EXTAL2
Vss-RTC
NMI
IREQ0
IREQ1
IREQ2
IREQ3
IREQ4
D31
D30
D29
D28
D27
D26
VssQ
D25
VccQ
D24
D23
D22
D21
D20
Vss
D19
Vcc
D18
D17
D16
VssQ
D15
VccQ
D14
D13
D12
D11
D10
D9
D8
D7
D6
VssQ
D5
VccQ
D4
D3
D2
D1
D0
R178 22*4
R179 22*4
R180 22*4
R181 22*4
R182 22*4
R183 22*4
R184 22*4
R185 22*4
C149 0.1
C150 0.1
C151 0.1
C152 0.1
AVss
AN1
AN0
MD5
MD4
MD3
RESETP
DREQ1
AVcc
A0
A1
A2
A3
VssQ
A4
VccQ
A5
A6
A7
A8
A9
A10
A11
A12
A13
C145 0.1
R168 22*4
R165 22*4
R162 22*4
GNDD
V+3D1
V+3SH
V+2SH
CCG1160-
F107
F112
C165 100/6.3
IC121 PQ070XZ02ZP
VIN VOUT
ON/OFF ADJ
R232 1.0K (F)
R229 270 (F)
R220 330 (F)
C166 100/6.3
F102
F108
CCG1160-

The ⚠ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.



A 1/8
1-C1, 1-J4
XRSTCPU
A 7/8
7-B5
MODE_SW
A 8/8
8-C1
CONFIG_CLK4
A 1/8
1-J11
CLK66M
A 1/8
1-F1, 1-J4
XRSTCPU
A 1/87
1-K4
HUDI_XRST
CN101
※※
R129 10K
IC122 TC7SH04FU
C114 0.1
IC105 TC7SH08FU
C108 0.1
FPGA_XPRG
V+3D1
GNDD
R236 22
IC123 TC7SH08FU
C170 0.1
R139 0
C125 0.01
C140 0.01
R160 100
R155 10K*4
R145 10K*4
MOT_EMPTY
XCARDIN
XDREQ1
XDREQ0
DMA_XINT
CARD_XINT
AV1_INT2
RXD2
RXD1
TXD2
SHMD5
SHMD4
SHMD3
CA
GSL_RST
CARD_RST
VQE_RST
DMA_RST
CONFDAT1
TI_BSACK
TI_PCMACK
R213 22
R135 0
R131 22
R130 22
C133 0.1
C126 0.1
C122 0.1
ASEMD0
AUDCK
XASKAK
TDO
TDI
XTRST
TMS
TCK
XAUSYC
ADATA3
ADATA2
ADATA1
ADATA0
HD6417709SHF200B
IC111
CPU
156 EXTAL
155 XTAL
154 Vcc
153 Vss
R126 0
C113 0.1
C109 1
C115 CH 470p
C110 1
C116 0.1
C117 0.1
GSL_XIRQ
GSL_XIRQS
AV1_XINTR
VDEC_XINT
R102 10K*4
R107 2.2K
R103 1K
R104 10K*4
R105 10K*4
R106 10K*4
R108 10K
R109 4.7K
R110 100K
RY/XBY
FPGA_XINIT
FPGA_DONE
XWAIT
R2117 0
R2118 0
R121 10K
R122 10K
R114 10K
R111 0
MUTE
6-H12 44/48
A 6/8
R115 22*4
R116 22*4
R117 22*4
AUDIO_SEL
AQE_RST
XDACK1
XDACK0
VDEC_RST
TI_RST
MOT_RST
XCASLL
AV1_RST
XRAS3L
CKE
IC101 TC74VHC04FT
GSL_PLL 4-D4
GSL_XRST 4-C1
C101 0.1
A 4/8
CARD_XRST 3-A2
A 3/8
DVC_XRST 8-A2
VQE_XRST 6-A4, 6-D3 8-B4
A 8/8
IC102 TC74VHC04FT
AQE_XRST 6-H3
A 6/8
VDEC_XRST 5-B3
A 5/8
TI_XRST 3-C3
MOT_XRST 3-C6
A 3/8
AV1_XRST 2-A5
A 2/8
C102 0.1
V+3SH
V+2SH
R156 0
XRD_FPGA
8-A2, 8-C1
A 8/8
C141 0.1
C134 0.1
C130 0.1
C123 0.1
R165 22*4
R162 22*4
R161 22*4
R157 22*4
R150 22*4
R146 22
R140 22
R141 22*4
R137 22*4
R136 22*4
R133 22
R132 22
XRD
XBS
XWE0
XWE1
XWE2
XWE3
RDWR
XAUSYC
XCS0
XCS2
XCS3
XCS4
XCS5
XCS6

A 1/8 MAIN ASSY


DSS1144-
65.9MHz
X101
C167
3p CJ
C168
5p CH
C169
CH
L101 1000p
1.5u
R222 1M
R223
100
C163
1
L2102
CTF1305-
IC119
TC7SZU04FU
V+3D1
GNDD
IC120
TC74LCX541FT
C162
L2101
CTF1305-
R230 56
R226 56
R225 82
R215 56
CLK66M
66MSDR
SYS_66M
CARD_66M
1-D1
1-G12
8-C1
3-A2
A 1/8
A 8/8
A 3/8
IC113
32M FLASH DYW1756-
R2121
***
RY/XBY
LXWE0
C155
0.1
R210 22*4
R202 22*4
R198 22*4
R197 22*4
V+3BUS
L A1
LXRD
L A17
L A2
L A3
L A4
L A5
L A6
L A7
L A8
L A18
L A19
L A22
L A21
L A20
L A9
L A10
L A11
L A12
L A13
L A14
L A15
L A16
L D0
L D8
L D1
L D9
L D2
L D10
L D3
L D11
L D4
L D12
L D5
L D13
L D6
L D14
L D7
L D15
R211
22
IC114
TC74LCX32FT
XCS0
A24
A25
C156
0.1
C144
0.1
256M SDRAM
②
XCS3
A 1/8
1-J11
66MSDR
IC115
TC74LCX541FT
C153
0.1
R204 56*4
R203 56*4
IC112
TC74LCX541FT
C147
0.1
R186 22*4
R176 22*4
IC110
TC74LCX541FT
C142
0.1
R166 22*4
R163 22*4
L A23
L A0

Notes

*** means STANDBY

-ww- :RS1/16S***J Ω

(F) -ww- :RS1/16S***F Ω

(D) -ww- :RS1/16SE***D Ω

:RAB4C***J Ω

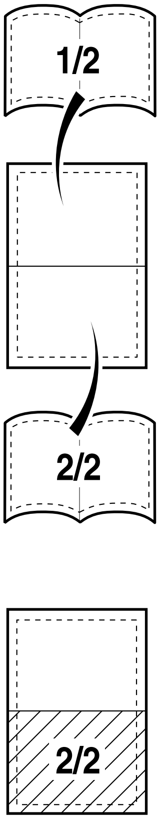
⊣⊢ :CKSRYB CH ⊣⊢ :CCSRCH

CJ ⊣⊢ :CCSRCJ CK ⊣⊢ :CCSRCK

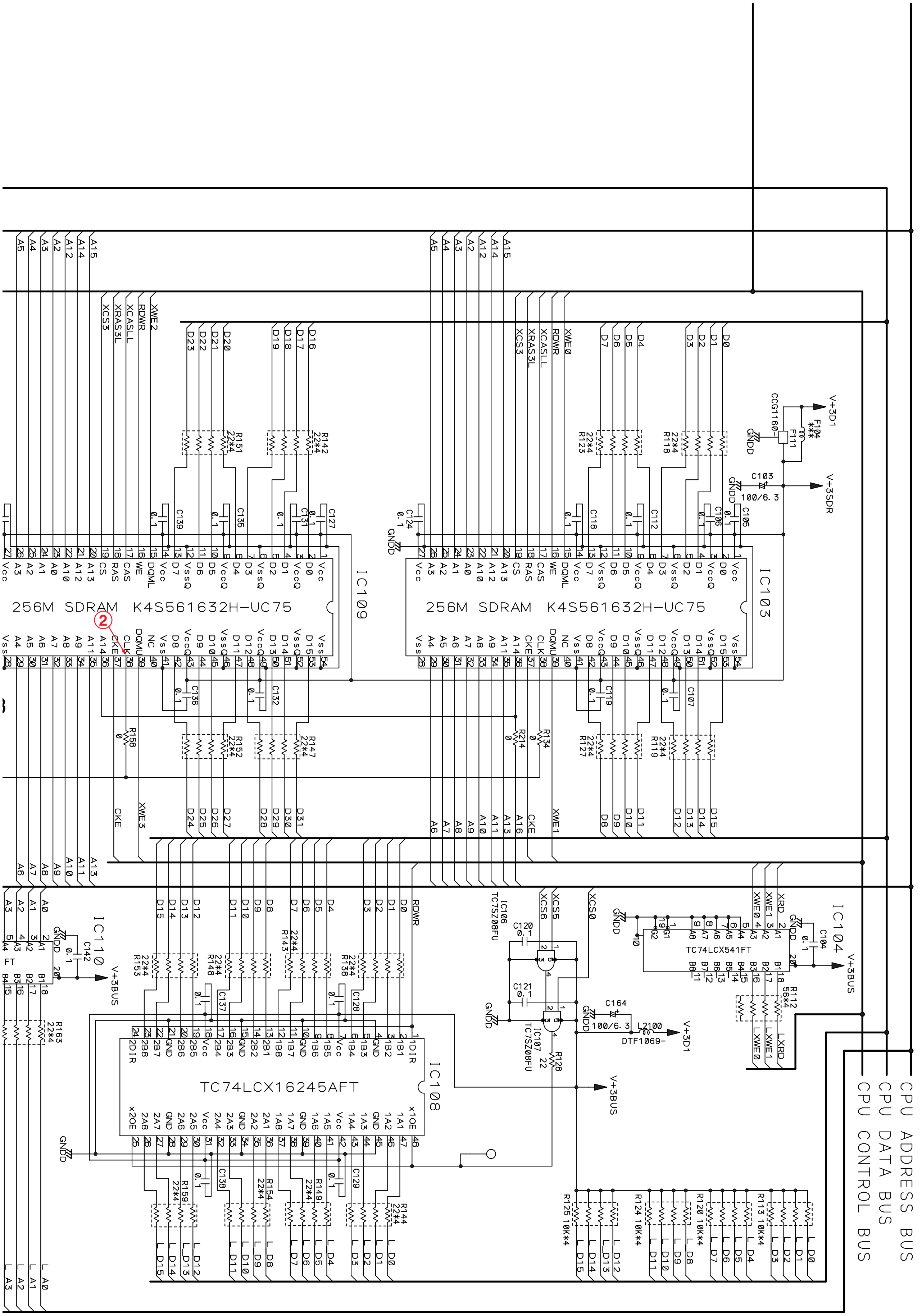
:CEHVKW or CEHVW

The capacitor without a unit is in µF.

Large size SCH diagram

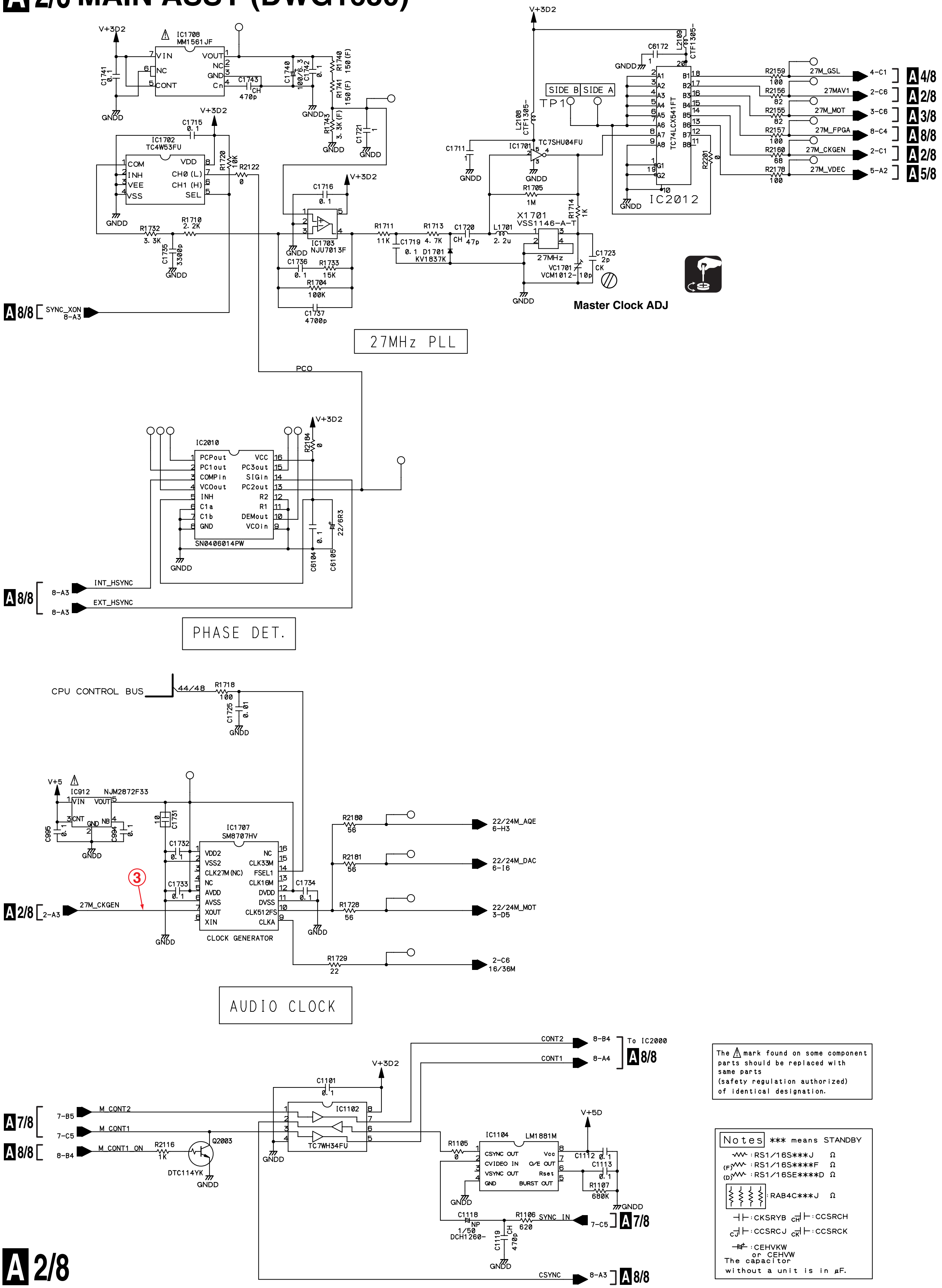


A 1/8


IC109
256M SDRAM K4S561632H-UC75
IC103
256M SDRAM K4S561632H-UC75
IC108
TC74LCX16245AFT
IC104
TC74LCX541FT
IC106
TC7SZ08FU
IC107
TC7SZ08FU
IC110
CPU ADDRESS BUS
CPU DATA BUS
CPU CONTROL BUS
V+3D1
V+3SDR
V+3BUS
CCG1160-
F104
F111
C103
100/6.3
C164
100/6.3
L2100
DTF1069-
GNDD
R151 22*4
R142 22*4
R123 22*4
R118 22*4
R152 22*4
R147 22*4
R127 22*4
R119 22*4
R158 0
R214 0
R134 0
R153 22*4
R148 22*4
R143 22*4
R138 22*4
R159 22*4
R154 22*4
R149 22*4
R144 22*4
R163 22*4
R112 56*4
R128 22
R125 10K*4
R124 10K*4
R120 10K*4
R113 10K*4
C139 C135 C131 C127 C124 C118 C112 C106 C105
C136 C132 C119 C107
C137 C128 C138 C129 C142 C104 C120 C121
0.1
XCS3 XRAS3L XCASLL RDWR XWE2 XWE0 XWE1 XWE3
XCS0 XCS5 XCS6 XRD
CKE
LXRD LXWE1 LXWE0
L D0 L D1 L D2 L D3 L D4 L D5 L D6 L D7 L D8 L D9 L D10 L D11 L D12 L D13 L D14 L D15
L A0 L A1 L A2 L A3
②

3.5 MAIN ASSY(2/8)

A 2/8 MAIN ASSY (DWG1630)



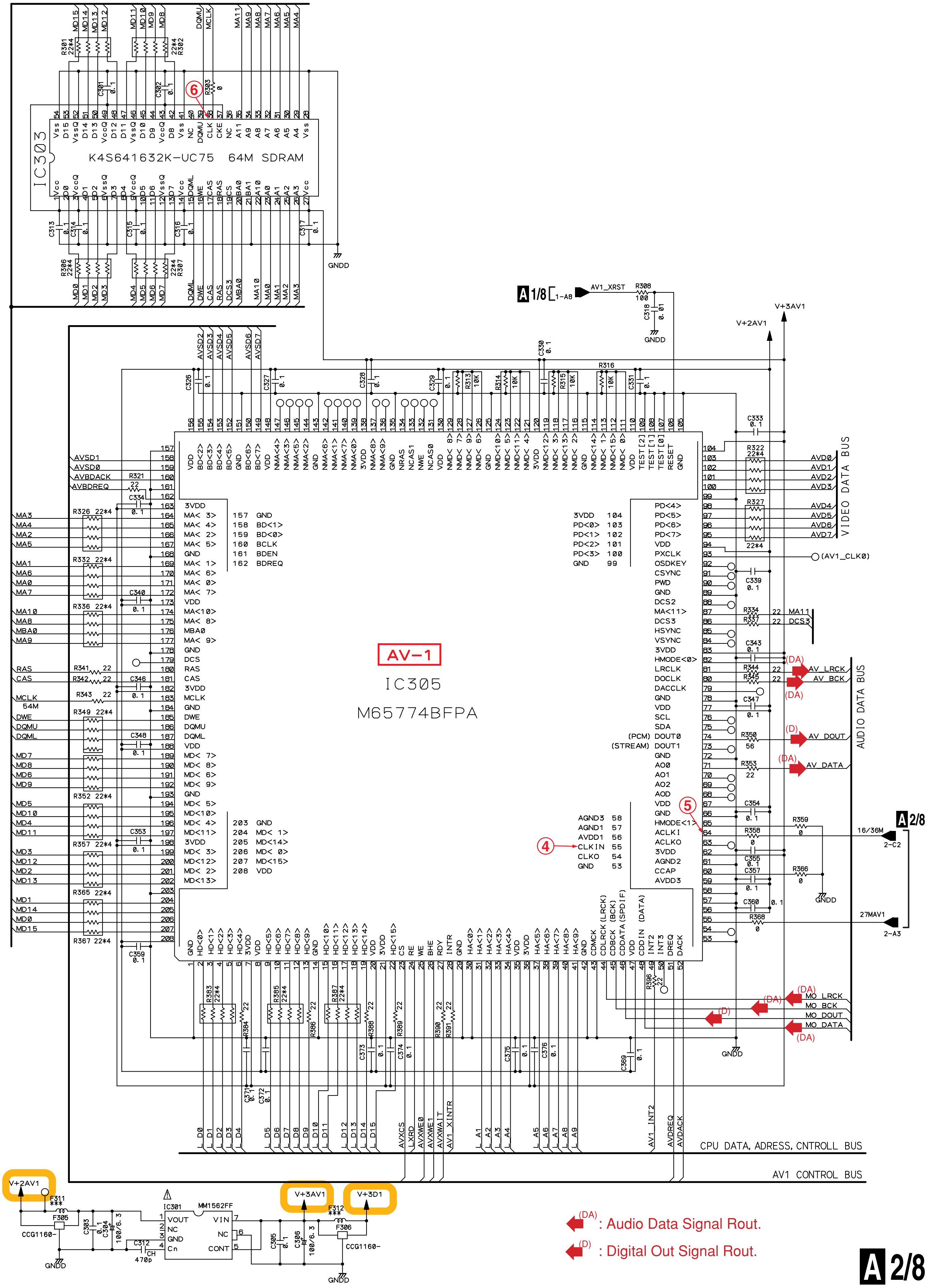
V+3D2
IC1708
MM1561JF
VIN
VOUT
NC
GND
CONT
Cn
C1741 0.1
C1743 CH 470p
C1740 100/6.3
C1742 0.1
R1740 150 (F)
R1741 150 (F)
R1743 3.3K (F)
C1721 1
GNDD
C1715 0.1
IC1702
TC4W53FU
COM
INH
VEE
VSS
VDD
CH0 (L)
CH1 (H)
SEL
R1720 10K
R2122 0
R1732 3.3K
R1710 2.2K
C1735 3300p
C1716 0.1
IC1703
NJU7013F
C1736 0.1
R1733 15K
R1704 100K
C1737 4700p
R1711 11K
R1713 4.7K
C1719 0.1
C1720 CH 47p
D1701 KV1837K
L1701 2.2u
X1701 VSS1146-A-T
27MHz
VC1701 VCM1012-10p
C1723 2p CK
R1705 1M
R1714 1K
C1711 1
IC1701
TC7SHU04FU
L2108 CTF1305-
L2109 CTF1305-
C6172 1
SIDE B
SIDE A
TP1
IC2012
TC74LCX541FT
A1 A2 A3 A4 A5 A6 A7 A8
B1 B2 B3 B4 B5 B6 B7 B8
G1 G2
R2201 0
R2159 100
R2156 82
R2155 82
R2157 100
R2160 68
R2178 100
27M_GSL
27MAV1
27M_MOT
27M_FPGA
27M_CKGEN
27M_VDEC
4-C1 A 4/8
2-C6 A 2/8
3-C6 A 3/8
8-C4 A 8/8
2-C1 A 2/8
5-A2 A 5/8
Master Clock ADJ
A 8/8 SYNC_XON 8-A3
27MHz PLL
PCO
IC2010
SN0406014PW
PCPout
PC1out
COMPin
VCOout
INH
C1a
C1b
GND
VCC
PC3out
SIGin
PC2out
R2
R1
DEMout
VCOin
R2104 0
C6104 0.1
C6105 22/6R3
A 8/8
8-A3 INT_HSYNC
8-A3 EXT_HSYNC
PHASE DET.
CPU CONTROL BUS
44/48
R1718 100
C1725 0.01
V+5
IC912
NJM2872F33
VIN
VOUT
CNT
GND
NB
C995 0.1
C994 0.1
C1731 10
IC1707
SM8707HV
VDD2
VSS2
CLK27M(NC)
NC
AVDD
AVSS
XOUT
XIN
NC
CLK33M
FSEL1
CLK16M
DVDD
DVSS
CLK512FS
CLKA
CLOCK GENERATOR
C1732 0.1
C1733 0.1
C1734 0.1
③
A 2/8 2-A3 27M_CKGEN
R2180 56
R2181 56
R1728 56
R1729 22
22/24M_AQE 6-H3
22/24M_DAC 6-I6
22/24M_MOT 3-D5
2-C6 16/36M
AUDIO CLOCK
CONT2 8-B4
CONT1 8-A4
To IC2000 A 8/8
C1101 0.1
IC1102
TC7WH34FU
A 7/8
7-B5 M_CONT2
7-C5 M_CONT1
A 8/8 8-B4 M_CONT1_ON
R2116 1K
Q2003
DTC114YK
IC1104
LM1881M
CSYNC OUT
CVIDEO IN
VSYNC OUT
GND
Vcc
O/E OUT
Rset
BURST OUT
R1105 0
V+5D
C1112 0.1
C1113 0.1
R1107 680K
C1118 NP 1/50 DCH1260-
R1106 620
C1119 CH 470p
SYNC IN 7-C5 A 7/8
CSYNC 8-A3 A 8/8
A 2/8

The ⚠ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.

Notes *** means STANDBY

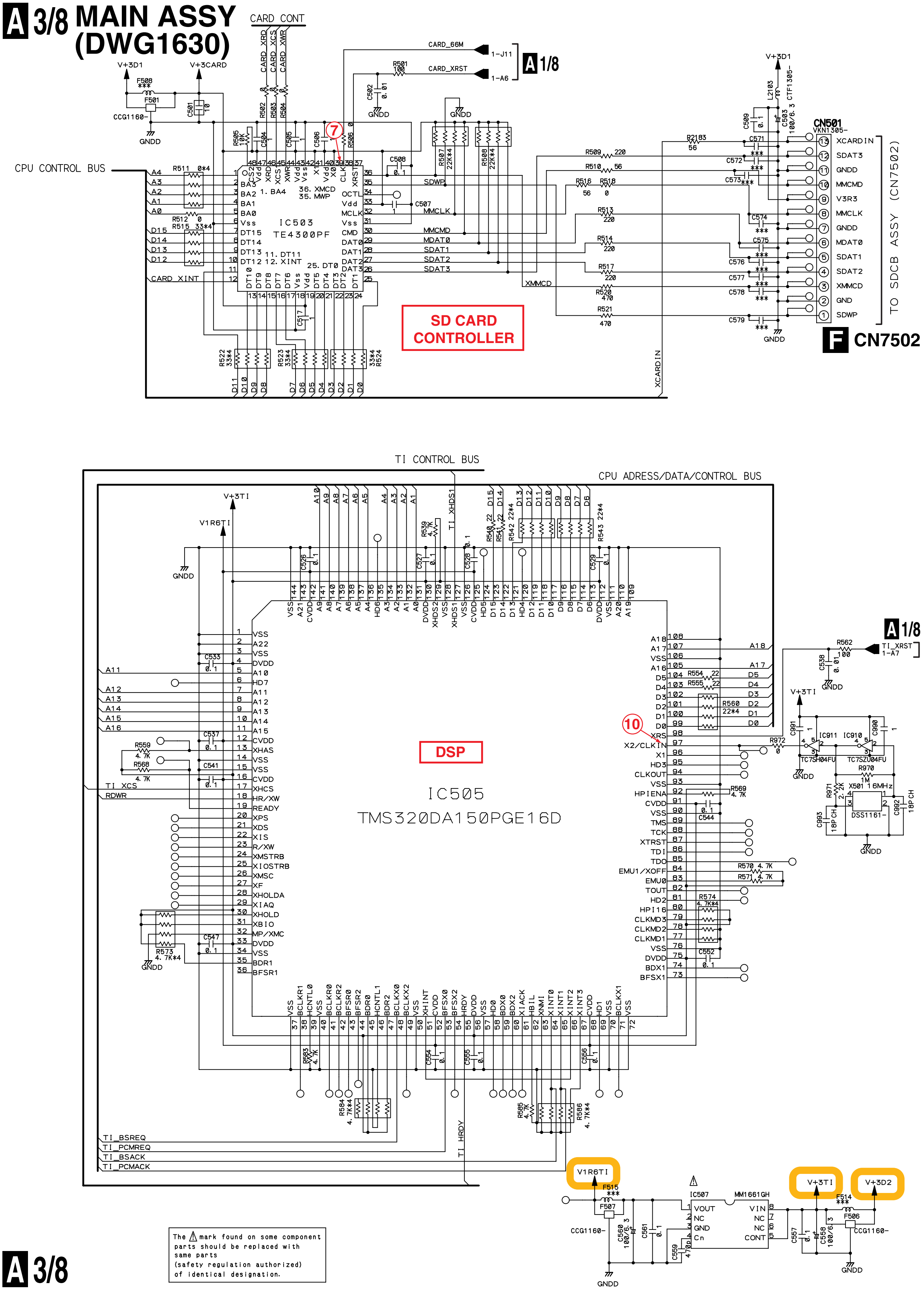
- RS1/16S***J Ω
- (F) RS1/16S****F Ω
- (D) RS1/16SE****D Ω
- RAB4C***J Ω
- CKSRYB CH: CCSRCH
- CJ: CCSRCJ CK: CCSRCK
- CEHVKW or CEHVW

The capacitor without a unit is in μF.


IC303
K4S641632K-UC75 64M SDRAM
AV-1
IC305
M65774BFPA
IC301
MM1562FF
VOUT
VIN
NC
GND
CONT
Cn
CCG1160-
F305
F306
F311
F312
V+2AV1
V+3AV1
V+3D1
V+3AV1
V+2AV1
GNDD
A 1/8 1-A8
AV1_XRST
R308
100
C318
0.01
(AV1_CLK0)
VIDEO DATA BUS
AVD0
AVD1
AVD2
AVD3
AVD4
AVD5
AVD6
AVD7
AUDIO DATA BUS
AV LRCK
AV BCK
AV DOUT
AV DATA
MA11
DCS3
A 2/8
16/36M
2-C2
27MAV1
2-A3
MO LRCK
MO BCK
MO DOUT
MO DATA
CPU DATA, ADRESS, CNTROLL BUS
AV1 CONTROL BUS
AVSD1
AVSD0
AVBDACK
AVBDREQ
AVSD2
AVSD3
AVSD4
AVSD5
AVSD6
AVSD7
MCLK
54M
④
⑤
⑥
(DA)
(D)

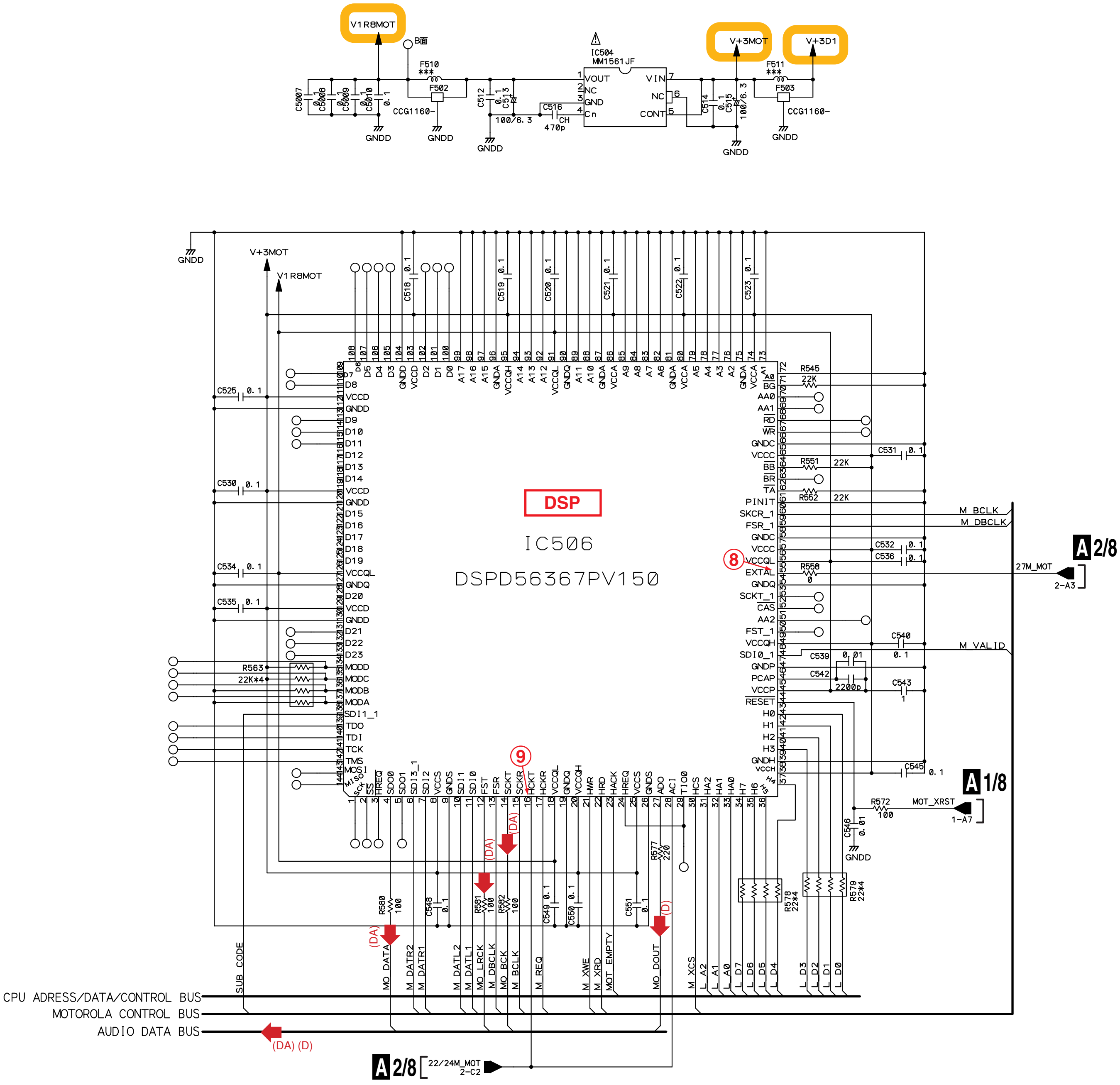
(DA) : Audio Data Signal Rout.

(D) : Digital Out Signal Rout.


A 3/8 MAIN ASSY (DWG1630)
CARD CONT
CARD_XRD
CARD_XCS
CARD_XWR
CARD_66M
CARD_XRST
1-J11
1-A6
A 1/8
V+3D1
V+3CARD
F508
F501
CCG1160-
C501
GNDD
CPU CONTROL BUS
A4
A3
A2
A1
A0
D15
D14
D13
D12
CARD XINT
R511 0*4
R512 0
R515 33*4
R501 100
C502 0.01
R502
R503
R504
R505 10K
C504
C505
C506
R506 0
7
C508 0.1
R507 22K*4
R508 22K*4
IC503
TE4300PF
SD CARD CONTROLLER
SDWP
OCTL
MCLK
CMD
DAT0
DAT1
DAT2
DAT3
MMCLK
MMCMD
MDAT0
SDAT1
SDAT2
SDAT3
XMMCD
C507
C517
R522 33*4
R523 33*4
R524 33*4
D11
D10
D9
D8
D7
D6
D5
D4
D3
D2
D1
D0
XCARDIN
R509 220
R510 56
R516 56
R518 0
R513 220
R514 220
R517 220
R520 470
R521 470
R2183 56
C509 0.1
C503 100/6.3
L2103
CTF1305-
C571
C572
C573
C574
C575
C576
C577
C578
C579
CN501
VKN1305-
XCARDIN
SDAT3
GNDD
MMCMD
V3R3
MMCLK
GNDD
MDAT0
SDAT1
SDAT2
XMMCD
GND
SDWP
TO SDCB ASSY (CN7502)
F CN7502
TI CONTROL BUS
CPU ADRESS/DATA/CONTROL BUS
V+3TI
V1R6TI
TI XHDS1
R539 4.7K
R540 22
R541 22
R542 22*4
R543 22*4
C526 0.1
C527 0.1
C528 0.1
C529 0.1
C533 0.1
C537 0.1
C541 0.1
C547 0.1
R559 4.7K
R568 4.7K
R573 4.7K*4
TI_XCS
RDWR
DSP
IC505
TMS320DA150PGE16D
10
A 1/8
R562 100
TI_XRST
1-A7
C538 0.01
R554 22
R555 22
R560 22*4
R972 0
IC911
IC910
TC7SH04FU
TC7SZU04FU
C991
C990
R970 1M
R971 2.2K
X501 16MHz
DSS1161-
C993 18P CH
C992 18P CH
R569 4.7K
C544 0.1
R570 4.7K
R571 4.7K
R574 4.7K*4
C552 0.1
C554 0.1
C555 0.1
C556 0.1
R583 4.7K
R584 4.7K*4
R585 4.7K
R586 4.7K*4
TI_BSREQ
TI_PCMREQ
TI_BSACK
TI_PCMACK
TI HRDY
V1R6TI
F515
F507
CCG1160-
C560 100/6.3
C561 0.1
C559 470p
IC507
MM1661GH
VOUT
NC
GND
Cn
VIN
NC
NC
CONT
V+3TI
V+3D2
F514
F506
C557 0.1
C558 100/6.3
CCG1160-
GNDD

The ⚠ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.

A 3/8


V1R8MOT
F510
***
F502
CCG1160-
C5007
C5008
C5009
C5010
0.1
GNDD
C512
C513
100/6.3
C516
CH
470p
IC504
MM1561JF
VOUT
NC
GND
Cn
VIN
NC
CONT
C514
C515
V+3MOT
V+3D1
F511
***
F503
CCG1160-
DSP
IC506
DSPD56367PV150
8
9
(DA)
(D)
R545 22K
R551 22K
R552 22K
R558 0
C531 0.1
C532 0.1
C536 0.1
C539 0.01
C540 0.1
C542 2200p
C543 1
C545 0.1
C546 0.01
R572 100
M_BCLK
M_DBCLK
27M_MOT
2-A3
A 2/8
M_VALID
MOT_XRST
1-A7
A 1/8
C525 0.1
C530 0.1
C534 0.1
C535 0.1
R563 22K*4
C518 0.1
C519 0.1
C520 0.1
C521 0.1
C522 0.1
C523 0.1
R580 100
R581 100
R582 100
C548 0.1
C549 0.1
C550 0.1
C551 0.1
R577 220
R578 22*4
R579 22*4
SUB_CODE
MO_DATA
M_DATR2
M_DATR1
M_DATL2
M_DATL1
MO_LRCK
M_DBCLK
MO_BCK
M_BCLK
M_REQ
M_XWE
M_XRD
MOT_EMPTY
MO_DOUT
M_XCS
L_A2
L_A1
L_A0
L_D7
L_D6
L_D5
L_D4
L_D3
L_D2
L_D1
L_D0
CPU ADRESS/DATA/CONTROL BUS
MOTOROLA CONTROL BUS
AUDIO DATA BUS
(DA) (D)
A 2/8
22/24M_MOT
2-C2

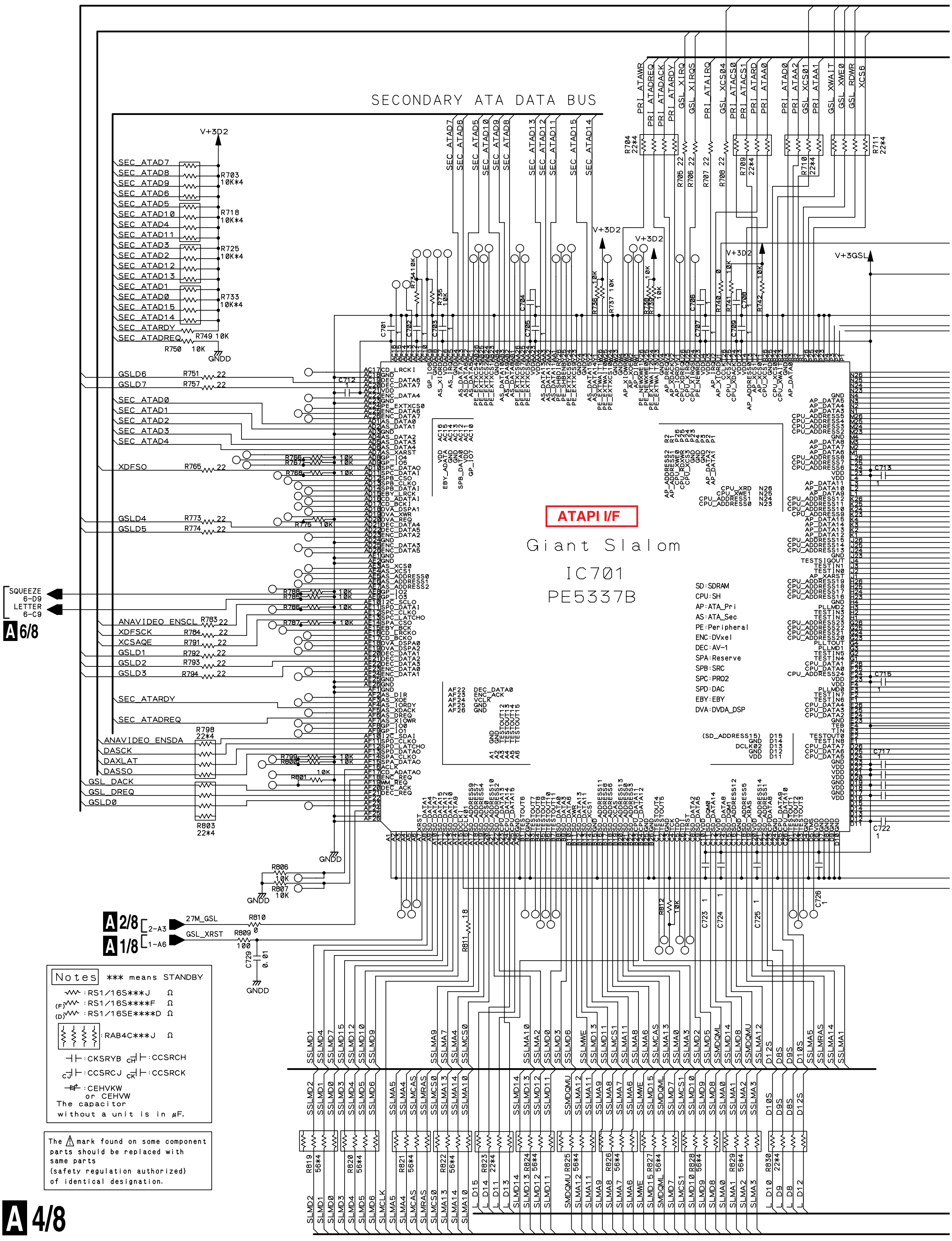
Notes *** means STANDBY

-ww- : RS1/16S***J Ω
(F) -ww- : RS1/16S****F Ω
(D) -ww- : RS1/16SE****D Ω
RAB4C***J Ω
⊣⊢ : CKSRYB CH ⊣⊢ : CCSRCH
CJ ⊣⊢ : CCSRCJ CK ⊣⊢ : CCSRCK
: CEHVKW or CEHVW
The capacitor without a unit is in µF.

(DA) : Audio Data Signal Rout.

(D) : Digital Out Signal Rout.

3.7 MAIN ASSY(4/8)


SECONDARY ATA DATA BUS
V+3D2
V+3GSL
R703 10K*4
R718 10K*4
R725 10K*4
R733 10K*4
R749 10K
R750 10K
GNDD
SEC_ATAD7
SEC_ATAD8
SEC_ATAD9
SEC_ATAD6
SEC_ATAD5
SEC_ATAD10
SEC_ATAD4
SEC_ATAD11
SEC_ATAD3
SEC_ATAD2
SEC_ATAD12
SEC_ATAD13
SEC_ATAD1
SEC_ATAD0
SEC_ATAD15
SEC_ATAD14
SEC_ATARDY
SEC_ATADREQ
GSLD6
GSLD7
XDFSO
GSLD4
GSLD5
SQUEEZE 6-D9
LETTER 6-C9
A 6/8
ANAVIDEO_ENSCL
XDFSCK
XCSAQE
GSLD1
GSLD2
GSLD3
ANAVIDEO_ENSDA
DASCK
DAXLAT
DASSO
GSL_DACK
GSL_DREQ
GSLD0
R798 22*4
R803 22*4
PRI_ATAWR
PRI_ATADREQ
PRI_ATADACK
PRI_ATARDY
GSL_XIRQ
GSL_XIRQS
PRI_ATAIRQ
GSL_XCS04
PRI_ATACS0
PRI_ATACS1
PRI_ATARD
PRI_ATAA0
PRI_ATAD0
PRI_ATAA2
GSL_XCS01
PRI_ATAA1
GSL_XWAIT
GSL_XWE0
GSL_RDWR
XCS6
R704 22*4
R711 22*4
R709 22*4
R710 22*4
ATAPI I/F
Giant Slalom
IC701
PE5337B
SD:SDRAM
CPU:SH
AP:ATA_Pri
AS:ATA_Sec
PE:Peripheral
ENC:DVxel
DEC:AV-1
SPA:Reserve
SPB:SRC
SPC:PRO2
SPD:DAC
EBY:EBY
DVA:DVDA_DSP
A 2/8 2-A3 27M_GSL
A 1/8 1-A6 GSL_XRST
R810
R809 100
C729 0.01
R806 10K
R807 10K
R811 18
R812 10K
C723
C724
C725
C726
R819 56*4
R820 56*4
R821 56*4
R822 56*4
R823 22*4
R824 56*4
R825 56*4
R826 56*4
R827 56*4
R828 56*4
R829 56*4
R830 22*4
SSLMD1
SSLMD4
SSLMD7
SSLMD15
SSLMD12
SSLMD10
SSLMD9
SSLMA9
SSLMA7
SSLMA4
SSLMCS0
SSLMA10
SSLMA2
SSLMD0
SSLMD3
SSLMD6
SSLMWE
SSLMD13
SSLMD11
SSLMCS1
SSLMA11
SSLMA8
SSLMA6
SSLMCAS
SSLMA13
SSLMA0
SSLMA3
SSLMD2
SSLMD5
SSMDQML
SSLMD14
SSLMD8
SSMDQMU
SSLMA12
D12S
D8S
D9S
D10S
SSLMA5
SSLMRAS
SSLMA14
SSLMA1
SLMD2
SLMD1
SLMD0
SLMD3
SLMD4
SLMD5
SLMD6
SLMCLK
SLMA5
SLMA4
SLMCAS
SLMRAS
SLMCS0
SLMA13
SLMA14
SLMA10
L_D15
L_D14
L_D11
L_D13
SLMD14
SLMD13
SLMD12
SLMD11
SMDQMU
SLMA12
SLMA11
SLMA9
SLMA8
SLMA7
SLMA6
SLMWE
SLMD15
SMDQML
SLMD7
SLMCS1
SLMD10
SLMD9
SLMD8
SLMA0
SLMA1
SLMA2
SLMA3
L_D10
L_D9
L_D8
L_D12

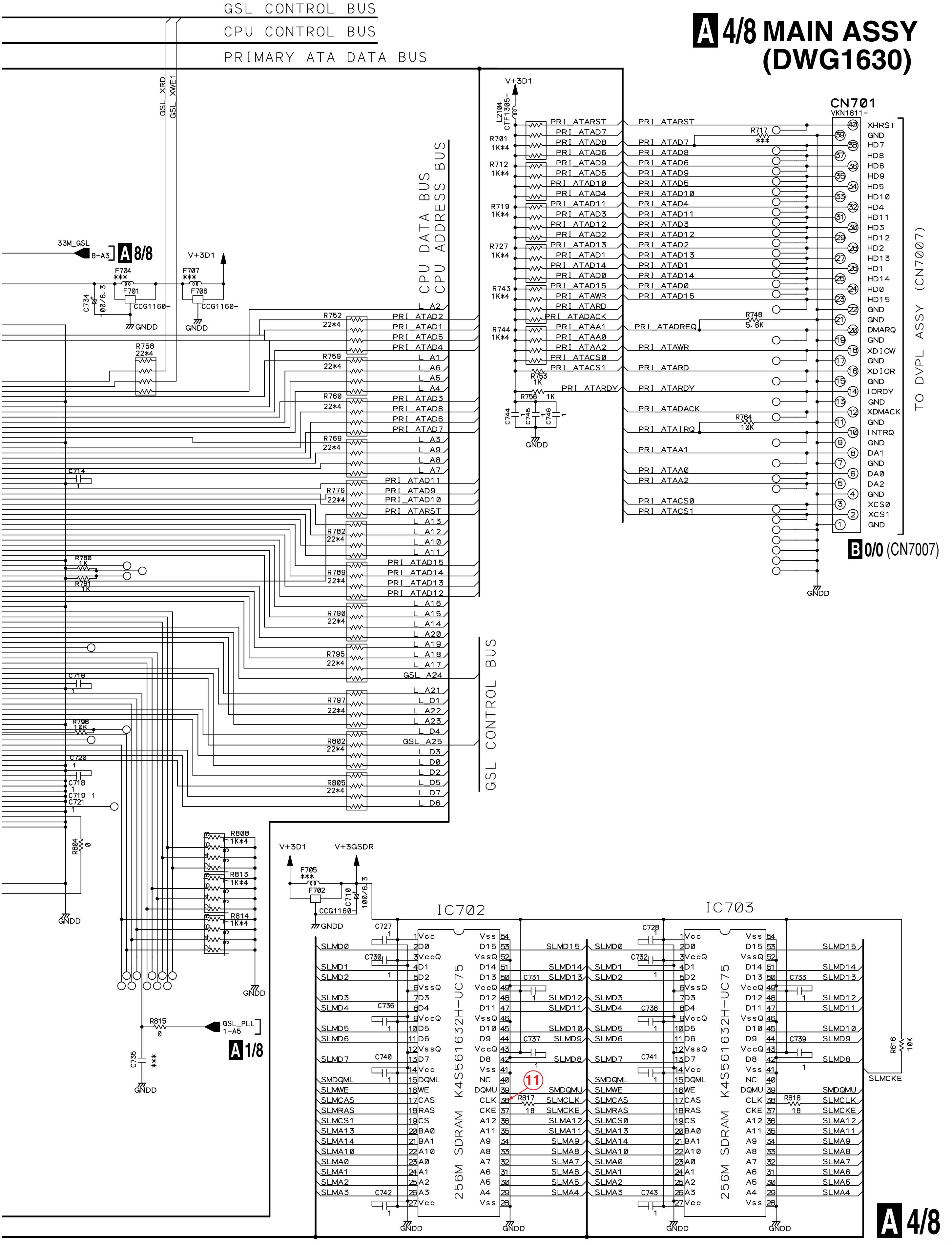
Notes *** means STANDBY

-ww- : RS1/16S***J Ω
(F) : RS1/16S****F Ω
(D) : RS1/16SE****D Ω
: RAB4C***J Ω
: CKSRYB CH : CCSRCH
CJ : CCSRCJ CK : CCSRCK
: CEHVKW
or CEHVW
The capacitor
without a unit is in µF.

The ⚠ mark found on some component
parts should be replaced with
same parts
(safety regulation authorized)
of identical designation.

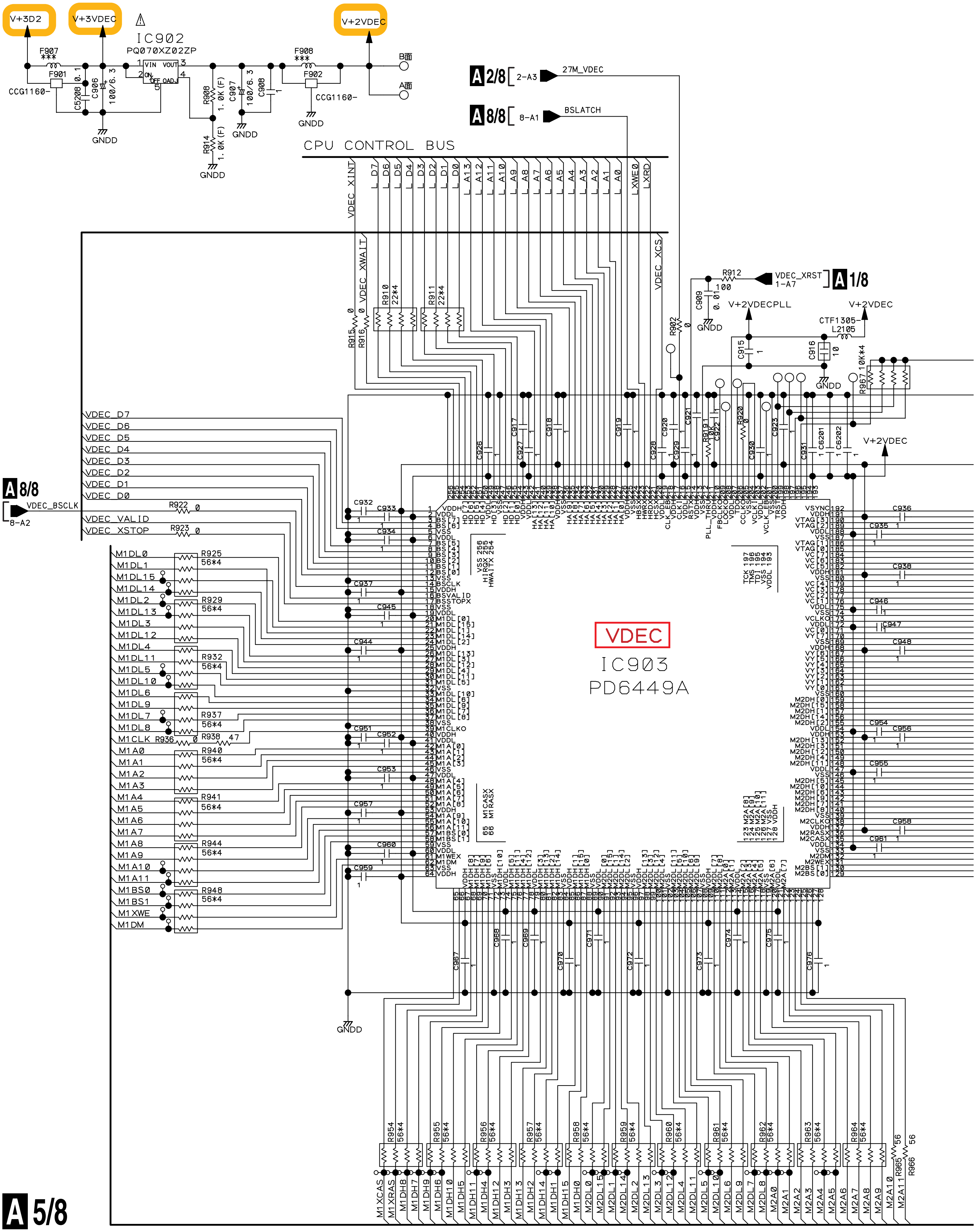
A 4/8

A 4/8 MAIN ASSY
(DWG1630)


GSL CONTROL BUS
CPU CONTROL BUS
PRIMARY ATA DATA BUS
GSL_XRD
GSL_XWE1
CPU DATA BUS
CPU ADDRESS BUS
GSL CONTROL BUS
33M_GSL
8-A3
A 8/8
V+3D1
F704
***
F707
***
F701
F706
CCG1160-
C734
100/6.3
GNDD
R758
22*4
R752 22*4
R759 22*4
R760 22*4
R769 22*4
R776 22*4
R782 22*4
R789 22*4
R790 22*4
R795 22*4
R797 22*4
R802 22*4
R805 22*4
C714
C716
C718
C719
C720
C721
R780 1K
R781 1K
R798 10K
R804 0
R808 1K*4
R813 1K*4
R814 1K*4
R815 0
GSL_PLL
1-A5
A 1/8
C735
***
L_A1
L_A2
L_A3
L_A4
L_A5
L_A6
L_A7
L_A8
L_A9
L_A10
L_A11
L_A12
L_A13
L_A14
L_A15
L_A16
L_A17
L_A18
L_A19
L_A20
L_A21
L_A22
L_A23
GSL_A24
GSL_A25
L_D0
L_D1
L_D2
L_D3
L_D4
L_D5
L_D6
L_D7
PRI_ATAD0
PRI_ATAD1
PRI_ATAD2
PRI_ATAD3
PRI_ATAD4
PRI_ATAD5
PRI_ATAD6
PRI_ATAD7
PRI_ATAD8
PRI_ATAD9
PRI_ATAD10
PRI_ATAD11
PRI_ATAD12
PRI_ATAD13
PRI_ATAD14
PRI_ATAD15
PRI_ATARST
PRI_ATAWR
PRI_ATARD
PRI_ATADACK
PRI_ATADREQ
PRI_ATAA0
PRI_ATAA1
PRI_ATAA2
PRI_ATACS0
PRI_ATACS1
PRI_ATARDY
PRI_ATAIRQ
L2104
CTF1305-
R701 1K*4
R712 1K*4
R719 1K*4
R727 1K*4
R743 1K*4
R744 1K*4
R753 1K
R756 1K
C744
C745
C746
R717 ***
R748 5.6K
R764 10K
CN701
VKN1811-
XHRST
GND
HD7
HD8
HD6
HD9
HD5
HD10
HD4
HD11
HD3
HD12
HD2
HD13
HD1
HD14
HD0
HD15
DMARQ
XDIOW
XDIOR
IORDY
XDMACK
INTRQ
DA1
DA0
DA2
XCS0
XCS1
TO DVPL ASSY (CN7007)
B 0/0 (CN7007)
V+3GSDR
F705
***
F702
CCG1160-
C710
100/6.3
IC702
IC703
256M SDRAM K4S561632H-UC75
C727
C728
C730
C731
C732
C733
C736
C737
C738
C739
C740
C741
C742
C743
Vcc
VccQ
VssQ
Vss
DQML
DQMU
WE
CAS
RAS
CS
BA0
BA1
CLK
CKE
NC
SLMD0
SLMD1
SLMD2
SLMD3
SLMD4
SLMD5
SLMD6
SLMD7
SLMD8
SLMD9
SLMD10
SLMD11
SLMD12
SLMD13
SLMD14
SLMD15
SLMDQML
SLMDQMU
SLMWE
SLMCAS
SLMRAS
SLMCS0
SLMCS1
SLMCLK
SLMCKE
SLMA0
SLMA1
SLMA2
SLMA3
SLMA4
SLMA5
SLMA6
SLMA7
SLMA8
SLMA9
SLMA10
SLMA11
SLMA12
SLMA13
SLMA14
R817 18
R818 18
⑪
R816 10K
A 4/8

3.8 MAIN ASSY(5/8)

A 5/8 MAIN ASSY (DWG1630)


V+3D2
V+3VDEC
V+2VDEC
IC902
PQ070XZ02ZP
VIN VOUT
ON OFF OADJ
F907 ***
F901
CCG1160-
C5208 0.1
C906 100/6.3
R908 1.0K (F)
C907 100/6.3
C908 1
R914 1.0K (F)
F908 ***
F902
CCG1160-
GNDD
B面
A面
A 2/8 [2-A3
27M_VDEC
A 8/8 [8-A1
BSLATCH
CPU CONTROL BUS
VDEC XINT
L D7
L D6
L D5
L D4
L D3
L D2
L D1
L D0
L A13
L A12
L A11
L A10
L A9
L A8
L A7
L A6
L A5
L A4
L A3
L A2
L A1
L A0
LXWE0
LXRD
VDEC XWAIT
VDEC XCS
R915 0
R916 0
R910 22*4
R911 22*4
R912 100
VDEC_XRST 1-A7
A 1/8
C909 0.01
R902 0
V+2VDECPLL
V+2VDEC
CTF1305-
L2105
C915 1
C916 10
R967 10K*4
VDEC_D7
VDEC_D6
VDEC_D5
VDEC_D4
VDEC_D3
VDEC_D2
VDEC_D1
VDEC_D0
A 8/8
VDEC_BSCLK
8-A2
R922 0
VDEC_VALID
VDEC_XSTOP
R923 0
M1DL0
M1DL1
M1DL15
M1DL14
M1DL2
M1DL13
M1DL3
M1DL12
M1DL4
M1DL11
M1DL5
M1DL10
M1DL6
M1DL9
M1DL7
M1DL8
M1CLK
M1A0
M1A1
M1A2
M1A3
M1A4
M1A5
M1A6
M1A7
M1A8
M1A9
M1A10
M1A11
M1BS0
M1BS1
M1XWE
M1DM
R925 56*4
R929 56*4
R932 56*4
R937 56*4
R936 0
R938 47
R940 56*4
R941 56*4
R944 56*4
R948 56*4
VDEC
IC903
PD6449A
VSS 256
HIRGX 255
HWAITX 254
TCK 197
TMS 196
TDI 195
VSS 194
VDDL 193
M1CASX 65
M1RASX 66
123 M2A[8]
124 M2A[9]
125 M2A[10]
126 M2A[11]
127 VSS
128 VDDH
VSYNC
VTAG
VCLKO
M2CLKO
M2RASX
M2CASX
M2DM
M2WEX
M2BS[1]
M2BS[0]
C917
C918
C919
C920
C921
C922
C923
C926
C927
C928
C929
C930
C931
C6201
C6202
R919 1K
R920 0
C932
C933
C934
C935
C936
C937
C938
C944
C945
C946
C947
C948
C951
C952
C953
C954
C955
C956
C957
C958
C959
C960
C961
C967
C968
C969
C970
C971
C972
C973
C974
C975
C976
R954 56*4
R955 56*4
R956 56*4
R957 56*4
R958 56*4
R959 56*4
R960 56*4
R961 56*4
R962 56*4
R963 56*4
R964 56*4
R965 56
R966 56
M1XCAS
M1XRAS
M1DH8
M1DH7
M1DH9
M1DH6
M1DH10
M1DH5
M1DH11
M1DH4
M1DH12
M1DH3
M1DH13
M1DH2
M1DH14
M1DH1
M1DH15
M1DH0
M2DL0
M2DL15
M2DL1
M2DL14
M2DL2
M2DL13
M2DL3
M2DL12
M2DL4
M2DL11
M2DL5
M2DL10
M2DL6
M2DL9
M2DL7
M2DL8
M2A0
M2A1
M2A2
M2A3
M2A4
M2A5
M2A6
M2A7
M2A8
M2A9
M2A10
M2A11

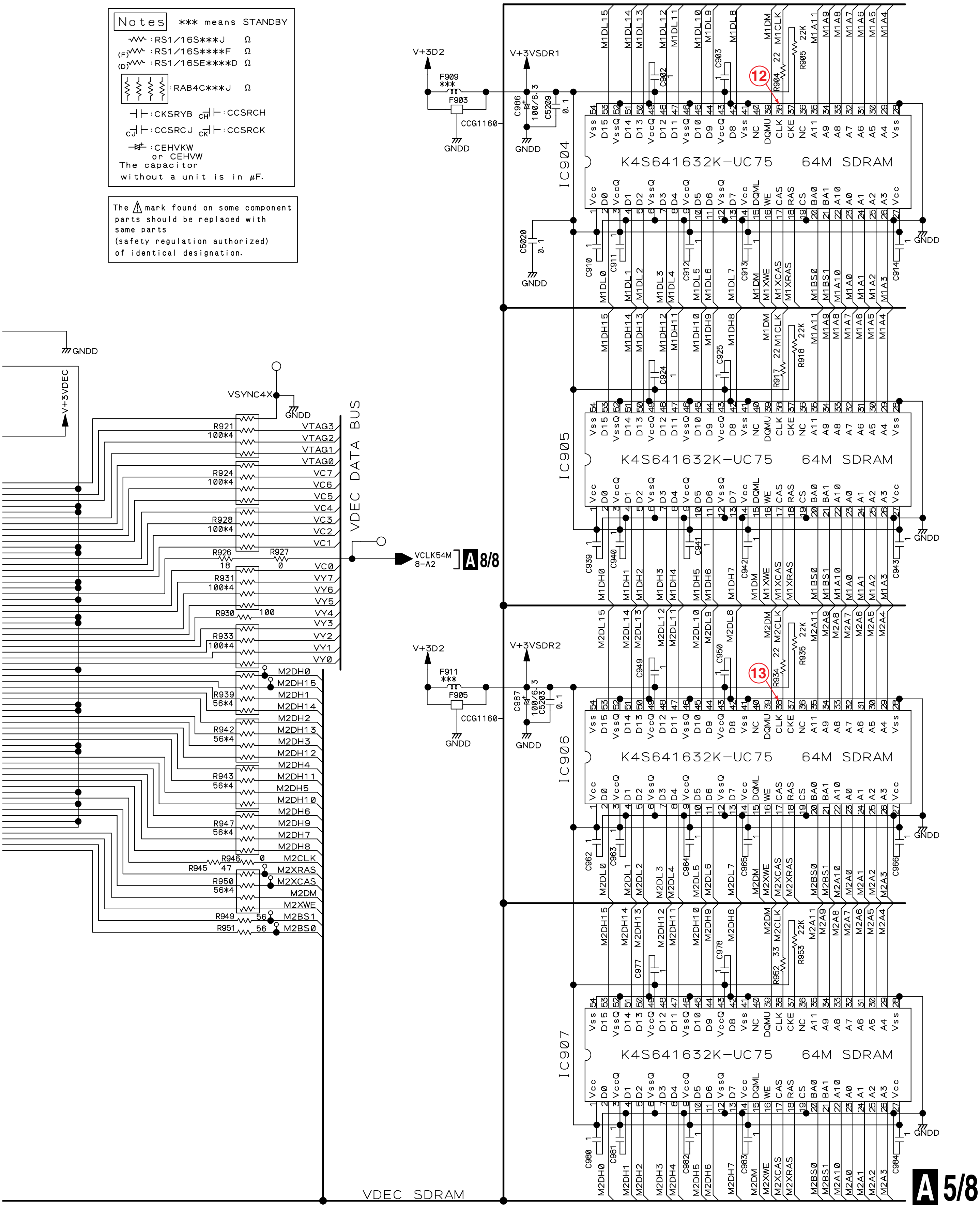
A 5/8

Notes *** means STANDBY

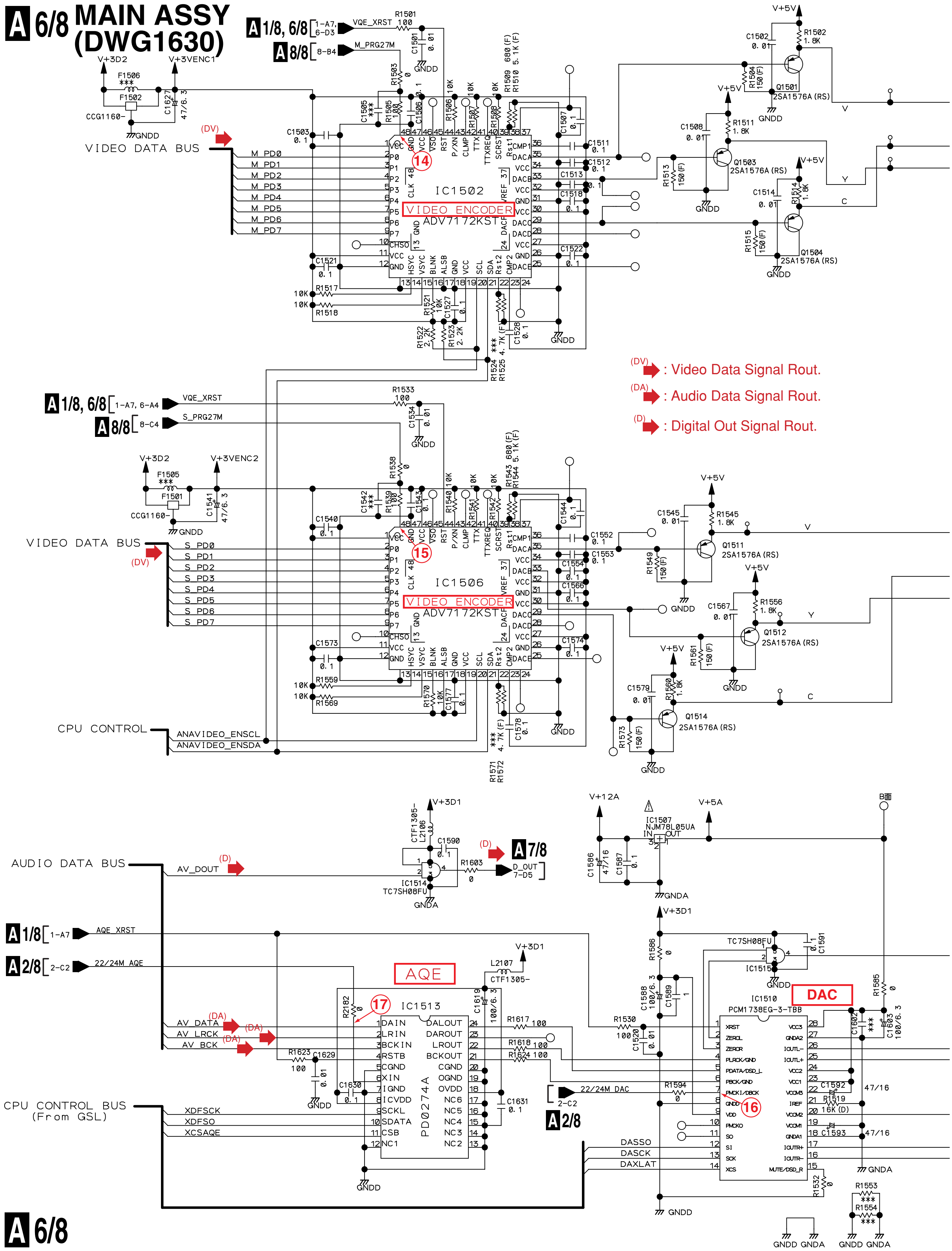
- :RS1/16S***J Ω
- (F) :RS1/16S****F Ω
- (D) :RS1/16SE****D Ω
- :RAB4C***J Ω
- :CKSRYB CH :CCSRCH
- CJ :CCSRCJ CK :CCSRCK
- :CEHVKW or CEHVW

The capacitor without a unit is in μF.

The ⚠ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.


V+3D2
F909
***
F903
CCG1160
GNDD
V+3VSDR1
C986
100/6.3
C5209
0.1
C902
C903
12
R904
22
M1CLK
R905
22K
IC904
K4S641632K-UC75 64M SDRAM
C5020
0.1
C910
C911
C912
C913
C914
M1DL0
M1DL15
M1DM
M1XWE
M1XCAS
M1XRAS
M1BS0
M1BS1
M1A10
M1A11
IC905
R917
R918
C924
C925
C939
C940
C941
C942
C943
M1DH0
M1DH15
GNDD
V+3VDEC
VSYNC4X
R921
100*4
VTAG3
VTAG2
VTAG1
VTAG0
R924
100*4
VC7
VC6
VC5
VC4
R928
100*4
VC3
VC2
VC1
R926
18
R927
0
VC0
VCLK54M
8-A2
A 8/8
R931
100*4
VY7
VY6
VY5
R930
100
VY4
VY3
R933
100*4
VY2
VY1
VY0
VDEC DATA BUS
R939
56*4
R942
56*4
R943
56*4
R947
56*4
R946
0
M2CLK
R945
47
M2XRAS
R950
56*4
M2XCAS
M2DM
M2XWE
R949
56
M2BS1
R951
56
M2BS0
M2DH0
M2DH15
V+3D2
F911
***
F905
CCG1160
V+3VSDR2
C987
100/6.3
C5203
0.1
C949
C950
13
R934
22
M2CLK
R935
22K
IC906
C962
C963
C964
C965
C966
M2DL0
M2DL15
IC907
R952
33
R953
22K
C977
C978
C980
C981
C982
C983
C984
VDEC SDRAM
Vss VssQ VccQ Vcc DQMU DQML CLK CKE CAS RAS WE CS BA0 BA1 NC
A 5/8

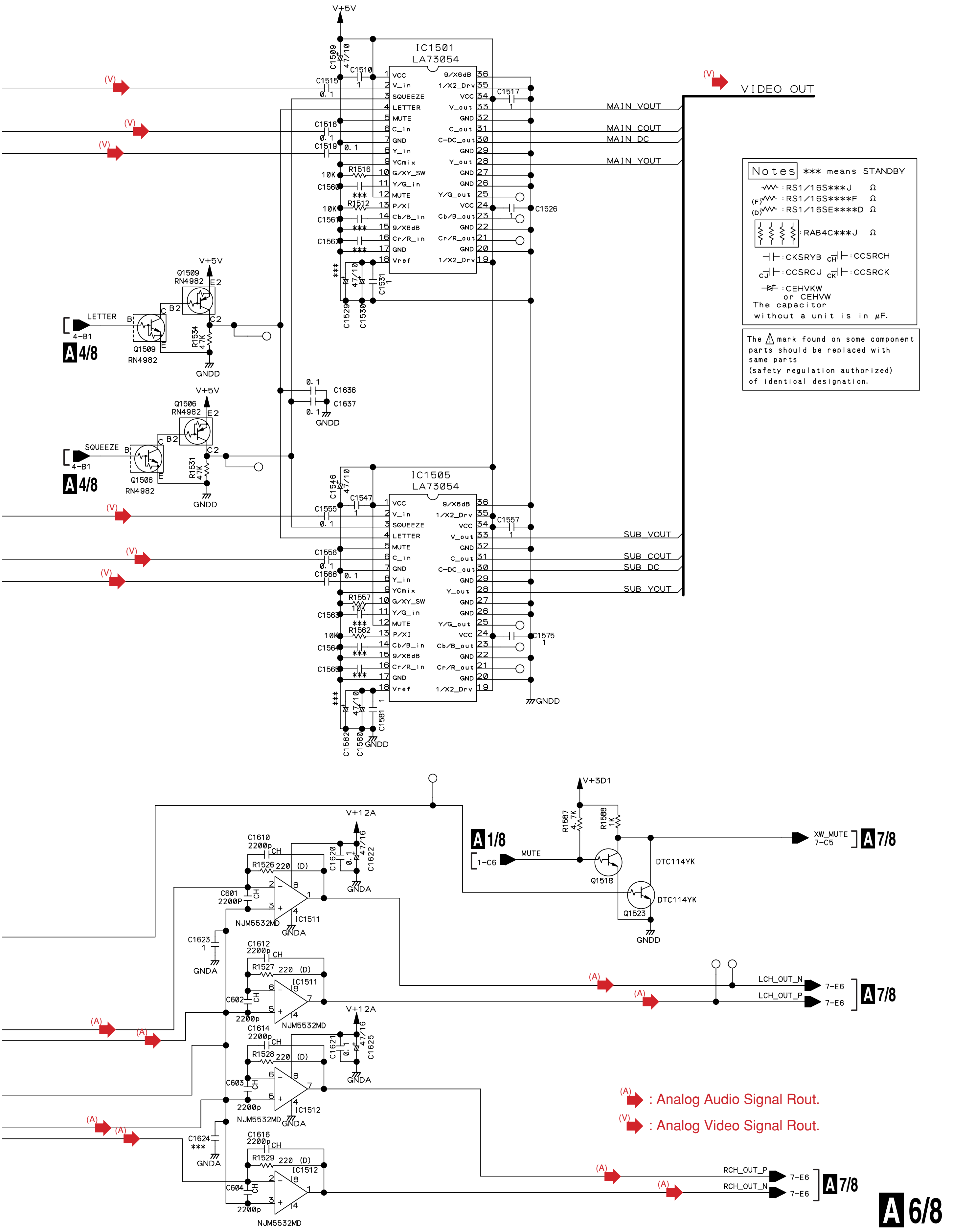
3.9 MAIN ASSY(6/8)


A 6/8 MAIN ASSY
(DWG1630)
A 1/8, 6/8 [1-A7, 6-D3
VQE_XRST
A 8/8 [8-B4
M_PRG27M
R1501 100
C1501 0.01
GNDD
V+3D2
V+3VENC1
F1506 ***
F1502
CCG1160-
C1527 47/6.3
VIDEO DATA BUS
(DV)
M_PD0
M_PD1
M_PD2
M_PD3
M_PD4
M_PD5
M_PD6
M_PD7
C1503 0.1
IC1502
VIDEO ENCODER
ADV7172KST
14
C1505 ***
R1505 100
C1506 0.1
R1503 0
R1506 10K
R1507 10K
R1508 10K
R1509 680 (F)
R1510 5.1K (F)
C1507 0.1
C1511 0.1
C1512 0.1
C1513 0.1
C1518 0.1
C1522 0.1
C1521 0.1
R1517 10K
R1518 10K
R1521 10K
R1522 2.2K
R1523 2.2K
C1527 0.1
C1528 0.1
R1524 ***
R1525 4.7K (F)
V+5V
C1502 0.01
R1502 1.8K
R1504 150 (F)
Q1501 2SA1576A (RS)
C1508 0.01
R1511 1.8K
R1513 150 (F)
Q1503 2SA1576A (RS)
C1514 0.01
R1514 1.8K
R1515 150 (F)
Q1504 2SA1576A (RS)
V
Y
C
A 1/8, 6/8 [1-A7, 6-A4
VQE_XRST
A 8/8 [8-C4
S_PRG27M
R1533 100
C1534 0.01
V+3VENC2
F1505 ***
F1501
C1541 47/6.3
S_PD0
S_PD1
S_PD2
S_PD3
S_PD4
S_PD5
S_PD6
S_PD7
C1540 0.1
IC1506
VIDEO ENCODER
ADV7172KST
15
R1538 0
C1542 ***
R1539 100
C1543 0.1
R1540 10K
R1541 10K
R1542 10K
R1543 680 (F)
R1544 5.1K (F)
C1544 0.1
C1552 0.1
C1553 0.1
C1554 0.1
C1566 0.1
C1574 0.1
C1573 0.1
R1559 10K
R1569 10K
R1570 10K
C1577 0.1
C1578 0.1
R1571 ***
R1572 4.7K (F)
C1545 0.01
R1545 1.8K
R1549 150 (F)
Q1511 2SA1576A (RS)
C1567 0.01
R1556 1.8K
R1561 150 (F)
Q1512 2SA1576A (RS)
C1579 0.01
R1560 1.8K
R1573 150 (F)
Q1514 2SA1576A (RS)
CPU CONTROL
ANAVIDEO_ENSCL
ANAVIDEO_ENSDA
V+3D1
CTF1305-
L2106
C1590 0.1
R1603 0
A 7/8
D_OUT 7-D5
IC1514 TC7SH08FU
GNDA
AUDIO DATA BUS
AV_DOUT
(D)
V+12A
V+5A
IC1507 NJM78L05UA
IN OUT
C1586 47/16
C1587 0.1
B面
A 1/8 [1-A7
AQE_XRST
A 2/8 [2-C2
22/24M_AQE
AQE
IC1513
PD0274A
17
L2107
CTF1305-
C1619 100/6.3
R2182 0
AV_DATA
AV_LRCK
AV_BCK
(DA)
R1623 100
C1629 0.01
C1630 0.1
C1631 0.1
R1617 100
R1618 100
R1624 100
DAIN
LRIN
BCKIN
RSTB
CGND
XIN
IGND
ICVDD
SCKL
SDATA
CSB
NC1
DALOUT
DAROUT
LROUT
BCKOUT
OGND
OVDD
NC6
NC5
NC4
NC3
NC2
CPU CONTROL BUS
(From GSL)
XDFSCK
XDFSO
XCSAQE
22/24M_DAC
A 2/8
2-C2
R1594 0
R1530 100
C1520 0.01
R1586 0
C1588 100/6.3
C1589 1
TC7SH08FU
IC1515
C1591 0.1
DAC
IC1510
PCM1738EG-3-TBB
16
XRST
ZEROL
ZEROR
PLRCK/GND
PDATA/DSD_L
PBCK/GND
PMCK1/DBCK
GNDD
VDD
PMCKO
SO
SI
SCK
XCS
VCC3
GNDA2
IOUTL-
IOUTL+
VCC2
VCC1
VCOMS
IREF
VCOM2
VCOM1
GNDA1
IOUTR+
IOUTR-
MUTE/DSD_R
C1602 ***
C1603 100/6.3
R1585 0
C1592 47/16
R1519 16K (D)
C1593 47/16
DASSO
DASCK
DAXLAT
R1532 0
R1553 ***
R1554 ***
GNDD GNDA
A 6/8

(DV) : Video Data Signal Rout.

(DA) : Audio Data Signal Rout.

(D) : Digital Out Signal Rout.


V+5V
IC1501
LA73054
C1509 47/10
C1510
C1515 0.1
C1516 0.1
C1519 0.1
R1516 10K
C1560
R1512 10K
C1561
C1562
C1529
C1530
C1531
C1517
C1526
1 VCC
2 V_in
3 SQUEEZE
4 LETTER
5 MUTE
6 C_in
7 GND
8 Y_in
9 YCmix
10 G/XY_SW
11 Y/G_in
12 MUTE
13 P/XI
14 Cb/B_in
15 9/X6dB
16 Cr/R_in
17 GND
18 Vref
36 9/X6dB
35 1/X2_Drv
34 VCC
33 V_out
32 GND
31 C_out
30 C-DC_out
29 GND
28 Y_out
27 GND
26 GND
25 Y/G_out
24 VCC
23 Cb/B_out
22 GND
21 Cr/R_out
20 GND
19 1/X2_Drv
(V)
VIDEO OUT
MAIN VOUT
MAIN COUT
MAIN DC
MAIN YOUT
Q1509 RN4982
LETTER 4-B1
A 4/8
R1534 47K
GNDD
C1636 0.1
C1637 0.1
Q1506 RN4982
SQUEEZE 4-B1
R1531 47K
IC1505
LA73054
C1546 47/10
C1547
C1555 0.1
C1556 0.1
C1568 0.1
R1557 10K
C1563
R1562 10K
C1564
C1565
C1582
C1580
C1581
C1557
C1575
SUB VOUT
SUB COUT
SUB DC
SUB YOUT
V+12A
C1610 2200p CH
R1526 220 (D)
C1620 0.1
C1622 47/16
C601 2200P CH
IC1511
NJM5532MD
GNDA
C1623 1
C1612 2200p CH
R1527 220 (D)
C602 2200p CH
C1614 2200p CH
R1528 220 (D)
C1621 0.1
C1625 47/16
C603 2200p CH
IC1512
C1624 ***
C1616 2200p CH
R1529 220 (D)
C604 2200p CH
V+3D1
R1587 4.7K
R1588 1K
A 1/8
MUTE 1-C6
Q1518 DTC114YK
Q1523 DTC114YK
XW_MUTE 7-C5
A 7/8
(A)
LCH_OUT_N 7-E6
LCH_OUT_P 7-E6
RCH_OUT_P 7-E6
RCH_OUT_N 7-E6

Notes *** means STANDBY

: RS1/16S***J Ω

(F) : RS1/16S****F Ω

(D) : RS1/16SE****D Ω

: RAB4C***J Ω

: CKSRYB CH : CCSRCH

CJ : CCSRCJ CK : CCSRCK

: CEHVKW or CEHVW

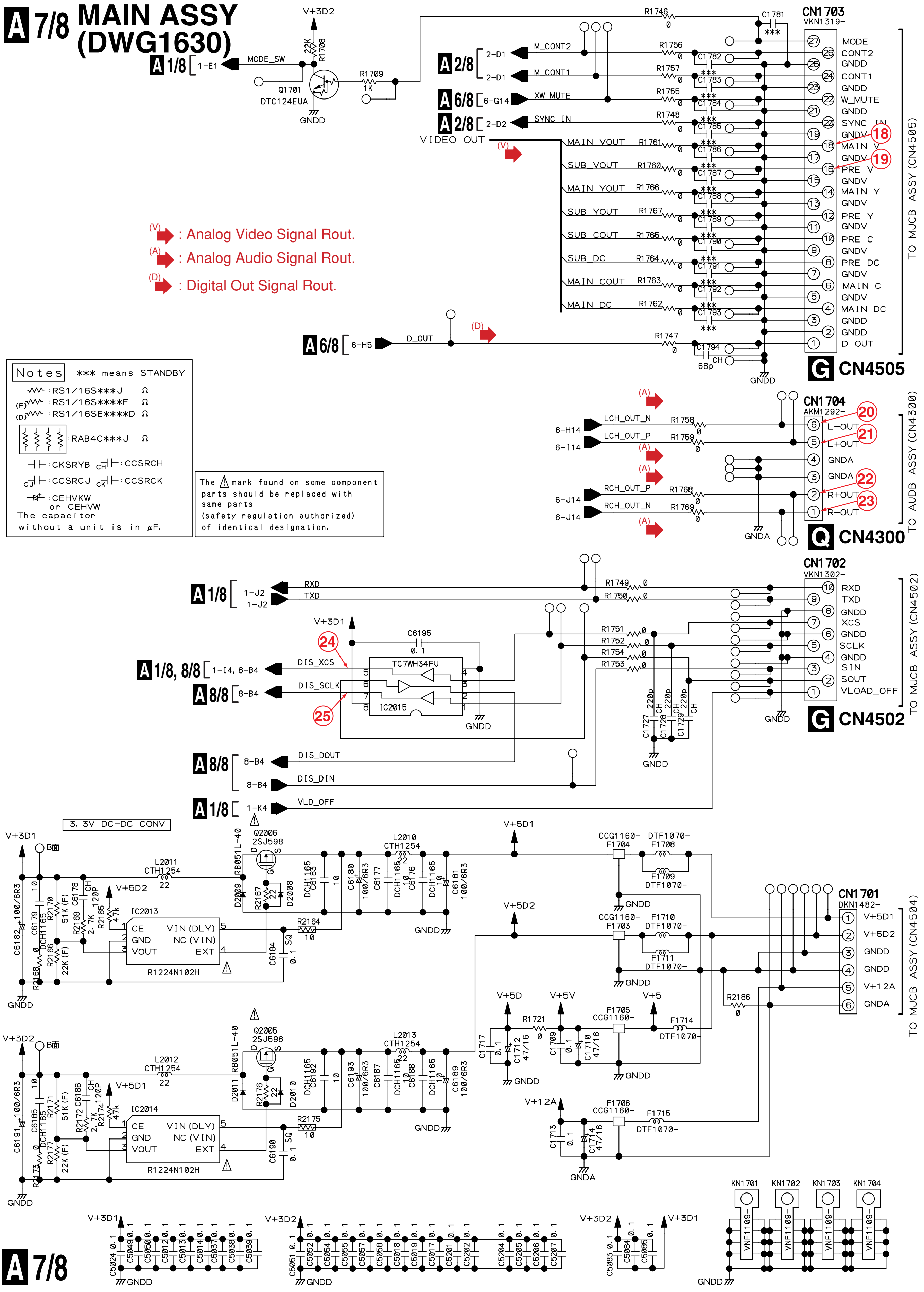
The capacitor without a unit is in μF.

The ⚠ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.

(A) : Analog Audio Signal Rout.

(V) : Analog Video Signal Rout.

3.10 MAIN ASSY(7/8)

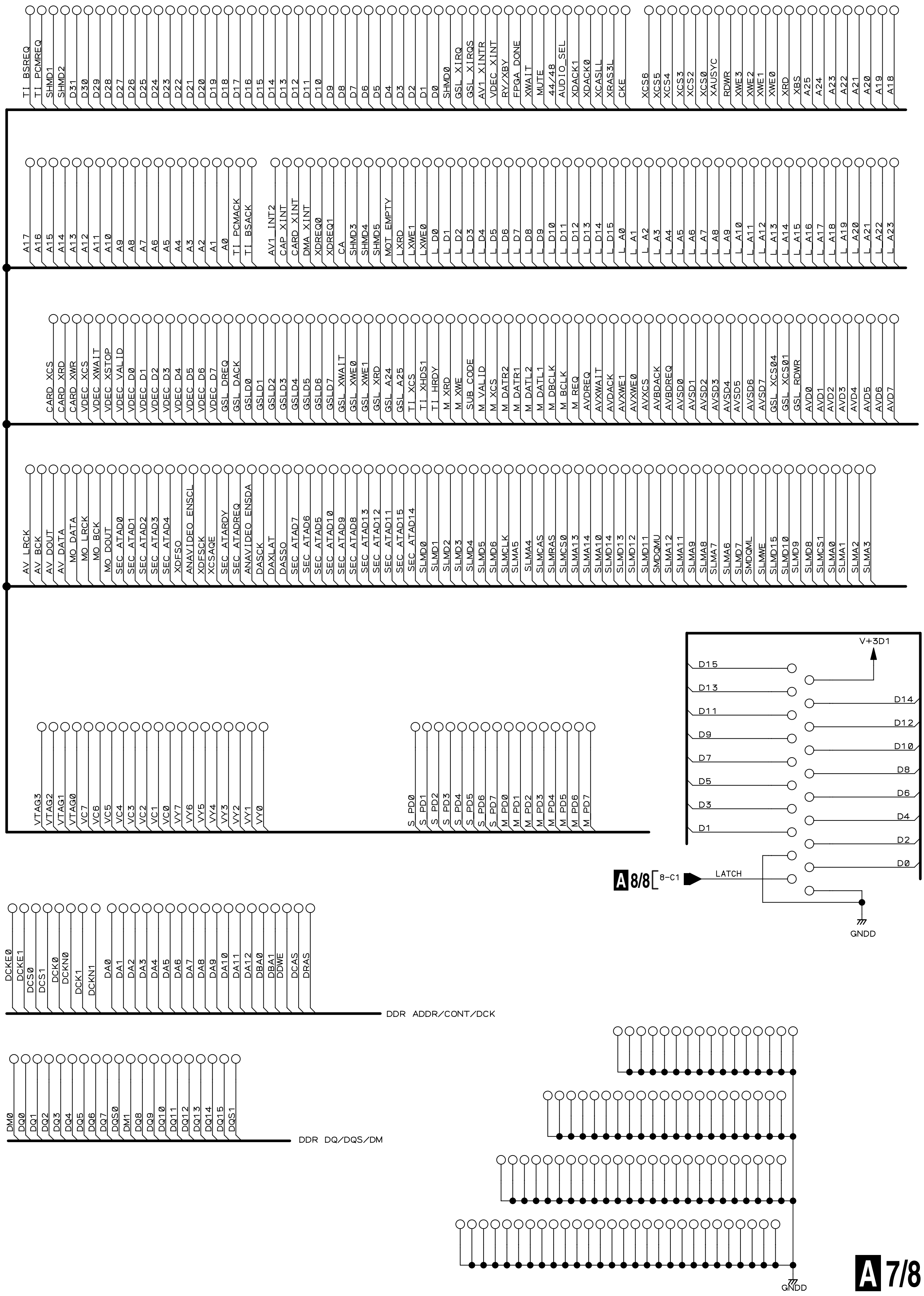


A 7/8 MAIN ASSY (DWG1630)
A 1/8 1-E1 MODE_SW
V+3D2
R1708 22K
Q1701 DTC124EUA
GNDD
R1709 1K
A 2/8 2-D1 M_CONT2
A 2/8 2-D1 M_CONT1
A 6/8 6-G14 XW_MUTE
A 2/8 2-D2 SYNC_IN
VIDEO OUT
(V)
MAIN_VOUT R1761
SUB_VOUT R1760
MAIN_YOUT R1766
SUB_YOUT R1767
SUB_COUT R1765
SUB_DC R1764
MAIN_COUT R1763
MAIN_DC R1762
R1746 0
R1756 0
R1757 0
R1755 0
R1748 0
C1781 ***
C1782 ***
C1783 ***
C1784 ***
C1785 ***
C1786 ***
C1787 ***
C1788 ***
C1789 ***
C1790 ***
C1791 ***
C1792 ***
C1793 ***
A 6/8 6-H5 D_OUT
(D)
R1747 0
C1794 CH 68p
GNDD
CN1703 VKN1319-
MODE
CONT2
GNDD
CONT1
GNDD
W_MUTE
GNDD
SYNC_IN
GNDV
MAIN V
GNDV
PRE V
GNDV
MAIN Y
GNDV
PRE Y
GNDV
PRE C
GNDV
PRE DC
GNDV
MAIN C
GNDV
MAIN DC
GNDD
GNDD
D OUT
18
19
G CN4505
TO MJCB ASSY (CN4505)
(V) : Analog Video Signal Rout.
(A) : Analog Audio Signal Rout.
(D) : Digital Out Signal Rout.
6-H14 LCH_OUT_N R1758 0
6-I14 LCH_OUT_P R1759 0
6-J14 RCH_OUT_P R1768 0
6-J14 RCH_OUT_N R1769 0
(A)
CN1704 AKM1292-
L-OUT
L+OUT
GNDA
GNDA
R+OUT
R-OUT
20
21
22
23
GNDA
Q CN4300
TO AUDB ASSY (CN4300)
A 1/8 1-J2 RXD
1-J2 TXD
R1749 0
R1750 0
V+3D1
C6195 0.1
24
25
A 1/8, 8/8 1-I4, 8-B4 DIS_XCS
A 8/8 8-B4 DIS_SCLK
TC7WH34FU
IC2015
GNDD
R1751 0
R1752 0
R1754 0
R1753 0
C1727 220p CH
C1728 220p CH
C1729 220p CH
GNDD
CN1702 VKN1302-
RXD
TXD
GNDD
XCS
GNDD
SCLK
GNDD
SIN
SOUT
VLOAD_OFF
G CN4502
TO MJCB ASSY (CN4502)
A 8/8 8-B4 DIS_DOUT
8-B4 DIS_DIN
A 1/8 1-K4 VLD_OFF
3.3V DC-DC CONV
V+3D1
B
L2011 CTH1254 22
V+5D2
C6182 100/6R3
C6179 10
DCH1165
R2170 51K (F)
R2169 2.7K
C6178 120P CH
R2165 47k
R2166 22K (F)
R2168 0
IC2013
CE VIN(DLY)
GND NC(VIN)
VOUT EXT
R1224N102H
R2164 10
C6184 0.1 SQ
D2009 RB051L-40
Q2006 2SJ598
R2167 22
D2008
C6183 DCH1165
C6180 10
C6177 100/6R3
C6176 DCH1165 10
C6181 DCH1165 10
100/6R3
L2010 CTH1254 22
GNDD
V+5D1
V+3D2
L2012 CTH1254 22
V+5D1
C6191 100/6R3
C6185 10
DCH1165
R2171 51K (F)
R2172 2.7K
C6186 120P CH
R2174 47k
R2177 22K (F)
R2173 0
IC2014
R1224N102H
R2175 10
C6190 0.1 SQ
D2011 RB051L-40
Q2005 2SJ598
R2176 22
D2010
C6192 DCH1165
C6193 10
C6187 100/6R3
C6188 DCH1165 10
C6189 DCH1165 10
100/6R3
L2013 CTH1254 22
GNDD
V+5D1
CCG1160- F1704
DTF1070- F1708
F1709 DTF1070-
GNDD
V+5D2
CCG1160- F1703
F1710 DTF1070-
F1711 DTF1070-
GNDD
V+5D V+5V V+5
R1721 0
C1717 0.1
C1712 47/16
C1709 0.1
C1710 47/16
F1705 CCG1160-
F1714 DTF1070-
GNDD
GNDD
V+12A
F1706 CCG1160-
F1715 DTF1070-
C1713 0.1
C1714 47/16
GNDA
R2186 0
CN1701 DKN1482-
V+5D1
V+5D2
GNDD
GNDD
V+12A
GNDA
TO MJCB ASSY (CN4504)
V+3D1
C5024 0.1
C5049 0.1
C5050 0.1
C5012 0.1
C5013 0.1
C5014 0.1
C5037 0.1
C5038 0.1
C5039 0.1
GNDD
V+3D2
C5051 0.1
C5052 0.1
C5054 0.1
C5055 0.1
C5057 0.1
C5058 0.1
C5018 0.1
C5019 0.1
C5017 0.1
C5201 0.1
C5202 0.1
C5204 0.1
C5205 0.1
C5206 0.1
C5207 0.1
GNDD
V+3D2 V+3D1
C5083 0.1
C5084 0.1
C5085 0.1
KN1701 KN1702 KN1703 KN1704
VNF1109-
GNDD
A 7/8
A B C D E F

Notes *** means STANDBY

:RS1/16S***J Ω
(F) :RS1/16S****F Ω
(D) :RS1/16SE****D Ω
:RAB4C***J Ω
:CKSRYB CH :CCSRCH
CJ :CCSRCJ CK :CCSRCK
:CEHVKW
or CEHVW
The capacitor
without a unit is in µF.

The ⚠ mark found on some component
parts should be replaced with
same parts
(safety regulation authorized)
of identical designation.

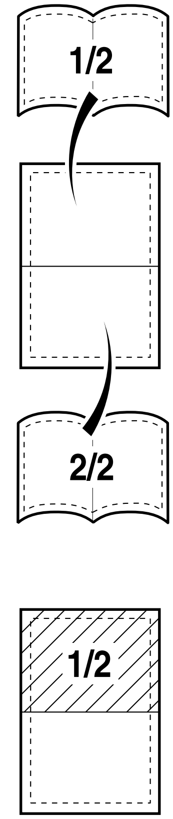

TI_BSREQ
TI_PCMREQ
SHMD1
SHMD2
D31
D30
D29
D28
D27
D26
D25
D24
D23
D22
D21
D20
D19
D18
D17
D16
D15
D14
D13
D12
D11
D10
D9
D8
D7
D6
D5
D4
D3
D2
D1
D0
SHMD0
GSL_XIRQ
GSL_XIRQS
AV1_XINTR
VDEC_XINT
RY/XBY
FPGA_DONE
XWAIT
MUTE
44/48
AUDIO_SEL
XDACK1
XDACK0
XCASLL
XRAS3L
CKE
XCS6
XCS5
XCS4
XCS3
XCS2
XCS0
XAUSYC
RDWR
XWE3
XWE2
XWE1
XWE0
XRD
XBS
A25
A24
A23
A22
A21
A20
A19
A18
A17
A16
A15
A14
A13
A12
A11
A10
A9
A8
A7
A6
A5
A4
A3
A2
A1
A0
TI_PCMACK
TI_BSACK
AV1_INT2
CAP_XINT
CARD_XINT
DMA_XINT
XDREQ0
XDREQ1
CA
SHMD3
SHMD4
SHMD5
MOT_EMPTY
LXRD
LXWE1
LXWE0
L_D0
L_D1
L_D2
L_D3
L_D4
L_D5
L_D6
L_D7
L_D8
L_D9
L_D10
L_D11
L_D12
L_D13
L_D14
L_D15
L_A0
L_A1
L_A2
L_A3
L_A4
L_A5
L_A6
L_A7
L_A8
L_A9
L_A10
L_A11
L_A12
L_A13
L_A14
L_A15
L_A16
L_A17
L_A18
L_A19
L_A20
L_A21
L_A22
L_A23
CARD_XCS
CARD_XRD
CARD_XWR
VDEC_XCS
VDEC_XWAIT
VDEC_XSTOP
VDEC_VALID
VDEC_D0
VDEC_D1
VDEC_D2
VDEC_D3
VDEC_D4
VDEC_D5
VDEC_D6
VDEC_D7
GSL_DREQ
GSL_DACK
GSLD0
GSLD1
GSLD2
GSLD3
GSLD4
GSLD5
GSLD6
GSLD7
GSL_XWAIT
GSL_XWE0
GSL_XWE1
GSL_XRD
GSL_A24
GSL_A25
TI_XCS
TI_XHDS1
TI_HRDY
M_XRD
M_XWE
SUB_CODE
M_VALID
M_XCS
M_DATR2
M_DATR1
M_DATL2
M_DATL1
M_DBCLK
M_BCLK
M_REQ
AVDREQ
AVXWAIT
AVDACK
AVXWE1
AVXWE0
AVXCS
AVBDACK
AVBDREQ
AVSD0
AVSD1
AVSD2
AVSD3
AVSD4
AVSD5
AVSD6
AVSD7
GSL_XCS04
GSL_XCS01
GSL_RDWR
AVD0
AVD1
AVD2
AVD3
AVD4
AVD5
AVD6
AVD7
AV_LRCK
AV_BCK
AV_DOUT
AV_DATA
MO_DATA
MO_LRCK
MO_BCK
MO_DOUT
SEC_ATAD0
SEC_ATAD1
SEC_ATAD2
SEC_ATAD3
SEC_ATAD4
XDFSO
ANAVIDEO_ENSCL
XDFSCK
XCSAQE
SEC_ATARDY
SEC_ATADREQ
ANAVIDEO_ENSDA
DASCK
DAXLAT
DASSO
SEC_ATAD7
SEC_ATAD6
SEC_ATAD5
SEC_ATAD10
SEC_ATAD9
SEC_ATAD8
SEC_ATAD13
SEC_ATAD12
SEC_ATAD11
SEC_ATAD15
SEC_ATAD14
SLMD0
SLMD1
SLMD2
SLMD3
SLMD4
SLMD5
SLMD6
SLMCLK
SLMA5
SLMA4
SLMCAS
SLMRAS
SLMCS0
SLMA13
SLMA14
SLMA10
SLMD14
SLMD13
SLMD12
SLMD11
SMDQMU
SLMA12
SLMA11
SLMA9
SLMA8
SLMA7
SLMA6
SLMD7
SMDQML
SLMWE
SLMD15
SLMD10
SLMD9
SLMD8
SLMCS1
SLMA0
SLMA1
SLMA2
SLMA3
VTAG3
VTAG2
VTAG1
VTAG0
VC7
VC6
VC5
VC4
VC3
VC2
VC1
VC0
VY7
VY6
VY5
VY4
VY3
VY2
VY1
VY0
S_PD0
S_PD1
S_PD2
S_PD3
S_PD4
S_PD5
S_PD6
S_PD7
M_PD0
M_PD1
M_PD2
M_PD3
M_PD4
M_PD5
M_PD6
M_PD7
V+3D1
D15
D13
D11
D9
D7
D5
D3
D1
D14
D12
D10
D8
D6
D4
D2
D0
A 8/8 8-C1
LATCH
GNDD
DCKE0
DCKE1
DCS0
DCS1
DCK0
DCKN0
DCK1
DCKN1
DA0
DA1
DA2
DA3
DA4
DA5
DA6
DA7
DA8
DA9
DA10
DA11
DA12
DBA0
DBA1
DDWE
DCAS
DRAS
DDR ADDR/CONT/DCK
DM0
DQ0
DQ1
DQ2
DQ3
DQ4
DQ5
DQ6
DQ7
DQS0
DM1
DQ8
DQ9
DQ10
DQ11
DQ12
DQ13
DQ14
DQ15
DQS1
DDR DQ/DQS/DM
GNDD
5
6
7
8
A
B
C
D
E
F

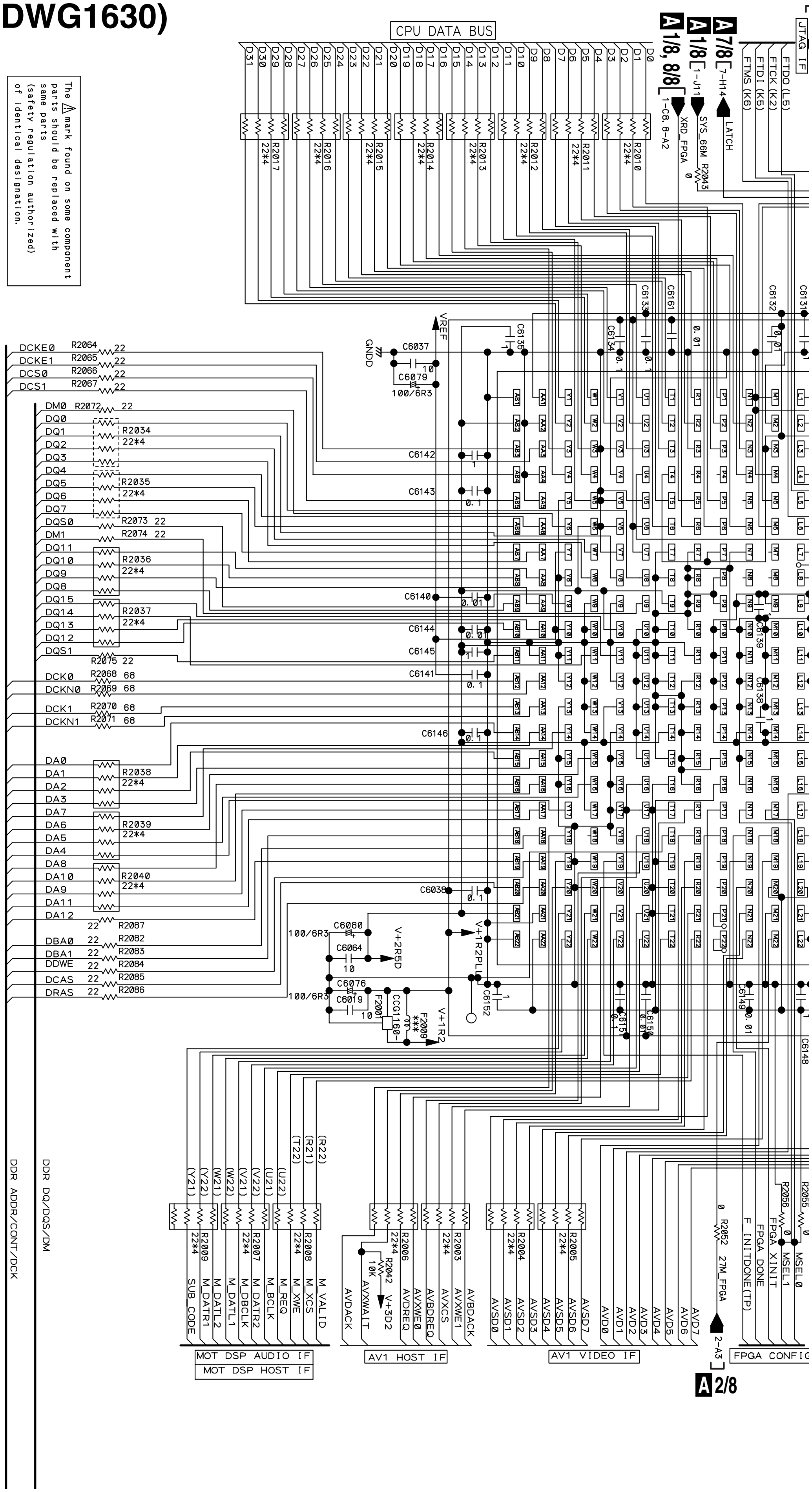
A 7/8

3.11 MAIN ASSY(8/8)

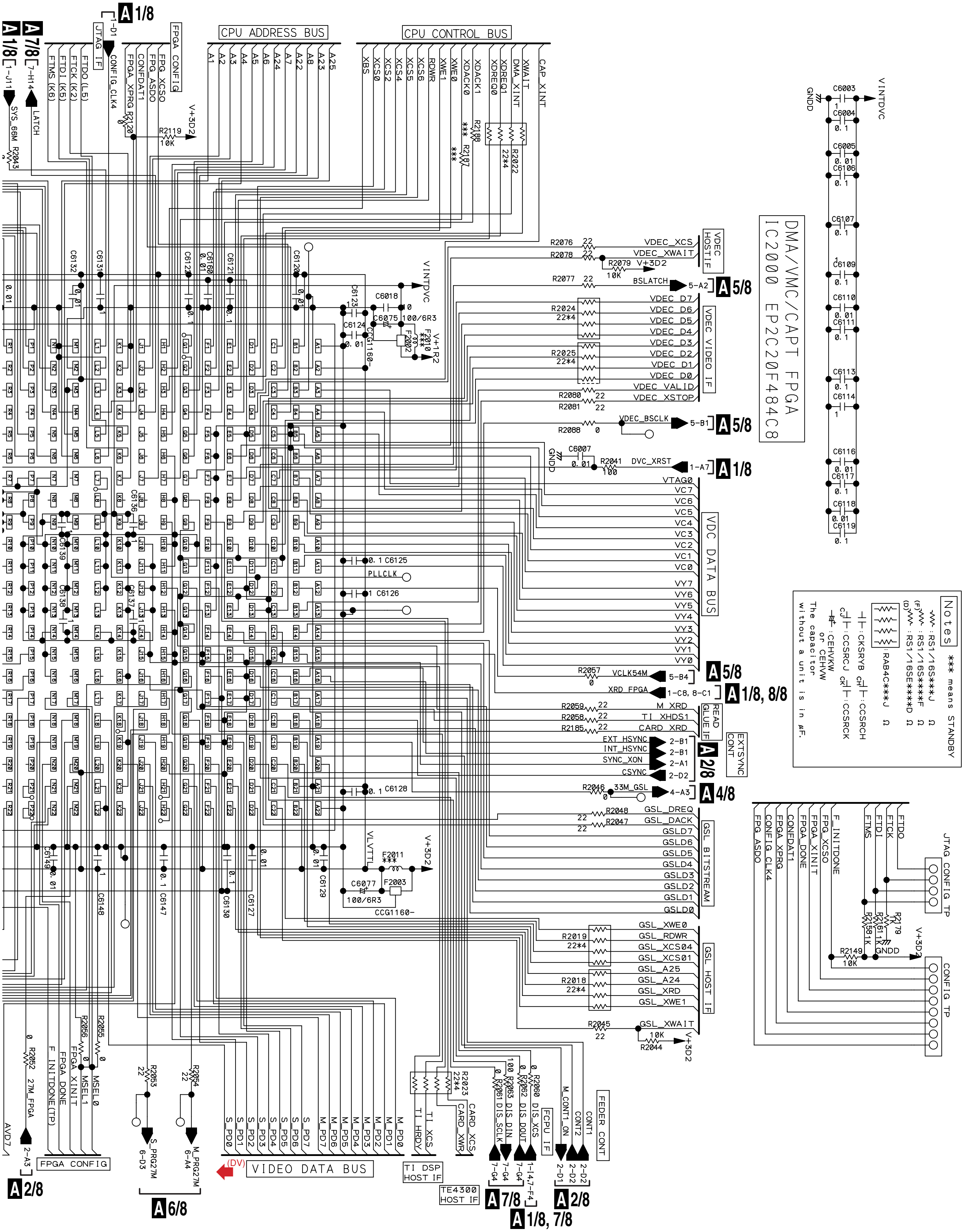
A 8/8 MAIN ASSY(DWG1630)

Large size SCH diagram



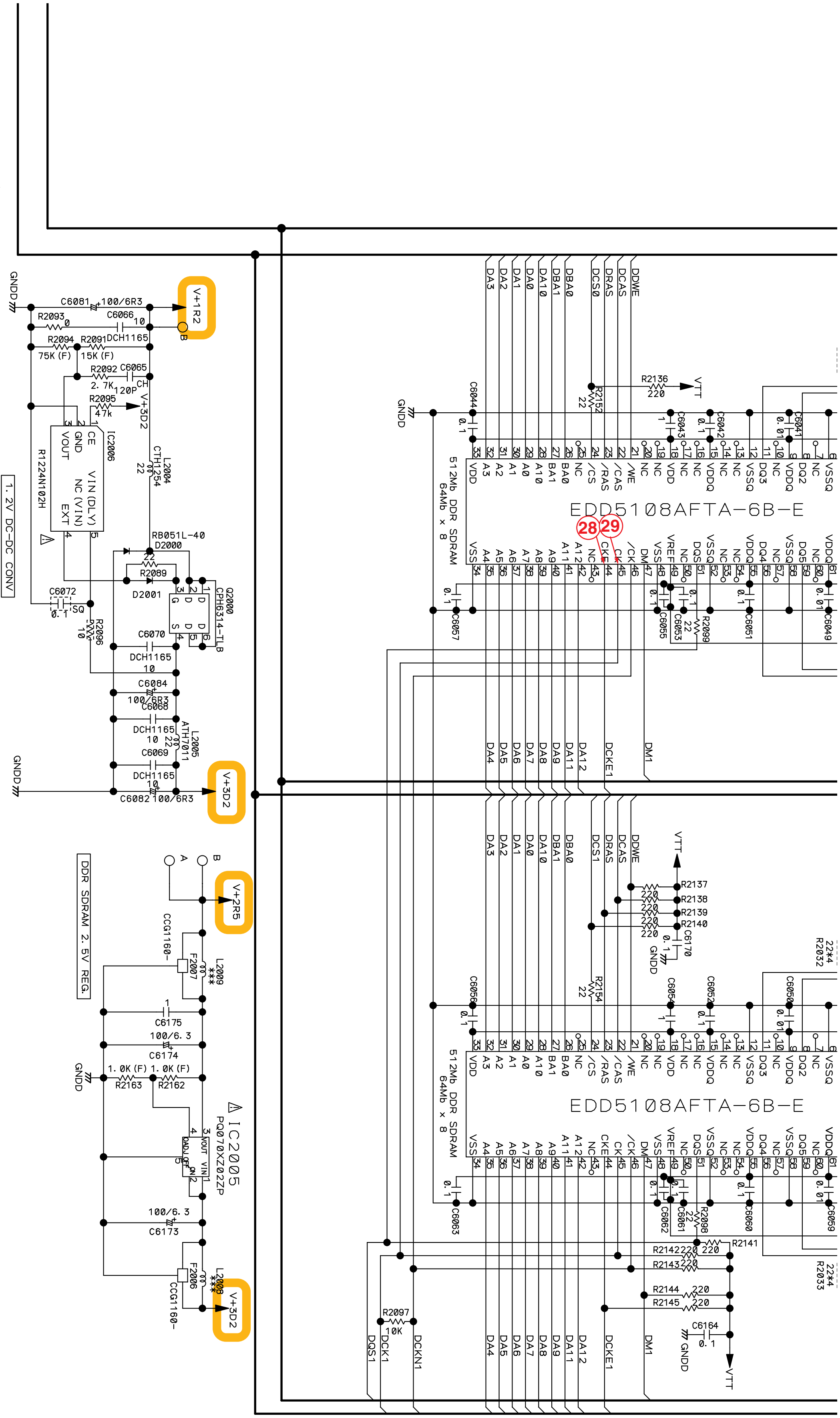

The ⚠ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.
CPU DATA BUS
D31 D30 D29 D28 D27 D26 D25 D24 D23 D22 D21 D20 D19 D18 D17 D16 D15 D14 D13 D12 D11 D10 D9 D8 D7 D6 D5 D4 D3 D2 D1 D0
R2017 22*4
R2016 22*4
R2015 22*4
R2014 22*4
R2013 22*4
R2012 22*4
R2011 22*4
R2010 22*4
A 1/8, 8/8 1-C8, 8-A2 XRD_FPGA
A 1/8 1-J11 SYS_66M R2043 0
A 7/8 7-H14 LATCH
JTAG IF
FTDO (L5)
FTCK (K2)
FTDI (K5)
FTMS (K6)
VREF
GNDD
C6037
C6079 10 100/6R3
C6135
C6134 0.1
C6133 0.1
C6161 0.01
C6132 0.01
C6131 1
DCKE0 R2064 22
DCKE1 R2065 22
DCS0 R2066 22
DCS1 R2067 22
DM0 R2072 22
DQ0 DQ1 DQ2 DQ3 R2034 22*4
DQ4 DQ5 DQ6 DQ7 R2035 22*4
DQS0 R2073 22
DM1 R2074 22
DQ11 DQ10 DQ9 DQ8 R2036 22*4
DQ15 DQ14 DQ13 DQ12 R2037 22*4
DQS1 R2075 22
DCK0 R2068 68
DCKN0 R2069 68
DCK1 R2070 68
DCKN1 R2071 68
DA0 DA1 DA2 DA3 R2038 22*4
DA7 DA6 DA5 DA4 R2039 22*4
DA8 DA10 DA9 DA11 R2040 22*4
DA12 22 R2087
DBA0 22 R2082
DBA1 22 R2083
DDWE 22 R2084
DCAS 22 R2085
DRAS 22 R2086
C6142 C6143 0.1 C6140 0.01 C6144 0.01 C6145 1 C6141 0.1 C6146 0.1 C6038 0.1
C6139 C6138
C6080 100/6R3 C6064 10 V+2R5D
C6076 100/6R3 C6019 10 F2001 CCG1160- V+1R2PLL C6152 1 F2009 V+1R2
C6151 0.1 C6150 0.01 C6149 0.01 C6148 1
DDR ADDR/CONT/DCK
DDR DQ/DQS/DM
(Y21) (Y22) (W21) (W22) (V21) (V22) (U21) (U22) (T22) (R21) (R22)
R2009 22*4 R2007 22*4 R2008 22*4
SUB_CODE M_DATR1 M_DATL2 M_DATL1 M_DBCLK M_DATR2 M_BCLK M_REQ M_XWE M_XCS M_VALID
MOT DSP AUDIO IF
MOT DSP HOST IF
R2042 10K V+3D2 AVXWAIT AVDACK
R2006 22*4 R2003 22*4
AVDREQ AVXWE0 AVBDREQ AVXCS AVXWE1 AVBDACK
AV1 HOST IF
R2004 22*4 R2005 22*4
AVSD0 AVSD1 AVSD2 AVSD3 AVSD4 AVSD5 AVSD6 AVSD7
AVD0 AVD1 AVD2 AVD3 AVD4 AVD5 AVD6 AVD7
AV1 VIDEO IF
R2052 0 27M_FPGA
2-A3 A 2/8
F_INITDONE(TP) FPGA_DONE FPGA_XINIT R2056 0 MSEL1 R2055 0 MSEL0
FPGA CONFIG

(DV) ➡ : Video Data Signal Rout.

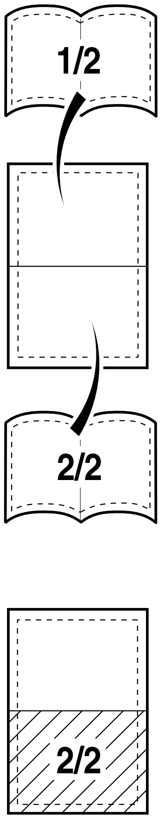

A 1/8
A 7/8
A 1/8
JTAG IF
FPGA CONFIG
CPU ADDRESS BUS
CPU CONTROL BUS
FTMS (K6)
FTDI (K5)
FTCK (K2)
FTDO (L5)
CONFIG_CLK4
FPGA_XPRG
CONFDAT1
FPG_ASDO
FPG_XCSO
A1 A2 A3 A4 A5 A6 A24 A7 A22 A8 A23 A25
XBS XCS0 XCS2 XCS4 XCS5 XCS6 RDWR XWE1 XWE0 XDACK0 XDACK1 XDREQ0 XDREQ1 DMA_XINT XWAIT CAP XINT
SYS_66M
LATCH
R2043
R2120
R2119 10K
V+3D2
R2188 ***
R2187 ***
R2022 22*4
DMA/VMC/CAPT FPGA
IC2000 EP2C20F484C8
VDEC HOSTIF
VDEC VIDEO IF
VDC DATA BUS
READ GLUE IF
EXTSYNC CONT
GSL BITSTREAM
GSL HOST IF
R2076 22 VDEC_XCS
R2078 22 VDEC_XWAIT
R2079 10K V+3D2
R2077 22 BSLATCH 5-A2
A 5/8
VDEC_D7 VDEC_D6 VDEC_D5 VDEC_D4 VDEC_D3 VDEC_D2 VDEC_D1 VDEC_D0
R2024 22*4
R2025 22*4
R2080 22 VDEC_VALID
R2081 22 VDEC_XSTOP
R2088 0 VDEC_BSCLK 5-B1
A 5/8
C6007 0.01 GNDD
R2041 100 DVC_XRST 1-A7
A 1/8
VTAG0 VC7 VC6 VC5 VC4 VC3 VC2 VC1 VC0
VY7 VY6 VY5 VY4 VY3 VY2 VY1 VY0
R2057 0 VCLK54M 5-B4
A 5/8
XRD_FPGA 1-C8, 8-C1
A 1/8, 8/8
R2059 22 M_XRD
R2058 22 TI_XHDS1
R2185 22 CARD_XRD
EXT_HSYNC 2-B1
INT_HSYNC 2-B1
SYNC_XON 2-A1
CSYNC 2-D2
A 2/8
R2046 0 33M_GSL 4-A3
A 4/8
R2048 22 GSL_DREQ
R2047 22 GSL_DACK
GSLD7 GSLD6 GSLD5 GSLD4 GSLD3 GSLD2 GSLD1 GSLD0
R2019 22*4
R2018 22*4
GSL_XWE0 GSL_RDWR GSL_XCS04 GSL_XCS01 GSL_A25 GSL_A24 GSL_XRD GSL_XWE1
R2045 22 GSL_XWAIT
R2044 10K V+3D2
VINTDVC
C6018 0
C6075 100/6R3
C6123 123
C6124 0.01
CCG1160
F2002
F2010 ***
V+1R2
C6125 0.1
PLLCLK
C6126 1
C6128 0.1
VLVTTL
F2011 ***
V+3D2
C6077 100/6R3
F2003
CCG1160-
C6129 0.01
C6127 0.01
C6130 0.1
C6147 0.1
C6148 1
C6149 0.01
C6132 0.01
C6131 1
C6122 0.1
C6160 0.01
C6121 0.1
C6120 0.01
C6136
C6139
C6138
C6137
C6003 1 C6004 0.1 C6005 0.01 C6106 0.1 C6107 0.1 C6109 0.1 C6110 0.01 C6111 0.1 C6113 0.1 C6114 1 C6116 0.01 C6117 0.1 C6118 0.01 C6119 0.1
GNDD
VINTDVC
R2052 0 27M_FPGA 2-A3
A 2/8
AVDD7
R2056 F_INITDONE(TP) FPGA_DONE FPGA_XINIT
R2055 0 MSEL1 MSEL0
FPGA CONFIG
R2053 22 S_PRG27M 6-D3
R2054 22 M_PRG27M 6-A4
A 6/8
S_PD0 S_PD1 S_PD2 S_PD3 S_PD4 S_PD5 S_PD6 S_PD7 M_PD7 M_PD6 M_PD5 M_PD4 M_PD3 M_PD2 M_PD1 M_PD0
(DV) VIDEO DATA BUS
TI HRDY TI XCS
TI DSP HOST IF
R2023 22*4
CARD_XWR CARD_XCS
TE4300 HOST IF
R2061 0 DIS_SCLK 7-G4
R2063 100 DIS_DIN 7-G4
R2062 0 DIS_DOUT 1-I4,7-F4
R2060 0 DIS_XCS
FCPU IF
A 7/8
A 1/8, 7/8
M_CONT1_ON CONT2 CONT1 2-D1 2-D2 2-D2
FEDER CONT
A 2/8
FTDO FTCK FTDI FTMS F_INITDONE FPG_XCSO FPGA_XINIT FPGA_DONE CONFDAT1 FPGA_XPRG CONFIG_CLK4 FPG_ASDO
JTAG CONFIG TP
R2179 1K
R2181 1K
R2150 5.6K
R2149 10K GNDD V+3D2
CONFIG TP
A B C D E F

Notes *** means STANDBY
-ww- :RS1/16S***J Ω
(F) :RS1/16S***F Ω
(D) :RS1/16SE***D Ω
RAB4C***J Ω
:CKSRYB :CCSRCH
:CCSRCJ :CCSRCK
:CEHVKW or CEHVW
The capacitor without a unit is in μF.

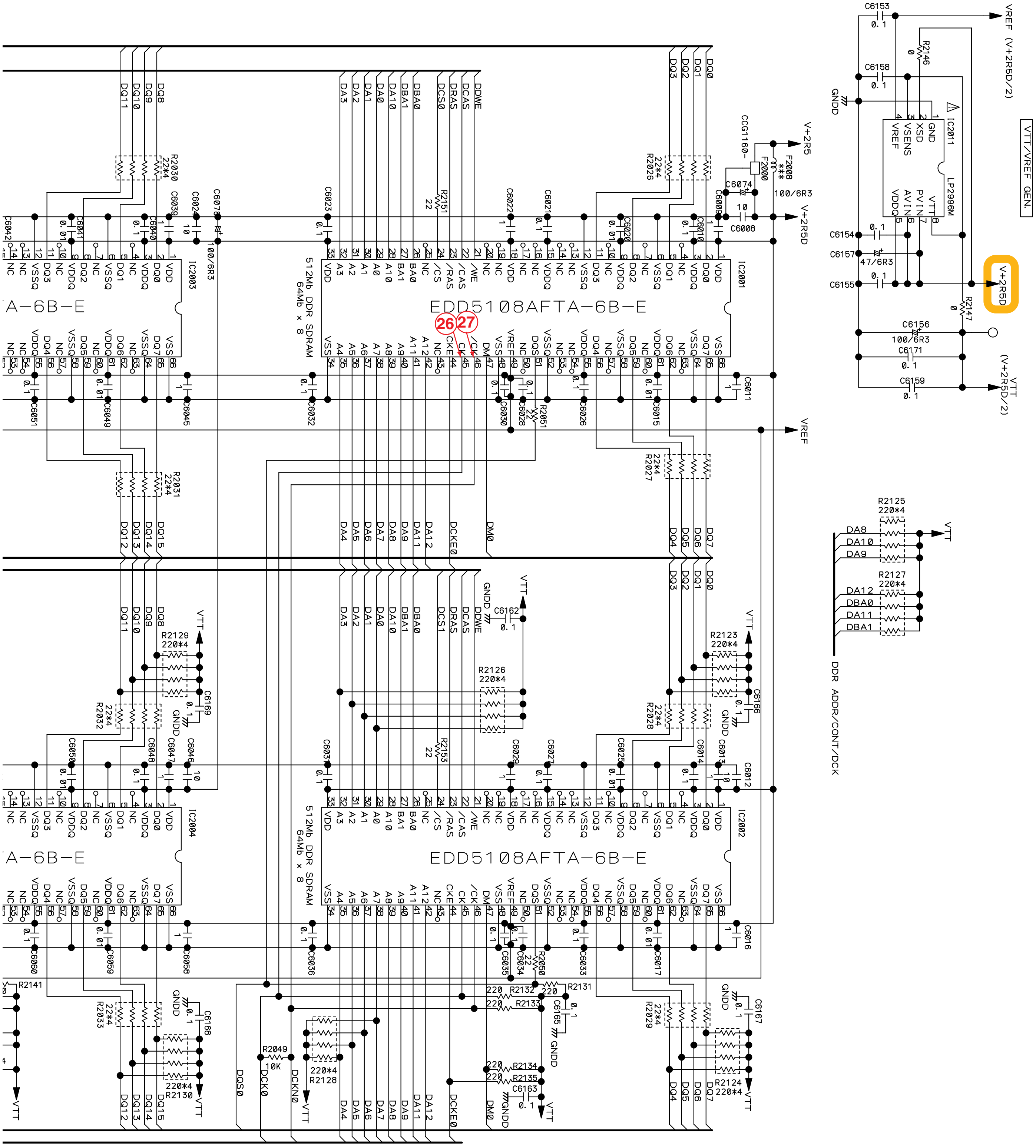
A 8/8 MAIN ASSY
(DWG1630)


GNDD
C6081 100/6R3
R2093 0
C6066 10
V+1R2
B
R2094 75K (F)
R2091 15K (F)
DCH1165
R2092 2.7K
C6065 120P CH
R2095 47k
V+3D2
IC2006 R1224N102H
CE
GND
VOUT
VIN (DLY)
NC (VIN)
EXT
L2004 CTH1254 22
1.2V DC-DC CONV
RB051L-40 D2000
R2089 22
D2001
Q2000 CPH6314-TLB
C6072 0.1 SQ
R2096 10
C6070 DCH1165 10
C6084 100/6R3
C6068 DCH1165 10
L2005 ATH7011 22
C6069 DCH1165 10
C6082 100/6R3
V+3D2
DDR SDRAM 2.5V REG.
A
B
V+2R5
CCG1160-
F2007
L2009 ***
C6175 100/6.3
C6174
1.0K (F) R2163
1.0K (F) R2162
IC2005 PQ070XZ02ZP
VOUT VIN
ADJ
OFF ON
100/6.3 C6173
CCG1160-
F2006
L2008 ***
V+3D2
DA3 DA2 DA1 DA0 DA10 DBA1 DBA0 DCS0 DRAS DCAS DDWE
R2136 220
VTT
R2152 22
C6044 0.1
C6043 1
C6042 0.1
C6041 0.01
512Mb DDR SDRAM 64Mb x 8
EDD5108AFTA-6B-E
VDD A3 A2 A1 A0 A10 BA1 BA0 NC /CS /RAS /CAS /WE NC NC VDD NC NC VDDQ NC NC VSSQ DQ3 NC VDDQ DQ2 NC VSSQ
VSS A4 A5 A6 A7 A8 A9 A11 A12 NC CKE CK /CK DM VSS VREF NC DQS VSSQ NC NC VDDQ DQ4 NC VSSQ DQ5 NC VDDQ
28
29
C6057 0.1
C6055 0.1
C6053 0.1
R2099 22
C6051 0.1
C6049 0.01
DA4 DA5 DA6 DA7 DA8 DA9 DA11 DA12 DCKE1 DM1
DA3 DA2 DA1 DA0 DA10 DBA1 DBA0 DCS1 DRAS DCAS DDWE
VTT
R2137 220
R2138 220
R2139 220
R2140 220
C6170 0.1
GNDD
R2154 22
22*4 R2032
C6058 0.1
C6054 1
C6052 0.1
C6050 0.01
512Mb DDR SDRAM 64Mb x 8
EDD5108AFTA-6B-E
C6063 0.1
C6062 0.1
C6061 0.1
R2098 22
C6060 0.1
C6059 0.01
R2141
R2142 220 220
R2143 220
R2144 220
R2145 220
22*4 R2033
C6164 0.1
GNDD
VTT
R2097 10K
DQS1 DCK1 DCKN1
DA4 DA5 DA6 DA7 DA8 DA9 DA11 DA12 DCKE1 DM1

Large size SCH diagram

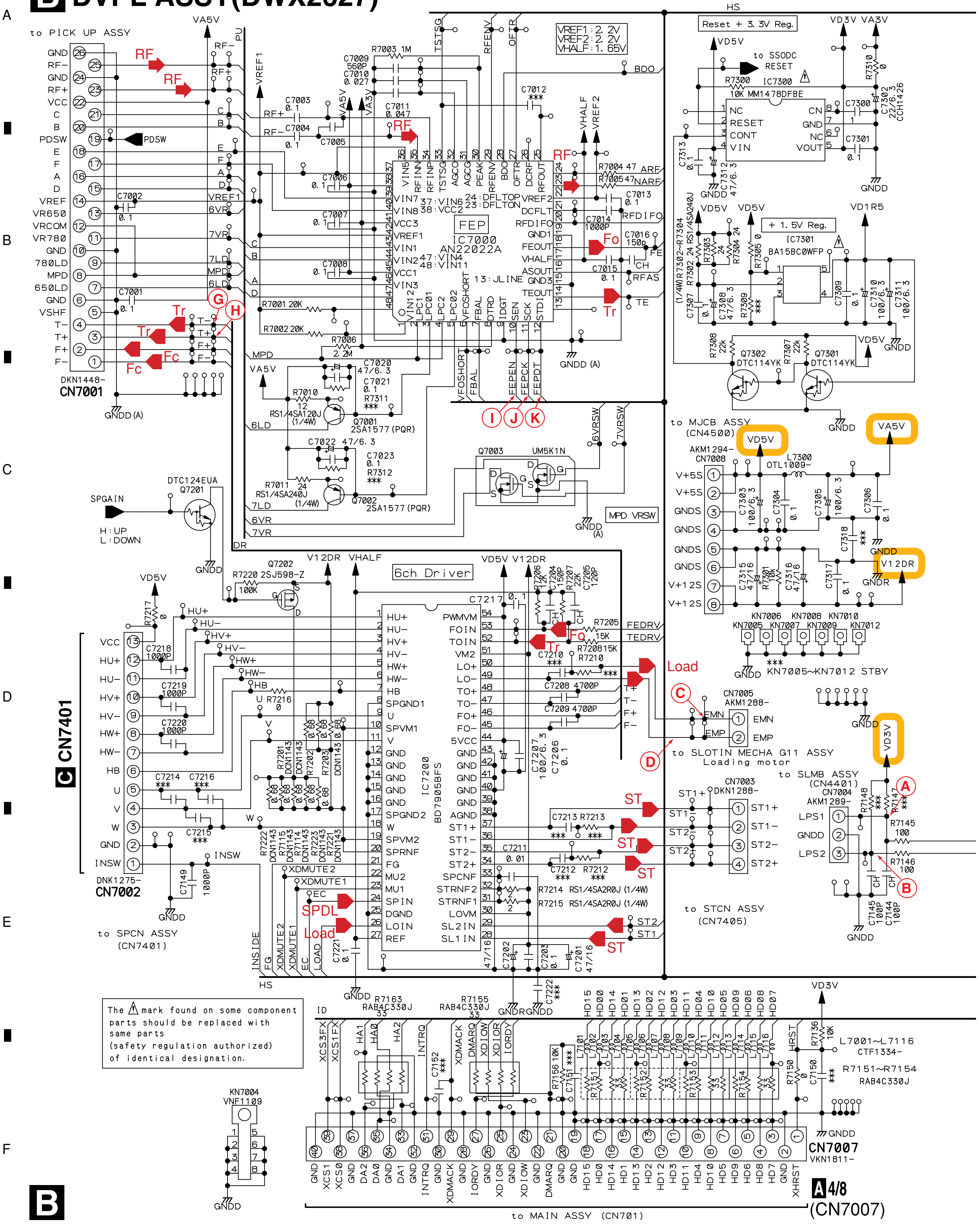


A 8/8


VREF (V+2R5D/2)
VTT/VREF GEN.
IC2011
LP2996M
GND
XSD
VSENS
VREF
VTT
PVIN
AVIN
VDDQ
C6153
0.1
R2146
0
C6158
0.1
GNDD
C6154
C6157
47/6R3
C6155
V+2R5D
R2147
0
C6156
100/6R3
C6171
0.1
C6159
0.1
VTT (V+2R5D/2)
V+2R5
F2008
F2000
CCG1160-
C6074
100/6R3
C6008
10
V+2R5D
VREF
IC2001
IC2002
IC2003
IC2004
EDD5108AFTA-6B-E
512Mb DDR SDRAM
64Mb x 8
26
27
DQ0
DQ1
DQ2
DQ3
DQ4
DQ5
DQ6
DQ7
DQ8
DQ9
DQ10
DQ11
DQ12
DQ13
DQ14
DQ15
DA0
DA1
DA2
DA3
DA4
DA5
DA6
DA7
DA8
DA9
DA10
DA11
DA12
DBA0
DBA1
DCS0
DCS1
DRAS
DCAS
DDWE
DCKE0
DM0
DQS0
DCK0
DCKN0
R2026
R2027
R2028
R2029
R2030
R2031
R2032
R2033
22*4
R2151
R2153
22
R2051
R2050
R2049
10K
R2123
R2124
R2125
R2126
R2127
R2128
R2129
R2130
220*4
R2131
R2132
R2133
R2134
R2135
220
R2141
C6162
C6163
C6165
C6166
C6167
C6168
C6169
0.1
VTT
GNDD
DDR ADDR/CONT/DCK

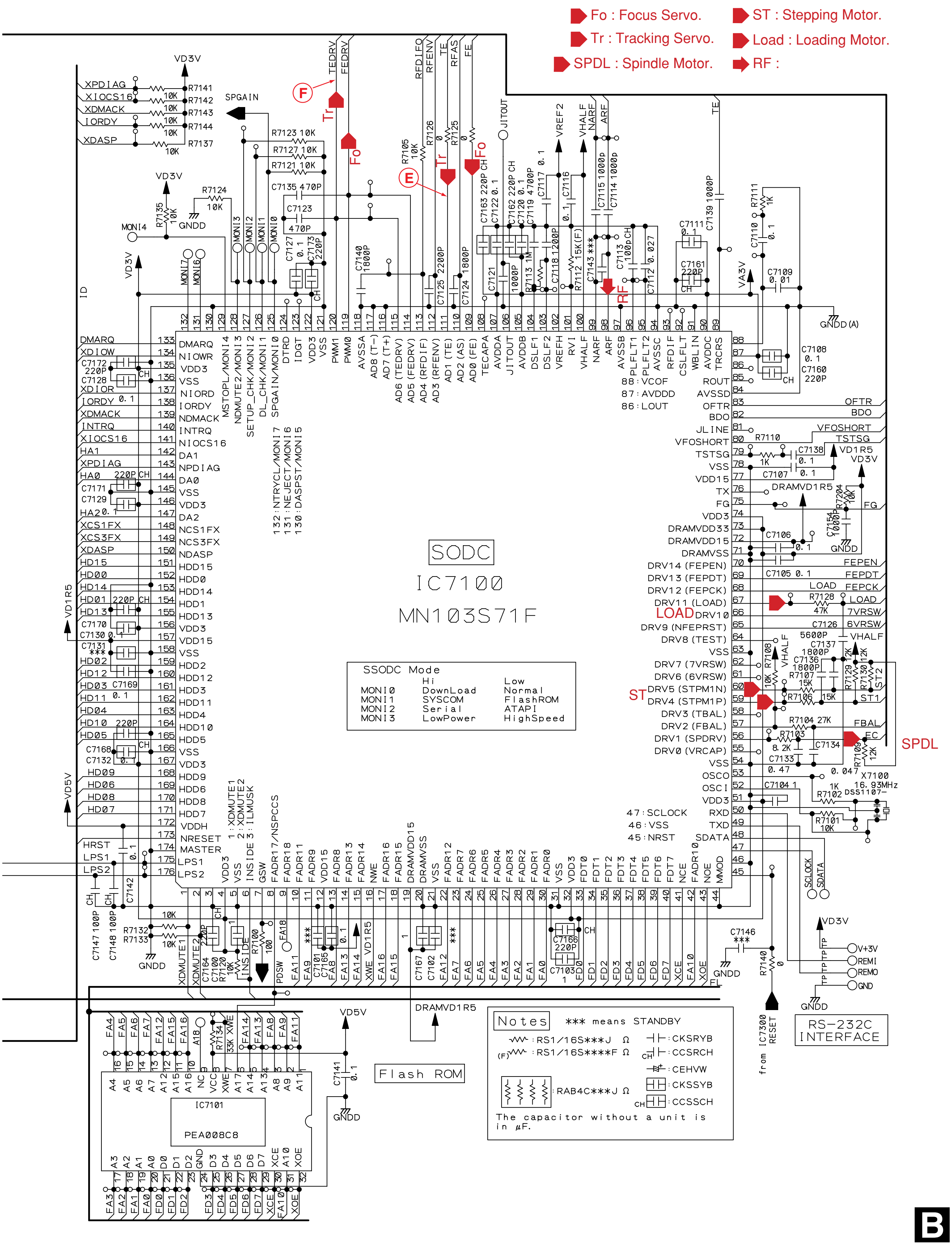
3.12 DVPL ASSY

B DVPL ASSY(DWX2627)

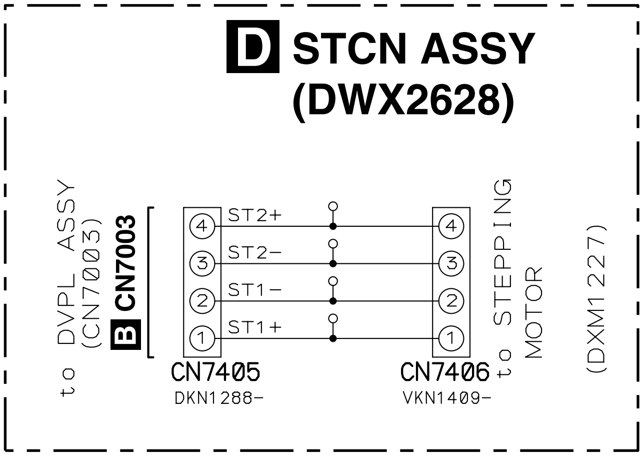


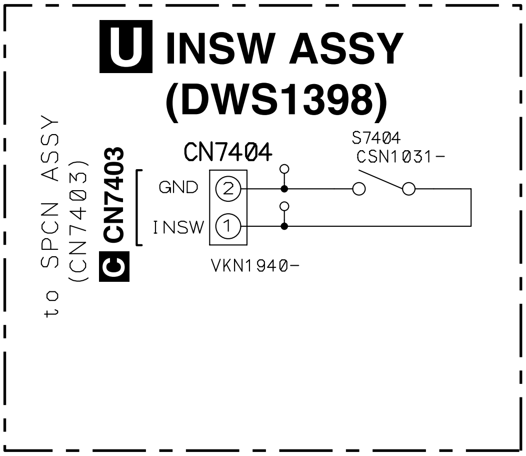
to PICK UP ASSY
GND
RF−
GND
RF+
VCC
C
B
PDSW
E
F
A
D
VREF
VR650
VRCOM
VR780
GND
780LD
MPD
650LD
GND
VSHF
T−
T+
F+
F−
DKN1448−
CN7001
RF
PDSW
Tr
Fc
G
H
VA5V
VREF1
C7002 0.1
C7001 0.1
R7001 20K
R7002 20K
R7003 1M
C7009 560P
C7010 0.027
C7003 0.1
C7004 0.1
C7005
C7011 0.047
C7012 ***
TSTSG
RFENV
OFTR
VREF1 : 2.2V
VREF2 : 2.2V
VHALF : 1.65V
BDO
VHALF
VREF2
FEP
IC7000
AN22022A
VIN5
RFINN
RFINP
TSTSG
AGCG
AGCG
PEAK
RFENV
BDO
OFTR
DCRF
RFOUT
VIN7
VIN8
VIN6
VCC3
VREF1
VIN1
VIN2
VCC1
VIN3
VIN4
VIN11
24 : DFLTOP
23 : DFLTON
37 : VIN6
38 : VCC2
47 : VIN4
48 : VIN11
13 : JLINE
VREF2
DCFLT
RFDIFO
GND1
FEOUT
VHALF
ASOUT
GND3
TEOUT
R7004 47
R7005 47
ARF
NARF
C7013 0.1
RFDIFO
C7014 1000P
C7016 150p
FE
CH
C7015 0.1
RFAS
TE
Fo
VIN12
LPC1
LPC01
LPC2
LPC02
VFOSHORT
FBAL
DTRD
IDGT
SEN
SCK
STDI
VFOSHORT
FBAL
FEPEN
FEPCK
FEPDT
I
J
K
GNDD (A)
R7006 2.2M
C7020 47/6.3
C7021 0.1
R7311 ***
R7010 12
RS1/4SA120J (1/4W)
Q7001 2SA1577(PQR)
C7022 47/6.3
C7023 0.1
R7312 ***
R7011 24
RS1/4SA240J (1/4W)
Q7002 2SA1577(PQR)
MPD
6LD
7LD
6VR
7VR
DR
Q7003 UM5K1N
6VRSW
7VRSW
MPD VRSW
GNDD (A)
DTC124EUA Q7201
SPGAIN
H : UP
L : DOWN
HS
Reset + 3.3V Reg.
VD5V
to SSODC RESET
R7300 10K
IC7300 MM1478DFBE
NC
RESET
CONT
VIN
CN
GND
NC
VOUT
C7300
C7301 0.1
C7302 22/6.3 CCH1426
C7313 0.1
C7312 47/6.3
R7310 0
VD3V
VA3V
GNDD
+1.5V Reg.
IC7301 BA15BC0WFP
VD1R5
R7302 24
R7304 24
RS1/4SA240J (1/4W)
R7303
R7305 0
C7307 0.1
C7308 47/6.3
R7309 ***
C7309 0.1
C7310 100/6.3
C7311 100/6.3
R7308 22k
Q7302 DTC114YK
R7307 22k
Q7301 DTC114YK
to MJCB ASSY (CN4500)
AKM1294− CN7008
V+5S
V+5S
GNDS
GNDS
GNDS
GNDS
V+12S
V+12S
VD5V
VA5V
L7300 OTL1009−
C7303 100/6.3
C7304 0.1
C7305 100/6.3
C7306 0.1
C7318 ***
C7315 47/16
R7301 10K
C7316 47/16
C7317 0.1
V12DR
GNDR
KN7005
KN7006
KN7007
KN7008
KN7009
KN7010
KN7012
*** KN7005~KN7012 STBY
VD5V
R7217 0
VCC
HU+
HU−
HV+
HV−
HW+
HW−
HB
U
V
W
GND
INSW
C CN7401
DNK1275− CN7002
C7218 1000P
C7219 1000P
C7220 1000P
C7214 ***
C7216 ***
C7215 ***
C7149 1000P
to SPCN ASSY (CN7401)
Q7202 2SJ598−Z
R7220 100K
V12DR
VHALF
6ch Driver
IC7200 BD7905BFS
R7216 0
R7201 DCN1143
R7202 DCN1143
R7203 DCN1143
R7222 DCN1143
R7113 DCN1143
R7114 DCN1143
R7223 DCN1143
R7221 DCN1143
XDMUTE2
XDMUTE1
EC
SPDL
Load
C7221 0.1
HU+
HU−
HV+
HV−
HW+
HW−
HB
SPGND1
U
SPVM1
V
GND
SPGND2
W
SPVM2
SPRNF
FG
MU2
MU1
SPIN
DGND
LOIN
REF
PWMVM
FOIN
TOIN
VM2
LO+
LO−
TO+
TO−
FO+
FO−
5VCC
AGND
ST1+
ST1−
ST2−
ST2+
SPCNF
STRNF2
STRNF1
LOVM
SL2IN
SL1IN
C7217 0.1
R7206 12K
C7204 150P
R7207 22K
C7205 120P
R7205 15K
R7208 15K
C7210 ***
R7210
C7208 4700P
C7209 4700P
C7207 100/6.3
C7206 0.1
FEDRV
TEDRV
Load
C
D
EMN
EMP
CN7005 AKM1288−
to SLOTIN MECHA G11 ASSY Loading motor
C7213 R7213
C7211 0.01
C7212 R7212
R7214 RS1/4SA2R0J (1/4W)
R7215 RS1/4SA2R0J (1/4W)
ST
ST1+
ST1−
ST2−
ST2+
CN7003 DKN1288−
to STCN ASSY (CN7405)
ST2
ST1
C7202
C7203 0.1
C7201 47/16
C7222 ***
to SLMB ASSY (CN4401)
CN7004 AKM1289−
LPS1
GNDD
LPS2
R7148 ***
R7147 ***
R7145 100
R7146 100
C7145 100P
C7144 100P
A
B
VD3V
INSIDE
FG
XDMUTE2
XDMUTE1
EC
LOAD
R7163 RAB4C330J
R7155 RAB4C330J
GNDR
GNDD
ID
XCS3FX
XCS1FX
HA1
HA0
HA2
INTRQ
C7152 ***
XDMACK
DMARQ
XDIOW
XDIOR
IORDY
R7156 10K
C7151 ***
R7151
R7152
R7153
R7154
L7101~L7116
HD15
HD00
HD14
HD01
HD13
HD02
HD12
HD03
HD11
HD04
HD10
HD05
HD09
HD06
HD08
HD07
HRST
R7136 10K
R7150 0
C7150 ***
L7001~L7116 CTF1334−
R7151~R7154 RAB4C330J
KN7004 VNF1109
GNDD
GND
XCS1
XCS0
GND
DA2
DA0
GND
DA1
GND
INTRQ
GND
XDMACK
IORDY
GND
XDIOR
GND
XDIOW
GND
DMARQ
GND
GND
HD15
HD0
HD14
HD1
HD13
HD2
HD12
HD3
HD11
HD4
HD10
HD5
HD9
HD6
HD8
HD7
GND
XHRST
CN7007
VKN1811−
A 4/8 (CN7007)
to MAIN ASSY (CN701)
1
2
3
4
A
B
C
D
E
F

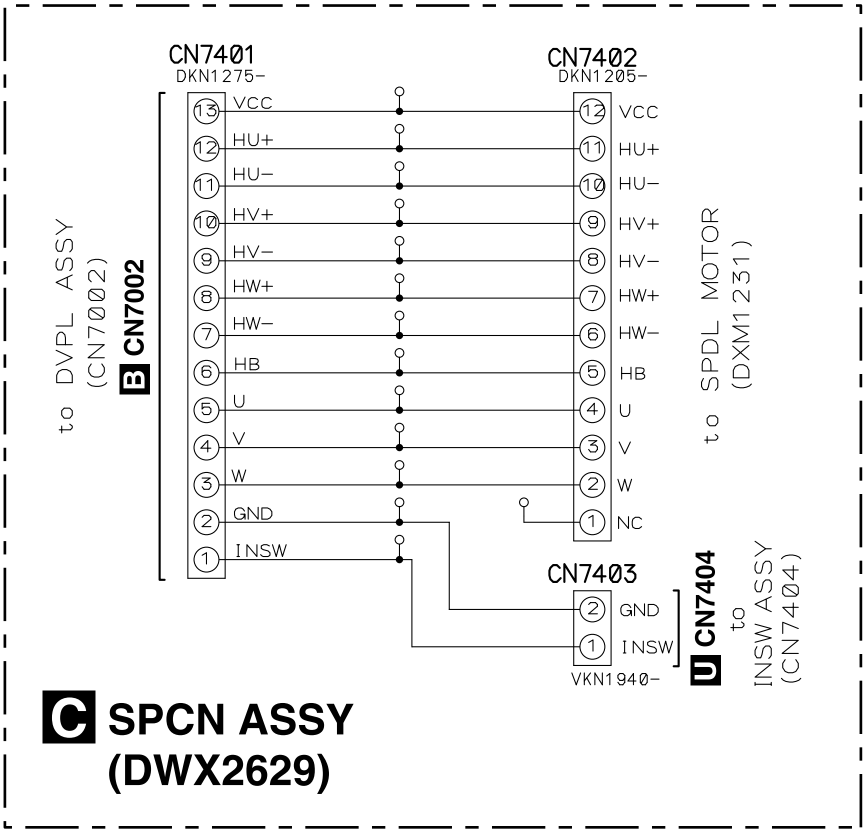
The ⚠ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.


Fo : Focus Servo.
Tr : Tracking Servo.
SPDL : Spindle Motor.
ST : Stepping Motor.
Load : Loading Motor.
RF :
SODC
IC7100
MN103S71F
SSODC Mode
Hi / Low
MONI0 DownLoad / Normal
MONI1 SYSCOM / FlashROM
MONI2 Serial / ATAPI
MONI3 LowPower / HighSpeed
132:NTRYCL/MONI7
131:NEJECT/MONI6
130:DASPST/MONI5
88:VCOF
87:AVDDD
86:LOUT
47:SCLOCK
46:VSS
45:NRST
1:XDMUTE1
2:XDMUTE2
3:ILMUSK
IC7101
PEA008C8
Flash ROM
RS-232C INTERFACE
from IC7300 RESET
X7100 16.93MHz DSS1107-
VD3V
VD5V
VD1R5
VA3V
GNDD
GNDD (A)
DRAMVD1R5
SPGAIN
TEDRV
FEDRV
RFDIFO
RFENV
TE
RFAS
FE
JITOUT
VREF2
VHALF
NARF
ARF
OFTR
BDO
VFOSHORT
TSTSG
FG
FEPEN
FEPDT
FEPCK
LOAD
7VRSW
6VRSW
ST2
ST1
FBAL
EC
V+3V
REMI
REMO
GND
SCLOCK
SDATA
Notes *** means STANDBY
:RS1/16S***J Ω
(F) :RS1/16S****F Ω
:RAB4C***J Ω
:CKSRYB
CH :CCSRCH
:CEHVW
:CKSSYB
CH :CCSSCH
The capacitor without a unit is in μF.

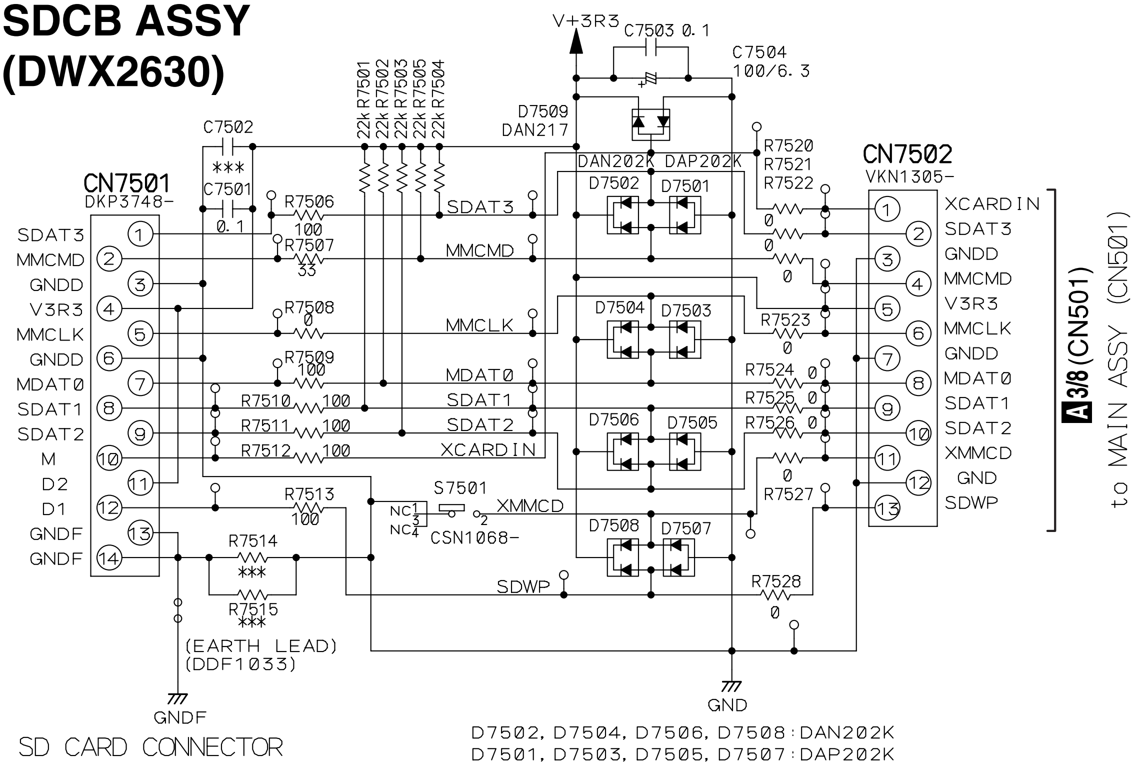
3.13 STCN, SPCN, INSW and SDCB ASSYS


D STCN ASSY
(DWX2628)
to DVPL ASSY
(CN7003)
B CN7003
ST2+
ST2–
ST1–
ST1+
4
3
2
1
CN7405
DKN1288-
CN7406
VKN1409-
to STEPPING MOTOR
(DXM1227)


U INSW ASSY
(DWS1398)
to SPCN ASSY
(CN7403)
C CN7403
CN7404
GND
INSW
2
1
VKN1940-
S7404
CSN1031-


CN7401
DKN1275-
CN7402
DKN1205-
to DVPL ASSY
(CN7002)
B CN7002
13 VCC
12 HU+
11 HU–
10 HV+
9 HV–
8 HW+
7 HW–
6 HB
5 U
4 V
3 W
2 GND
1 INSW
12 VCC
11 HU+
10 HU–
9 HV+
8 HV–
7 HW+
6 HW–
5 HB
4 U
3 V
2 W
1 NC
to SPDL MOTOR
(DXM1231)
CN7403
2 GND
1 INSW
VKN1940-
U CN7404
to INSW ASSY
(CN7404)
C SPCN ASSY
(DWX2629)

F SDCB ASSY (DWX2630)


V+3R3
C7503 0.1
C7504
100/6.3
D7509
DAN217
R7501 22k
R7502 22k
R7503 22k
R7505 22k
R7504 22k
C7502
***
C7501
0.1
CN7501
DKP3748-
SDAT3 ①
MMCMD ②
GNDD ③
V3R3 ④
MMCLK ⑤
GNDD ⑥
MDAT0 ⑦
SDAT1 ⑧
SDAT2 ⑨
M ⑩
D2 ⑪
D1 ⑫
GNDF ⑬
GNDF ⑭
R7506 100
R7507 33
R7508 0
R7509 100
R7510 100
R7511 100
R7512 100
R7513 100
R7514 ***
R7515 ***
SDAT3
MMCMD
MMCLK
MDAT0
SDAT1
SDAT2
XCARDIN
S7501
NC 1
NC 3
NC 4
2
XMMCD
CSN1068-
SDWP
(EARTH LEAD)
(DDF1033)
GNDF
SD CARD CONNECTOR
DAN202K DAP202K
D7502 D7501
D7504 D7503
D7506 D7505
D7508 D7507
R7520
R7521
R7522
0
0
0
R7523 0
R7524 0
R7525 0
R7526 0
0
R7527
R7528 0
GND
CN7502
VKN1305-
① XCARDIN
② SDAT3
③ GNDD
④ MMCMD
⑤ V3R3
⑥ MMCLK
⑦ GNDD
⑧ MDAT0
⑨ SDAT1
⑩ SDAT2
⑪ XMMCD
⑫ GND
⑬ SDWP
A 3/8 (CN501)
to MAIN ASSY (CN501)

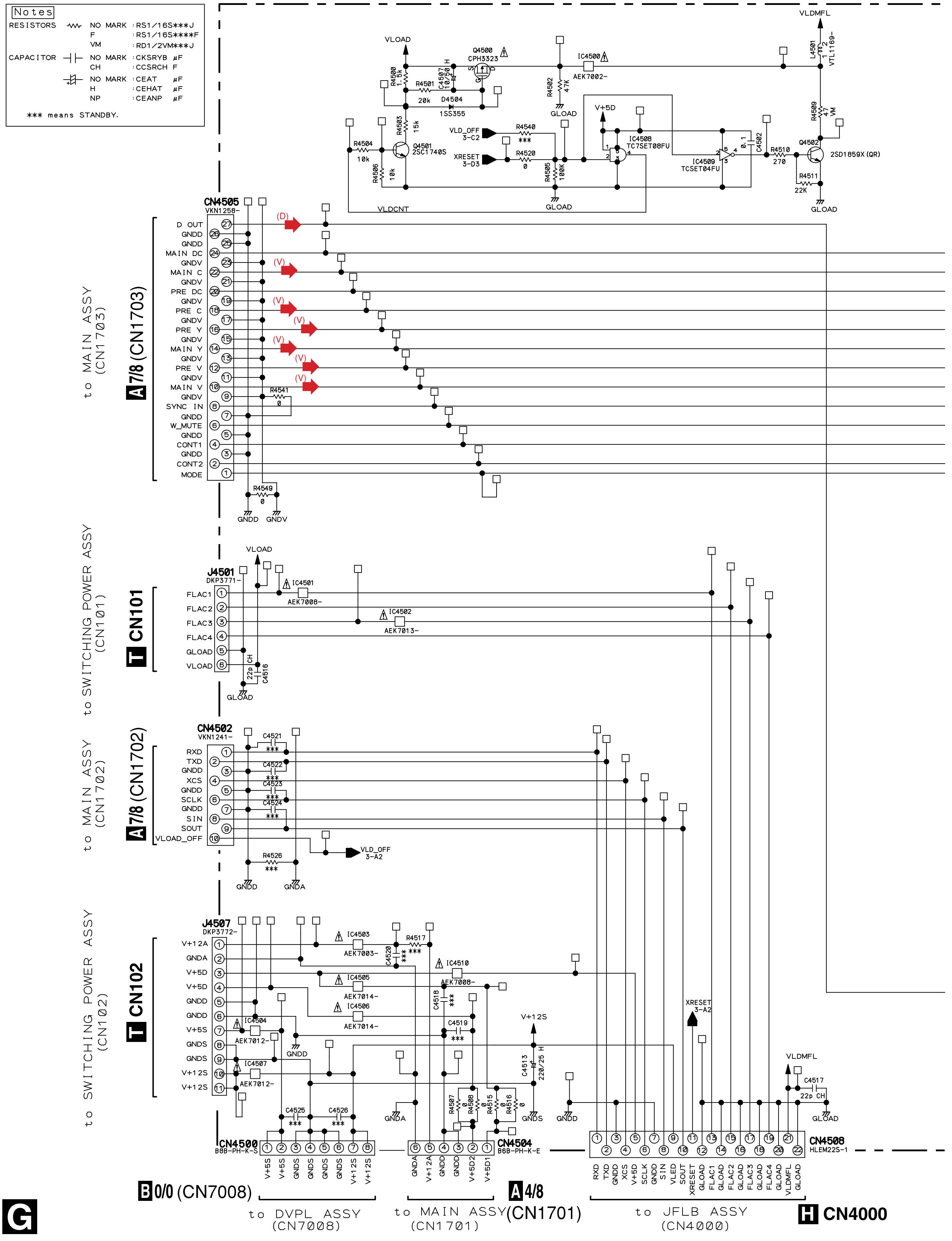
D7502, D7504, D7506, D7508 : DAN202K
D7501, D7503, D7505, D7507 : DAP202K

Notes ***means STANDBY

-\/\/\- : RS1/16S***J Ω
(F)-\/\/\- : RS1/16S****F Ω
⊣⊢ : CKSRYB
CH ⊣⊢ : CCSRCH
⊣⊢+ : CEHVW
⊣⊢ : CKSSYB
CH ⊣⊢ : CCSSCH
RAB4C***J Ω

The capacitor without a unit is in μF.

3.14 MJCB ASSY



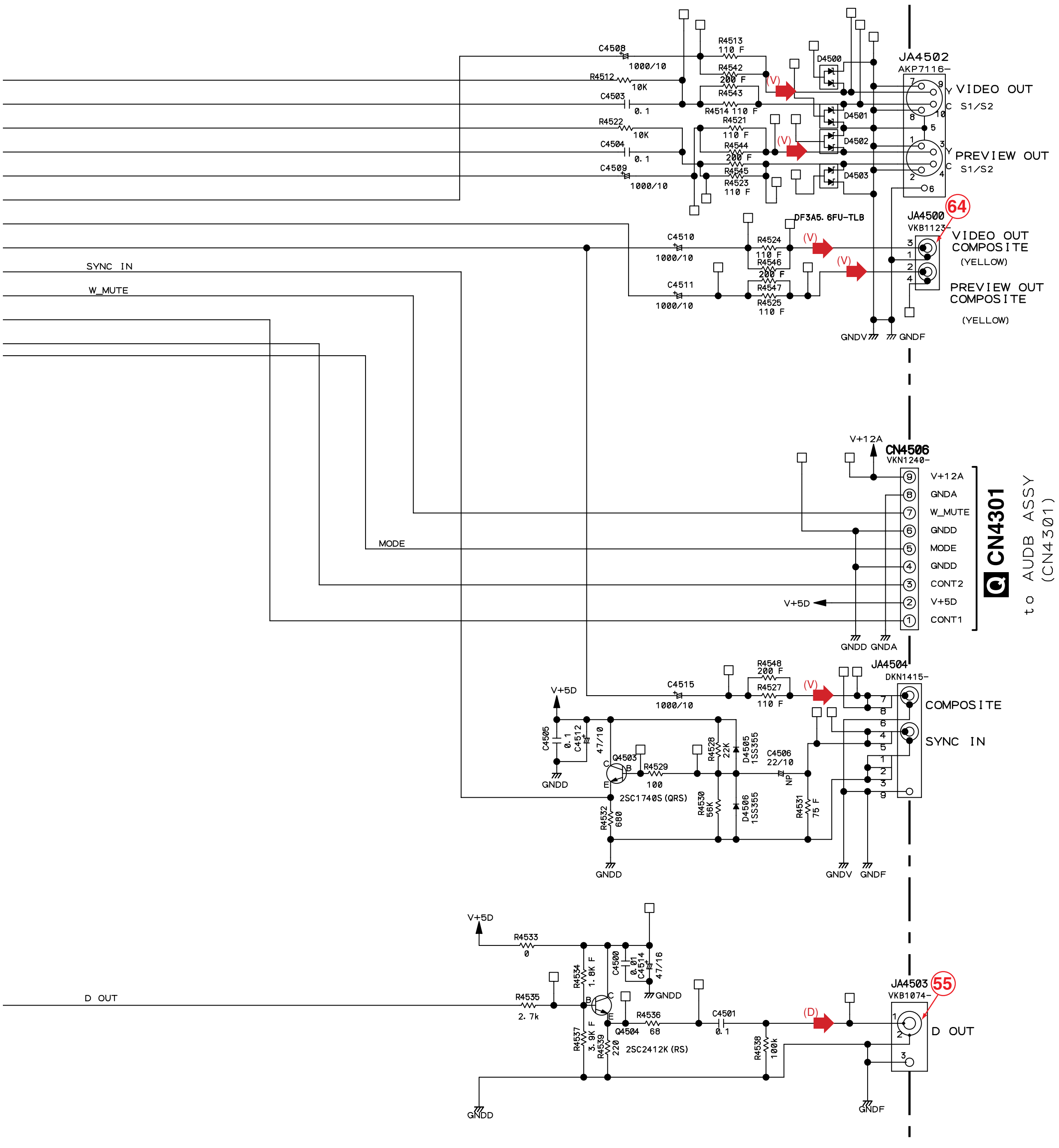
Notes

RESISTORS	NO MARK	:	RS1/16S***J
	F	:	RS1/16S****F
	VM	:	RD1/2VM***J
CAPACITOR	NO MARK	:	CKSRYB µF
	CH	:	CCSRCH F
	NO MARK	:	CEAT µF
	H	:	CEHAT µF
	NP	:	CEANP µF

*** means STANDBY.

VLOAD
Q4500 CPH3323
VLDMFL
L4501 VTL1169-
IC4500 AEK7002-
R4500 1.5k
R4501 20k
C4507 10/50 H
D4504 1SS355
R4502 47K
GLOAD
V+5D
R4503 15k
VLD_OFF 3-C2
R4540 ***
IC4508 TC7SET08FU
C4502 0.1
R4509 47 VM
R4504 10k
Q4501 2SC1740S
XRESET 3-D3
R4520 0
IC4509 TCSET04FU
R4510 270
Q4502 2SD1859X (QR)
R4506 10k
R4505 100k
R4511 22K
VLDCNT
CN4505 VKN1258-
(D)
(V)
D OUT 27
GNDD 26
GNDD 25
MAIN DC 24
GNDV 23
MAIN C 22
GNDV 21
PRE DC 20
GNDV 19
PRE C 18
GNDV 17
PRE Y 16
GNDV 15
MAIN Y 14
GNDV 13
PRE V 12
GNDV 11
MAIN V 10
GNDV 9
SYNC IN 8
GNDD 7
W_MUTE 6
GNDD 5
CONT1 4
GNDD 3
CONT2 2
MODE 1
R4541 0
R4549 0
GNDD
GNDV
to MAIN ASSY (CN1703)
A 7/8 (CN1703)
J4501 DKP3771-
IC4501 AEK7008-
IC4502 AEK7013-
FLAC1 1
FLAC2 2
FLAC3 3
FLAC4 4
GLOAD 5
VLOAD 6
22p CH C4516
to SWITCHING POWER ASSY (CN101)
T CN101
CN4502 VKN1241-
C4521 ***
C4522 ***
C4523 ***
C4524 ***
RXD 1
TXD 2
GNDD 3
XCS 4
GNDD 5
SCLK 6
GNDD 7
SIN 8
SOUT 9
VLOAD_OFF 10
VLD_OFF 3-A2
R4526 ***
GNDA
to MAIN ASSY (CN1702)
A 7/8 (CN1702)
J4507 DKP3772-
IC4503 AEK7003-
R4517 ***
C4520 ***
IC4510 AEK7008-
IC4505 AEK7014-
IC4506 AEK7014-
C4518 ***
IC4504 AEK7012-
C4519 ***
IC4507 AEK7012-
V+12S
C4513 220/25 H
XRESET 3-A2
VLDMFL
C4517 22p CH
V+12A 1
GNDA 2
V+5D 3
V+5D 4
GNDD 5
GNDD 6
V+5S 7
GNDS 8
GNDS 9
V+12S 10
V+12S 11
to SWITCHING POWER ASSY (CN102)
T CN102
C4525 ***
C4526 ***
R4507 0
R4508 0
R4515 0
R4516 0
GNDS
CN4500 B8B-PH-K-S
V+5S V+5S GNDS GNDS GNDS GNDS V+12S V+12S
CN4504 B6B-PH-K-E
GNDA V+12A GNDD GNDD V+5D2 V+5D1
CN4508 HLEM22S-1
RXD TXD GNDD XCS V+5D SCLK GNDD SIN VLED SOUT XRESET GLOAD FLAC1 GLOAD FLAC2 GLOAD FLAC3 GLOAD FLAC4 GLOAD VLDMFL GLOAD
B 0/0 (CN7008)
to DVPL ASSY (CN7008)
A 4/8
to MAIN ASSY (CN1701)
to JFLB ASSY (CN4000)
H CN4000

G MJCB ASSY
(DWG1633)


C4508
1000/10
R4512
10K
C4503
0. 1
R4522
10K
C4504
0. 1
C4509
1000/10
R4513
110 F
R4542
200 F
R4543
R4514 110 F
R4521
110 F
R4544
200 F
R4545
R4523
110 F
D4500
D4501
D4502
D4503
(V)
(V)
JA4502
AKP7116-
VIDEO OUT
S1/S2
PREVIEW OUT
S1/S2
Y
C
DF3A5. 6FU-TLB
JA4500
VKB1123-
64
VIDEO OUT
COMPOSITE
(YELLOW)
PREVIEW OUT
COMPOSITE
(YELLOW)
C4510
1000/10
R4524
110 F
R4546
200 F
R4547
R4525
110 F
C4511
1000/10
GNDV
GNDF
SYNC IN
W_MUTE
MODE
V+12A
CN4506
VKN1240-
V+12A
GNDA
W_MUTE
GNDD
MODE
GNDD
CONT2
V+5D
CONT1
V+5D
GNDD GNDA
Q CN4301
to AUDB ASSY
(CN4301)
JA4504
DKN1415-
COMPOSITE
SYNC IN
R4548
200 F
R4527
110 F
C4515
1000/10
V+5D
C4505
0. 1
C4512
47/10
GNDD
Q4503
R4529
100
2SC1740S (QRS)
R4528
22K
D4505
1SS355
C4506
22/10
NP
R4530
56K
D4506
1SS355
R4531
75 F
R4532
680
GNDD
GNDV GNDF
V+5D
R4533
0
R4534
1. 8K F
C4500
0. 01
C4514
47/16
GNDD
D OUT
R4535
2. 7k
Q4504
R4536
68
2SC2412K (RS)
R4537
3. 9K F
R4539
220
C4501
0. 1
R4538
100k
(D)
JA4503
VKB1074-
55
D OUT
GNDD
GNDF

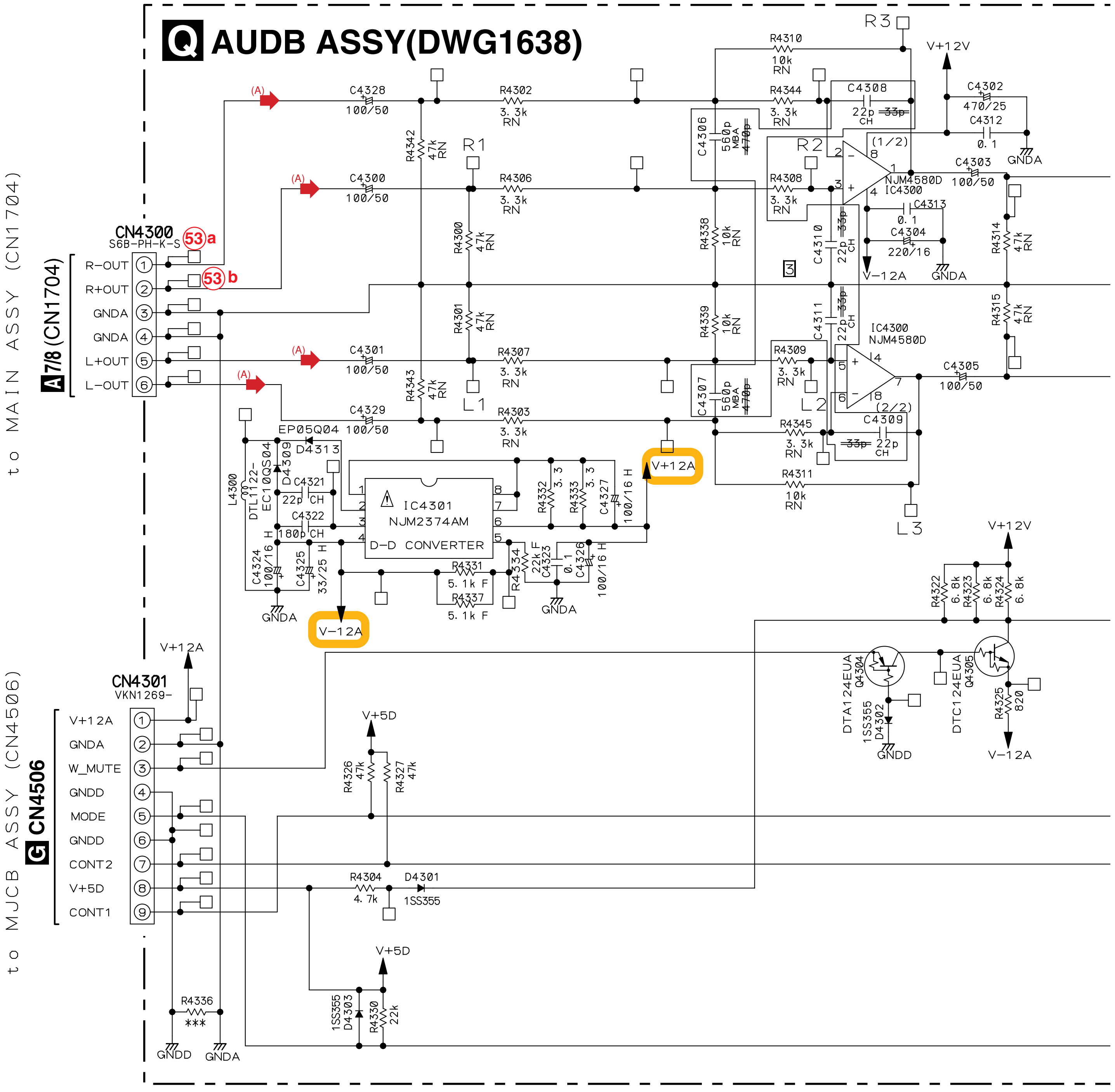
CAUTION - FOR CONTINUED PROTECTION AGAINST RISK OF FIRE, REPLACE ONLY WITH SAME TYPE NO. IC4500:491. 250, IC4501:491. 800, IC4502:491002., IC4503:491. 315, IC4504:49101. 6, IC4505:49102. 5, IC4506:49102. 5, IC4507:49101. 6, IC4510:491. 800 MFD. BY LITTELFUSE INC.

The ⚠ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.

(V) ➡ : Analog Video Signal Rout.

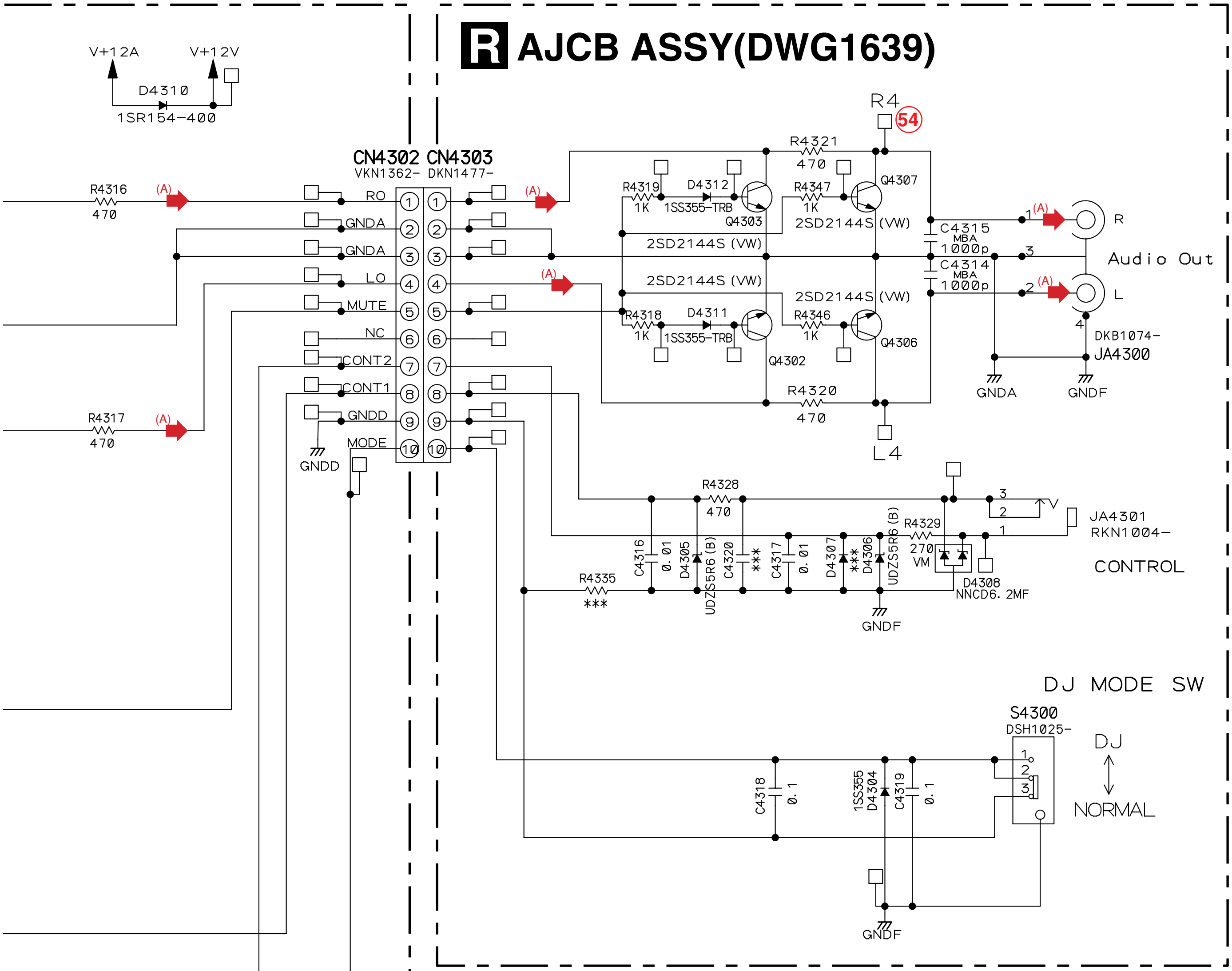
(D) ➡ : Digital Out Signal Rout.

3.15 AUDB and AJCB ASSYS


Q AUDB ASSY(DWG1638)
to MAIN ASSY (CN1704)
A 7/8 (CN1704)
CN4300
S6B-PH-K-S
53a
53b
R-OUT ①
R+OUT ②
GNDA ③
GNDA ④
L+OUT ⑤
L-OUT ⑥
(A)
C4328 100/50
C4300 100/50
C4301 100/50
C4329 100/50
R4302 3.3k RN
R4306 3.3k RN
R4307 3.3k RN
R4303 3.3k RN
R4342 47k RN
R4300 47k RN
R4301 47k RN
R4343 47k RN
R1
L1
R2
L2
R3
L3
R4310 10k RN
R4344 3.3k RN
R4308 3.3k RN
R4309 3.3k RN
R4345 3.3k RN
R4311 10k RN
R4338 10k RN
R4339 10k RN
C4306 560p MBA 470p
C4307 560p MBA 470p
C4308 22p 33p CH
C4309 33p 22p CH
C4310 22p 33p CH
C4311 22p 33p CH
(1/2) NJM4580D IC4300
IC4300 NJM4580D (2/2)
C4313 0.1
C4304 220/16
C4302 470/25
C4312 0.1
C4303 100/50
C4305 100/50
R4314 47k RN
R4315 47k RN
V+12V
V-12A
V+12A
GNDA
3
EP05Q04 D4313
L4300 DTL1122-
EC10QS04 D4309
C4321 22p CH
C4322 180p CH
C4324 100/16 H
C4325 33/25 H
IC4301 NJM2374AM D-D CONVERTER
R4332 3.3
R4333 3.3
C4327 100/16 H
R4334 22k F
C4323 0.1
C4326 100/16 H
R4331 5.1k F
R4337 5.1k F
CN4301 VKN1269-
to MJCB ASSY (CN4506)
G CN4506
V+12A ①
GNDA ②
W_MUTE ③
GNDD ④
MODE ⑤
GNDD ⑥
CONT2 ⑦
V+5D ⑧
CONT1 ⑨
V+5D
R4326 47k
R4327 47k
R4304 4.7k
D4301 1SS355
1SS355 D4303
R4330 22k
R4336 ***
GNDD
R4322 6.8k
R4323 6.8k
R4324 6.8k
DTA124EUA Q4304
DTC124EUA Q4305
1SS355 D4302
R4325 820

(A) : Analog Audio Signal Rout.

R AJCB ASSY(DWG1639)


V+12A
V+12V
D4310
1SR154-400
CN4302 CN4303
VKN1362- DKN1477-
R4316
470
R4317
470
(A)
RO
GNDA
GNDA
LO
MUTE
NC
CONT2
CONT1
GNDD
MODE
GNDD
R4
54
R4321
470
R4319
1K
D4312
1SS355-TRB
Q4303
R4347
1K
Q4307
2SD2144S (VW)
2SD2144S (VW)
2SD2144S (VW)
2SD2144S (VW)
R4318
1K
D4311
1SS355-TRB
Q4302
R4346
1K
Q4306
R4320
470
L4
C4315
MBA
1000p
C4314
MBA
1000p
R
L
Audio Out
DKB1074-
JA4300
GNDA
GNDF
R4328
470
JA4301
RKN1004-
CONTROL
R4329
270
VM
D4308
NNCD6. 2MF
C4316
0. 01
D4305
UDZS5R6 (B)
C4320
***
C4317
0. 01
D4307
***
D4306
UDZS5R6 (B)
R4335
***
GNDF
DJ MODE SW
S4300
DSH1025-
DJ
NORMAL
C4318
0. 1
1SS355
D4304
C4319
0. 1
GNDF

Notes

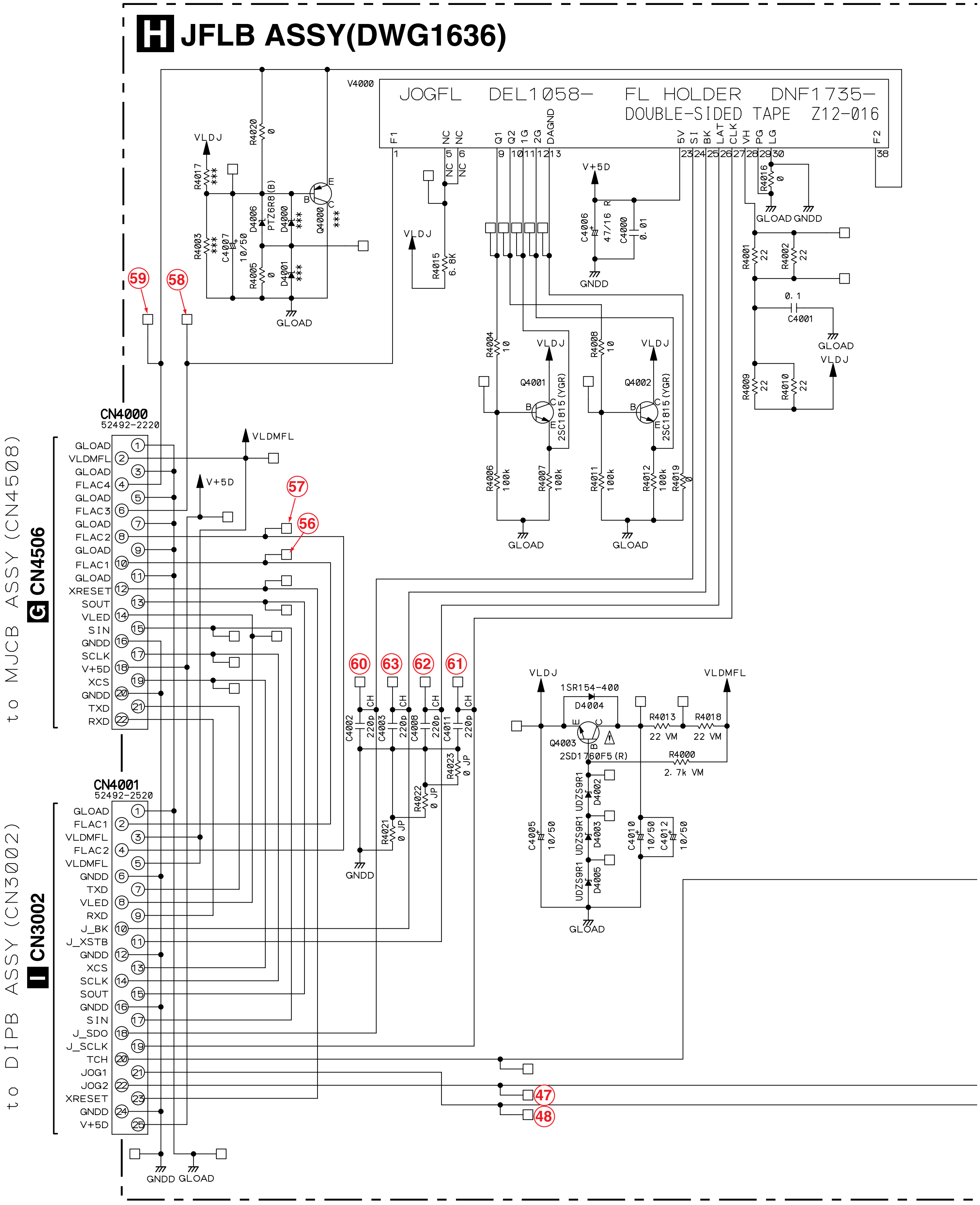
RESISTORS	⩘	NO MARK	: RS1/16S***J
		F	: RS1/16S****F
		VM	: RD1/2VM***J
		RN	: RN1/16SE****D
CAPACITOR	⊣⊢	NO MARK	: CKSRYB μF
		CH	: CCSRCH F
		MBA	: CQMBA F
		NO MARK	: CEAT μF
		H	: CEHAT μF
		NP	: CEANP μF

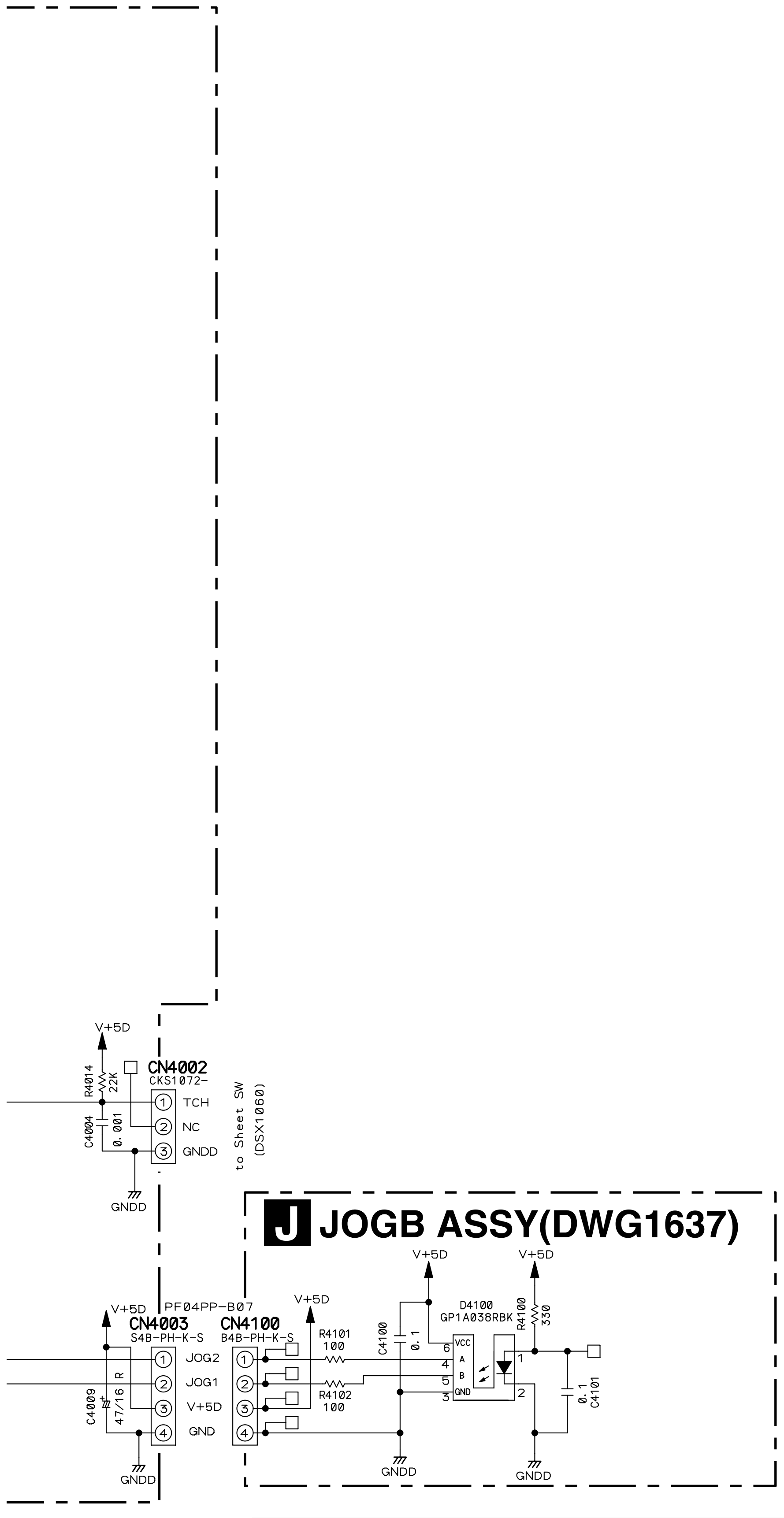
*** means STANDBY.

(A) ➡ : Analog Audio Signal Rout.

The ⚠ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.

3.16 JFLB and JOGB ASSYS


H JFLB ASSY(DWG1636)
V4000
JOGFL DEL1058-
FL HOLDER DNF1735-
DOUBLE-SIDED TAPE Z12-016
F1 NC NC Q1 Q2 1G 2G DAGND 5V SI BK LAT CLK VH PG LG F2
1 5 6 9 10 11 12 13 23 24 25 26 27 28 29 30 38
VLDJ
R4020 0
R4017 ***
R4003 ***
C4007 10/50
D4006 PTZ6R8(B)
D4000 ***
Q4000 ***
R4005 0
D4001 ***
GLOAD
R4015 6.8K
V+5D
C4006 47/16 R
C4000 0.01
GNDD
R4016 0
GLOAD GNDD
R4001 22
R4002 22
0.1
C4001
GLOAD
VLDJ
R4009 22
R4010 22
59
58
R4004 10
Q4001
2SC1815(YGR)
R4008 10
Q4002
2SC1815(YGR)
R4006 100k
R4007 100k
R4011 100k
R4012 100k
R4019 0
GLOAD
GLOAD
CN4000
52492-2220
GLOAD ① VLDMFL ② GLOAD ③ FLAC4 ④ GLOAD ⑤ FLAC3 ⑥ GLOAD ⑦ FLAC2 ⑧ GLOAD ⑨ FLAC1 ⑩ GLOAD ⑪ XRESET ⑫ SOUT ⑬ VLED ⑭ SIN ⑮ GNDD ⑯ SCLK ⑰ V+5D ⑱ XCS ⑲ GNDD ⑳ TXD ㉑ RXD ㉒
VLDMFL
V+5D
57
56
to MJCB ASSY (CN4508)
G CN4506
60 63 62 61
C4002 220p CH
C4003 220p CH
C4008 220p CH
C4011 220p CH
R4023 0 JP
R4022 0 JP
R4021 0 JP
GNDD
VLDJ
1SR154-400
D4004
Q4003
2SD1760F5(R)
VLDMFL
R4013 22 VM
R4018 22 VM
R4000 2.7k VM
D4002 UDZS9R1
D4003 UDZS9R1
D4005 UDZS9R1
C4005 10/50
C4010 10/50
C4012 10/50
GLOAD
CN4001
52492-2520
GLOAD ① FLAC1 ② VLDMFL ③ FLAC2 ④ VLDMFL ⑤ GNDD ⑥ TXD ⑦ VLED ⑧ RXD ⑨ J_BK ⑩ J_XSTB ⑪ GNDD ⑫ XCS ⑬ SCLK ⑭ SOUT ⑮ GNDD ⑯ SIN ⑰ J_SDO ⑱ J_SCLK ⑲ TCH ⑳ JOG1 ㉑ JOG2 ㉒ XRESET ㉓ GNDD ㉔ V+5D ㉕
to DIPB ASSY (CN3002)
I CN3002
47
48
GNDD GLOAD


V+5D
R4014
22K
CN4002
CKS1072-
① TCH
② NC
③ GNDD
C4004
0.001
GNDD
to Sheet SW
(DSX1060)
J JOGB ASSY(DWG1637)
V+5D
V+5D
D4100
GP1A038RBK
R4100
330
C4100
0.1
VCC
A
B
GND
6
4
5
3
1
2
0.1
C4101
V+5D
R4101
100
R4102
100
V+5D
PF04PP-B07
CN4003
S4B-PH-K-S
CN4100
B4B-PH-K-S
① JOG2
② JOG1
③ V+5D
④ GND
C4009
47/16 R
GNDD
GNDD
GNDD

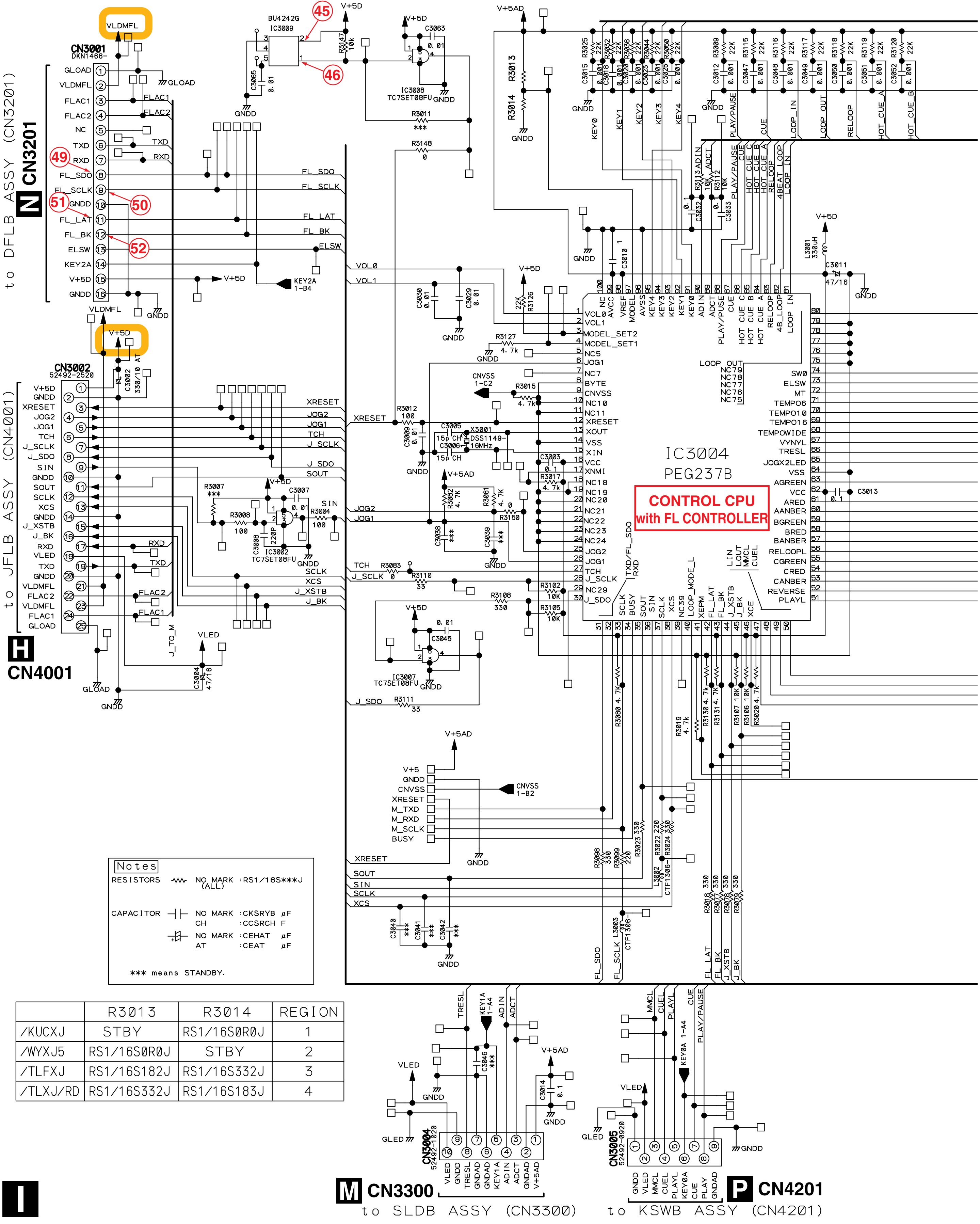
Notes			
RESISTORS	-ww-	NO MARK	:RS1/16S***J
		JP	:RS1/10S0R0J
		VM	:RD1/2VM***J
CAPACITOR	⊣⊢	NO MARK	:CKSRYB μF
		CH	:CCSRCH F
		NO MARK	:CEHAZL μF
		R	:CEHAR μF

*** means STANDBY.

The ⚠ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.

3.17 DIPB and PSWB ASSYS

I DIPB ASSY(DWG1631: /KUCXJ, DWG1640 : /WYXJ5 DWG1641 : /TLFXJ, DWG1642 : /TLXJ/RD)



N CN3201
to DFLB ASSY (CN3201)
CN3001 DKN1468-
GLOAD
VLDMFL
FLAC1
FLAC2
NC
TXD
RXD
FL_SDO
FL_SCLK
GNDD
FL_LAT
FL_BK
ELSW
KEY2A
V+5D
(45)
(46)
(49)
(50)
(51)
(52)
BU4242G IC3009
IC3008 TC7SET08FU
H CN4001
to JFLB ASSY (CN4001)
CN3002 52492-2520
XRESET
JOG2
JOG1
TCH
J_SCLK
J_SDO
SIN
SOUT
SCLK
XCS
J_XSTB
J_BK
VLED
IC3002 TC7SET08FU
IC3007 TC7SET08FU
X3001 DSS1149- 16MHz
IC3004
PEG237B
CONTROL CPU
with FL CONTROLLER
CNVSS
V+5AD
M CN3300
to SLDB ASSY (CN3300)
CN3004 52492-1020
P CN4201
to KSWB ASSY (CN4201)
CN3005 52492-0920

Notes

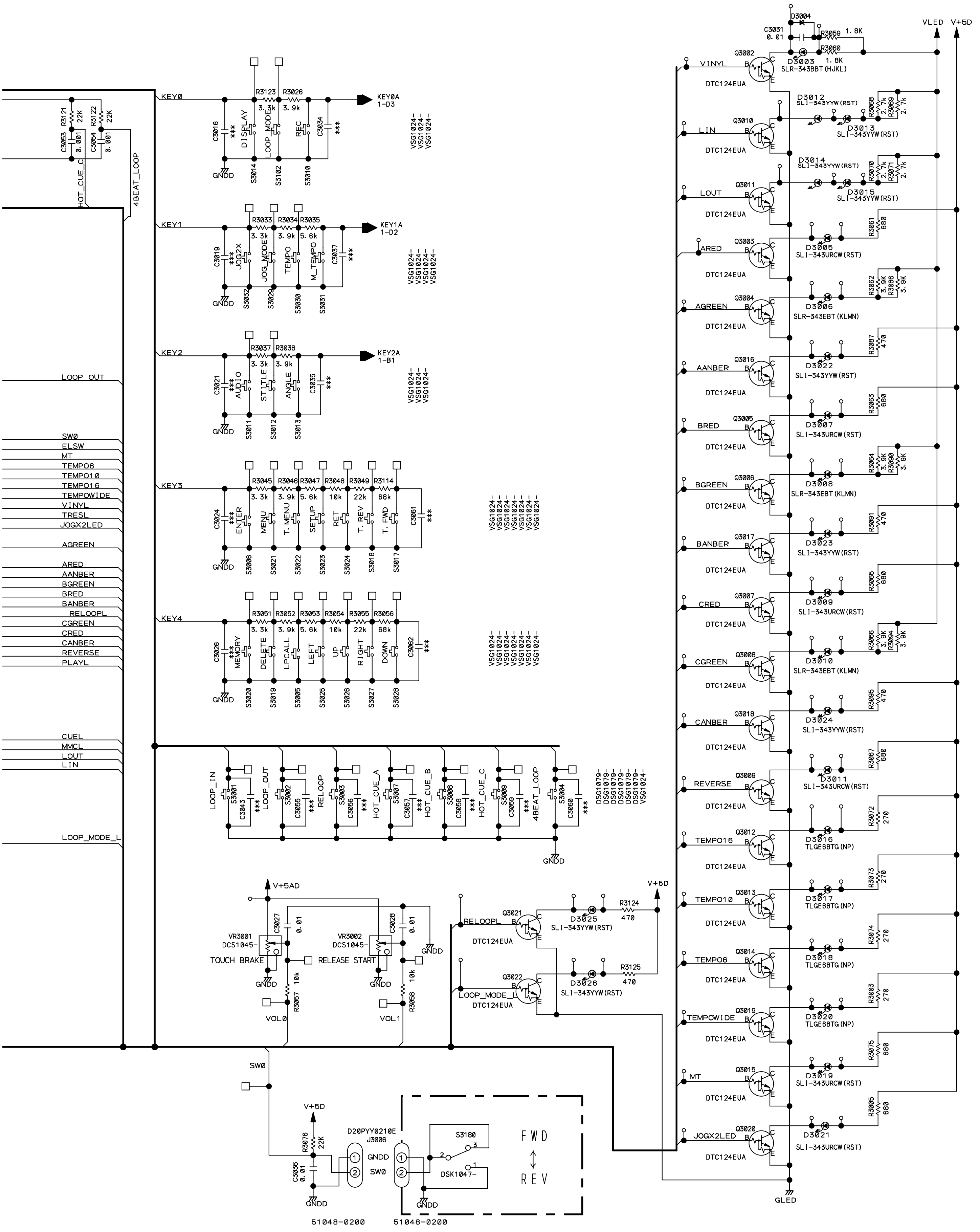
RESISTORS NO MARK (ALL) :RS1/16S***J

CAPACITOR NO MARK :CKSRYB µF
CH :CCSRCH F
NO MARK :CEHAT µF
AT :CEAT µF

*** means STANDBY.

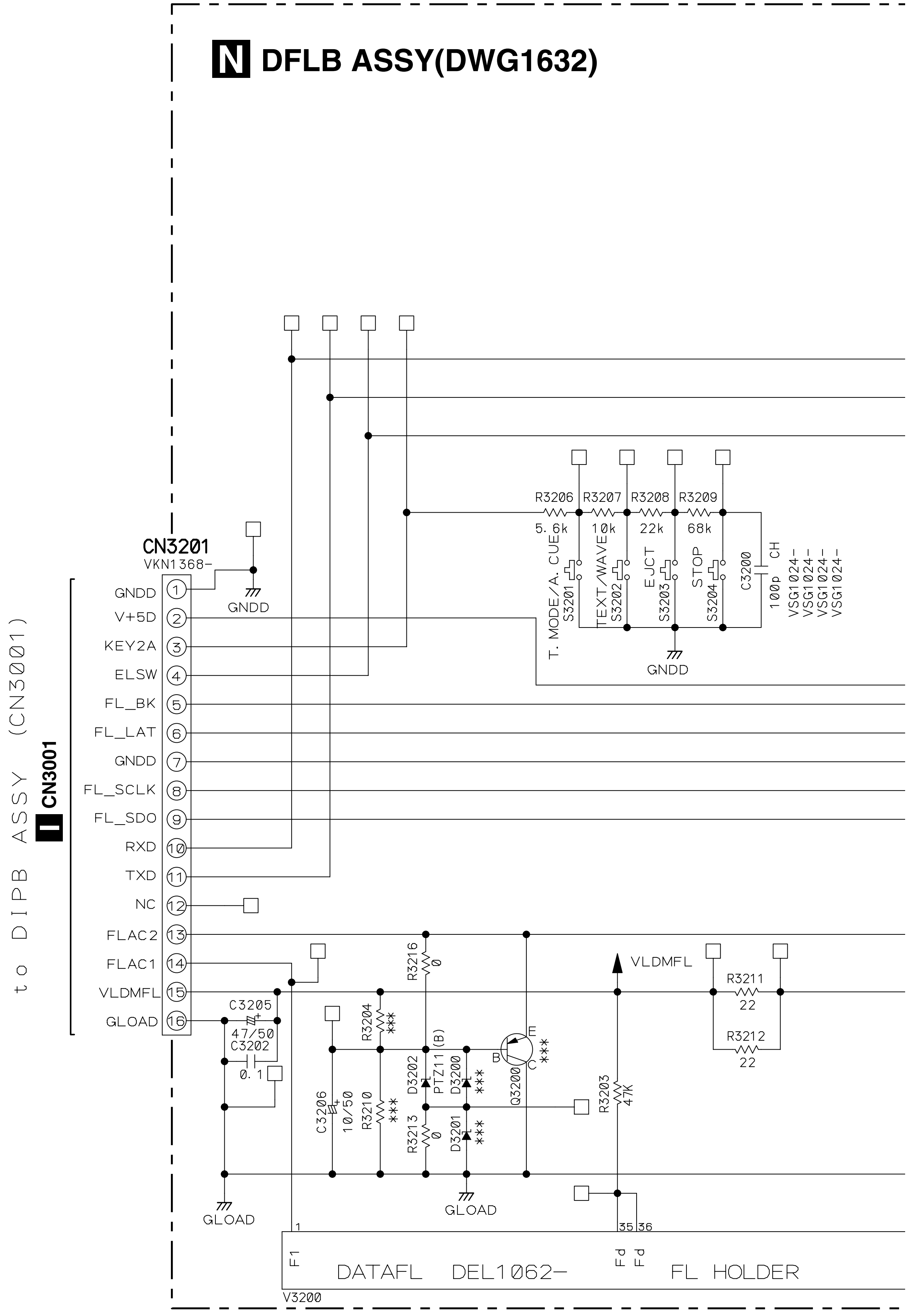
	R3013	R3014	REGION
/KUCXJ	STBY	RS1/16S0R0J	1
/WYXJ5	RS1/16S0R0J	STBY	2
/TLFXJ	RS1/16S182J	RS1/16S332J	3
/TLXJ/RD	RS1/16S332J	RS1/16S183J	4

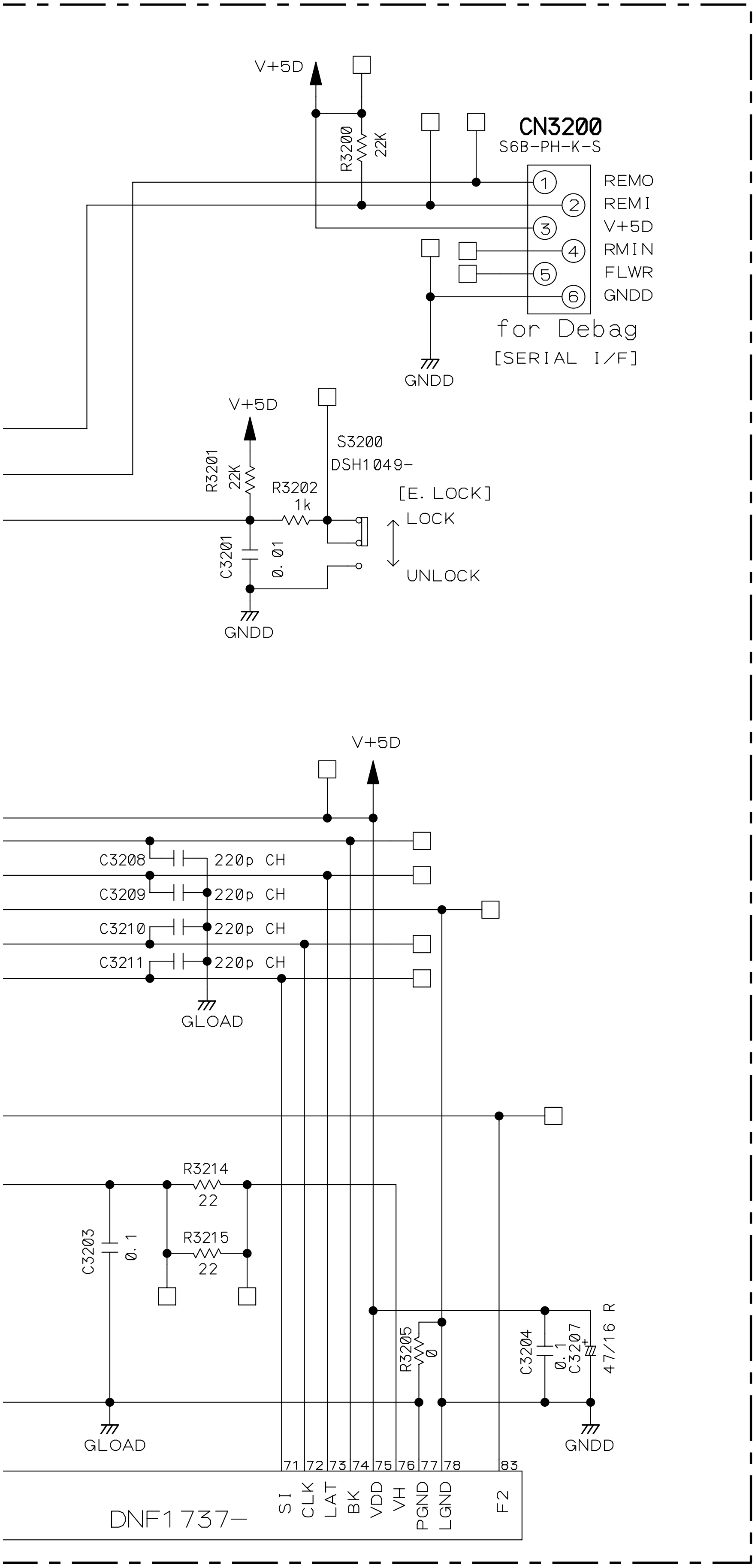
I


KEY0
KEY0A 1-D3
KEY1
KEY1A 1-D2
KEY2
KEY2A 1-B1
KEY3
KEY4
R3121 22K
R3122 22K
C3053 0.001
C3054 0.001
HOT_CUE_C
4BEAT_LOOP
R3123 3.3k
R3026 3.9k
C3016 ***
DISPLAY
LOOP_MODE
REC
C3034 ***
S3014
S3102
S3010
GNDD
VSG1024-
R3033 3.3k
R3034 3.9k
R3035 5.6k
C3019 ***
JOG2X
JOG_MODE
TEMPO
M_TEMPO
C3037 ***
S3032
S3029
S3030
S3031
R3037 3.3k
R3038 3.9k
C3021 ***
AUDIO
STITLE
ANGLE
C3035 ***
S3011
S3012
S3013
R3045 3.3k
R3046 3.9k
R3047 5.6k
R3048 10k
R3049 22k
R3114 68k
C3024 ***
ENTER
MENU
T. MENU
SETUP
RET
T. REV
T. FWD
C3061 ***
S3006
S3021
S3022
S3023
S3024
S3018
S3017
R3051 3.3k
R3052 3.9k
R3053 5.6k
R3054 10k
R3055 22k
R3056 68k
C3026 ***
MEMORY
DELETE
LPCALL
LEFT
UP
RIGHT
DOWN
C3062 ***
S3020
S3019
S3005
S3025
S3026
S3027
S3028
LOOP_OUT
SW0
ELSW
MT
TEMPO6
TEMPO10
TEMPO16
TEMPOWIDE
VINYL
TRESL
JOGX2LED
AGREEN
ARED
AANBER
BGREEN
BRED
BANBER
RELOOPL
CGREEN
CRED
CANBER
REVERSE
PLAYL
CUEL
MMCL
LOUT
LIN
LOOP_MODE_L
LOOP_IN
S3001
C3043 ***
LOOP_OUT
S3002
C3055 ***
RELOOP
S3003
C3056 ***
HOT_CUE_A
S3007
C3057 ***
HOT_CUE_B
S3008
C3058 ***
HOT_CUE_C
S3009
C3059 ***
4BEAT_LOOP
S3004
C3060 ***
DSG1079-
VSG1024-
V+5AD
VR3001 DCS1045-
C3027 0.01
TOUCH BRAKE
R3057 10k
VOL0
VR3002 DCS1045-
C3028 0.01
RELEASE START
R3058 10k
VOL1
Q3021
RELOOPL
DTC124EUA
D3025 SLI-343YYW(RST)
R3124 470
V+5D
Q3022
LOOP_MODE_L
DTC124EUA
D3026 SLI-343YYW(RST)
R3125 470
SW0
V+5D
R3076 22K
C3036 0.01
D20PYY0210E
J3006
GNDD
SW0
S3180
DSK1047-
FWD ↕ REV
51048-0200
51048-0200
VLED
V+5D
C3031 0.01
D3004
R3059 1.8K
R3060 1.8K
D3003 SLR-343BBT(HJKL)
Q3002 VINYL DTC124EUA
D3012 SLI-343YYW(RST)
R3068 2.7K
R3069 2.7K
D3013 SLI-343YYW(RST)
Q3010 LIN DTC124EUA
D3014 SLI-343YYW(RST)
R3070 2.7K
R3071 2.7K
D3015 SLI-343YYW(RST)
Q3011 LOUT DTC124EUA
Q3003 ARED DTC124EUA
D3005 SLI-343URCW(RST)
R3061 680
Q3004 AGREEN DTC124EUA
D3006 SLR-343EBT(KLMN)
R3062 3.9K
R3086 3.9K
Q3016 AANBER DTC124EUA
D3022 SLI-343YYW(RST)
R3087 470
Q3005 BRED DTC124EUA
D3007 SLI-343URCW(RST)
R3063 680
Q3006 BGREEN DTC124EUA
D3008 SLR-343EBT(KLMN)
R3064 3.9K
R3090 3.9K
Q3017 BANBER DTC124EUA
D3023 SLI-343YYW(RST)
R3091 470
Q3007 CRED DTC124EUA
D3009 SLI-343URCW(RST)
R3065 680
Q3008 CGREEN DTC124EUA
D3010 SLR-343EBT(KLMN)
R3066 3.9K
R3094 3.9K
Q3018 CANBER DTC124EUA
D3024 SLI-343YYW(RST)
R3095 470
Q3009 REVERSE DTC124EUA
D3011 SLI-343URCW(RST)
R3067 680
Q3012 TEMPO16 DTC124EUA
D3016 TLGE68TG(NP)
R3072 270
Q3013 TEMPO10 DTC124EUA
D3017 TLGE68TG(NP)
R3073 270
Q3014 TEMPO6 DTC124EUA
D3018 TLGE68TG(NP)
R3074 270
Q3019 TEMPOWIDE DTC124EUA
D3020 TLGE68TG(NP)
R3003 270
Q3015 MT DTC124EUA
D3019 SLI-343URCW(RST)
R3075 680
Q3020 JOGX2LED DTC124EUA
D3021 SLI-343URCW(RST)
R3005 680
GLED
A
B
C
D
E
F

O PSWB ASSY(DWS1392 : /KUCXJ, DWS1396 : /WYXJ5, DWS1396 : /TLFXJ, DWS1396 : TLXJ/RD)

3.18 DFLB ASSY


N DFLB ASSY(DWG1632)
CN3201
VKN1368-
GNDD ①
V+5D ②
KEY2A ③
ELSW ④
FL_BK ⑤
FL_LAT ⑥
GNDD ⑦
FL_SCLK ⑧
FL_SDO ⑨
RXD ⑩
TXD ⑪
NC ⑫
FLAC2 ⑬
FLAC1 ⑭
VLDMFL ⑮
GLOAD ⑯
to DIPB ASSY (CN3001)
I CN3001
GNDD
R3206 5.6k
R3207 10k
R3208 22k
R3209 68k
T. MODE/A. CUE S3201
TEXT/WAVE S3202
EJCT S3203
STOP S3204
C3200 100p CH
VSG1024-
VSG1024-
VSG1024-
VSG1024-
GNDD
C3205 47/50
C3202 0.1
GLOAD
R3216 0
R3204 ***
C3206 10/50
R3210 ***
D3202 PTZ11(B)
D3200 ***
R3213 0
D3201 ***
Q3200 ***
E
B
C
GLOAD
VLDMFL
R3203 47K
R3211 22
R3212 22
1
35 36
F1
Fd Fd
DATAFL DEL1062-
FL HOLDER
V3200

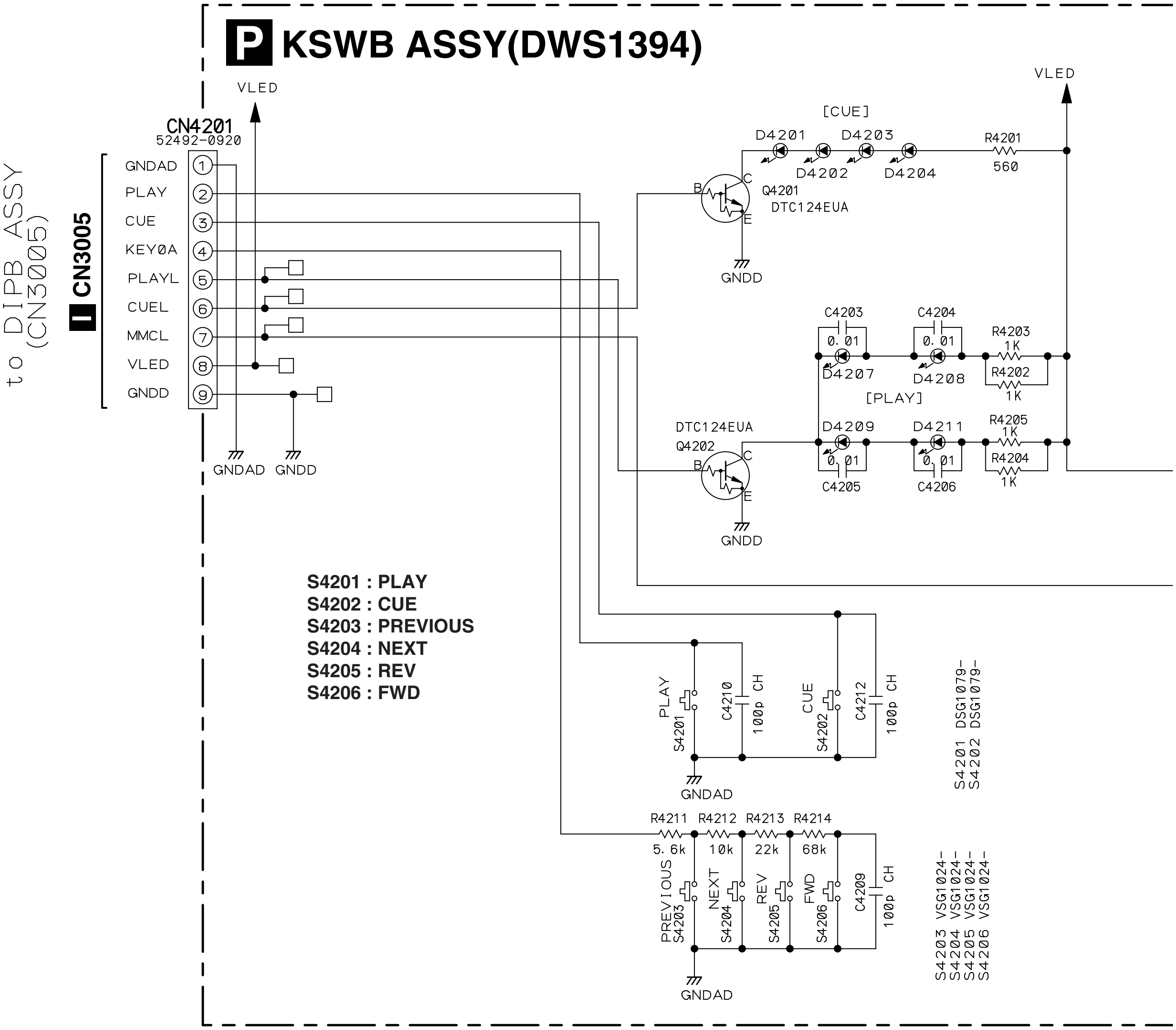

V+5D
R3200
22K
CN3200
S6B-PH-K-S
1
2
3
4
5
6
REMO
REMI
V+5D
RMIN
FLWR
GNDD
for Debag
[SERIAL I/F]
GNDD
V+5D
R3201
22K
R3202
1k
S3200
DSH1049-
[E. LOCK]
LOCK
UNLOCK
C3201
0.01
GNDD
V+5D
C3208
C3209
C3210
C3211
220p CH
220p CH
220p CH
220p CH
GLOAD
R3214
22
R3215
22
C3203
0.1
GLOAD
R3205
0
C3204
0.1
C3207
47/16 R
GNDD
71
72
73
74
75
76
77
78
83
SI
CLK
LAT
BK
VDD
VH
PGND
LGND
F2
DNF1737-

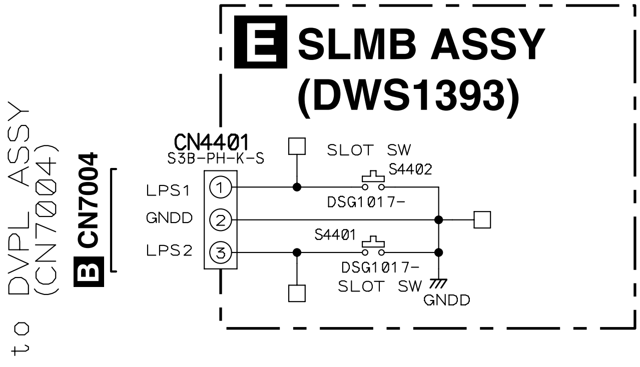
Notes

RESISTORS	∿	NO MARK (ALL)	:RS1/16S***J
CAPACITOR	⊣⊢	NO MARK	:CKSRYB μF
		CH	:CCSRCH F
	+⊣⊢	NO MARK	:CEHAT μF
		R	:CEHAR μF

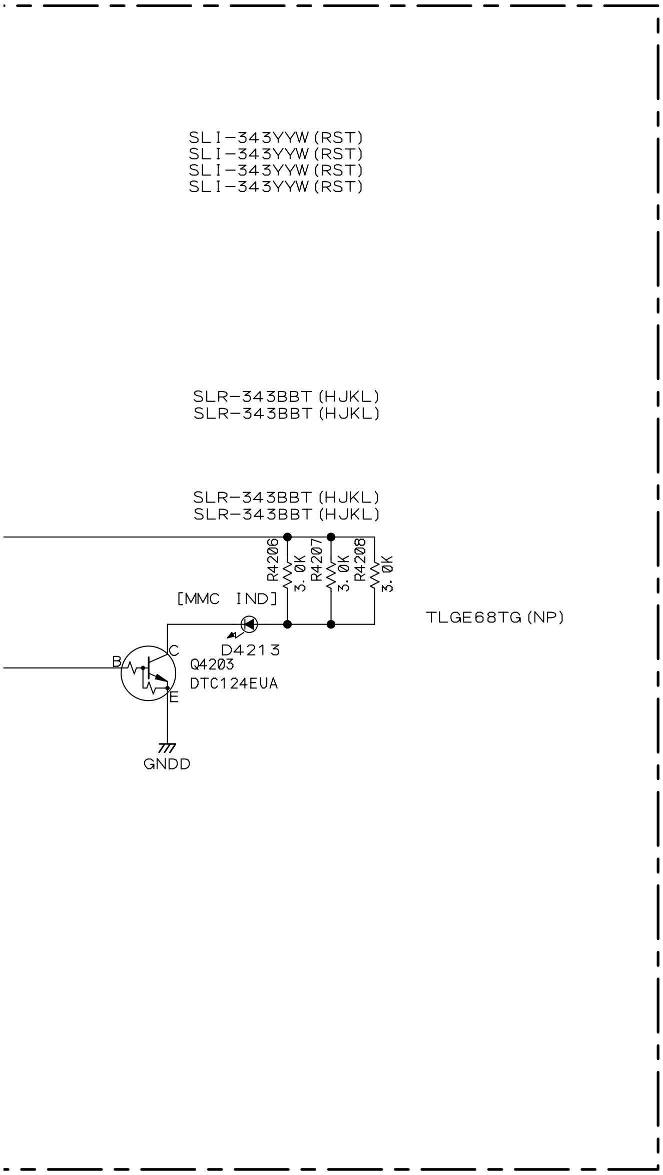
*** means STANDBY.

3.19 KSWB and SLMB ASSYS


P KSWB ASSY(DWS1394)
to DIPB ASSY (CN3005)
I CN3005
CN4201
52492-0920
GNDAD
PLAY
CUE
KEY0A
PLAYL
CUEL
MMCL
VLED
GNDD
VLED
GNDAD GNDD
[CUE]
D4201 D4203
D4202 D4204
R4201 560
Q4201 DTC124EUA
B C E
GNDD
C4203 0.01
C4204 0.01
R4203 1K
R4202 1K
D4207 D4208
[PLAY]
DTC124EUA Q4202
D4209 D4211
R4205 1K
R4204 1K
C4205 C4206
GNDD
S4201 : PLAY
S4202 : CUE
S4203 : PREVIOUS
S4204 : NEXT
S4205 : REV
S4206 : FWD
PLAY S4201
C4210 100p CH
CUE S4202
C4212 100p CH
GNDAD
S4201 DSG1079-
S4202 DSG1079-
R4211 R4212 R4213 R4214
5.6k 10k 22k 68k
PREVIOUS S4203
NEXT S4204
REV S4205
FWD S4206
C4209 100p CH
GNDAD
S4203 VSG1024-
S4204 VSG1024-
S4205 VSG1024-
S4206 VSG1024-


E SLMB ASSY (DWS1393)
to DVPL ASSY (CN7004)
B CN7004
CN4401
S3B-PH-K-S
LPS1
GNDD
LPS2
SLOT SW
S4402
DSG1017-
S4401
DSG1017-
SLOT SW
GNDD

E P


SLI-343YYW (RST)
SLI-343YYW (RST)
SLI-343YYW (RST)
SLI-343YYW (RST)
SLR-343BBT (HJKL)
SLR-343BBT (HJKL)
SLR-343BBT (HJKL)
SLR-343BBT (HJKL)
R4206
3.0K
R4207
3.0K
R4208
3.0K
[MMC IND]
D4213
TLGE68TG (NP)
B
C
E
Q4203
DTC124EUA
GNDD

Notes

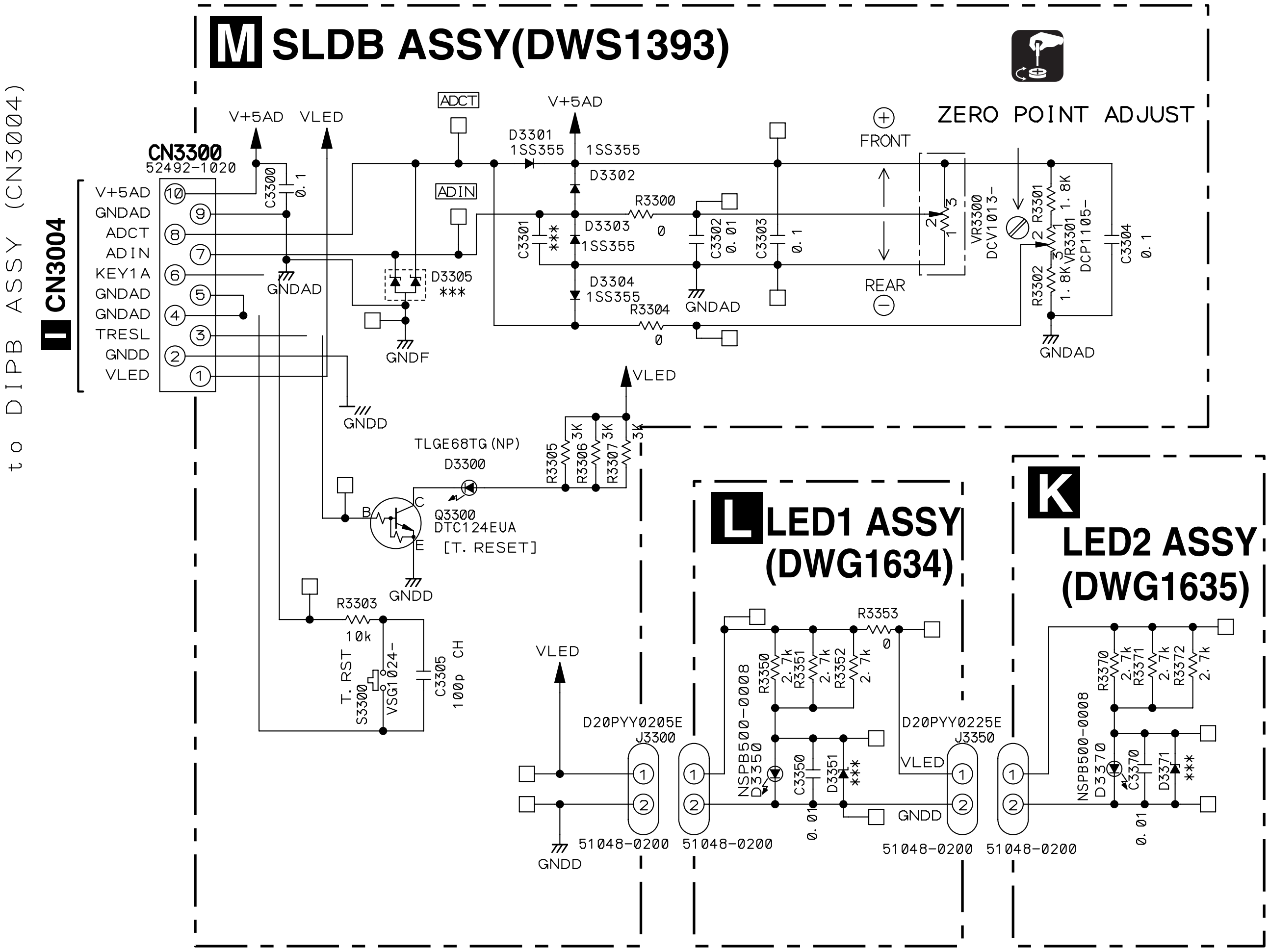
RESISTORS -ᴧᴧᴧ- NO MARK (ALL) : RS1/16S***J

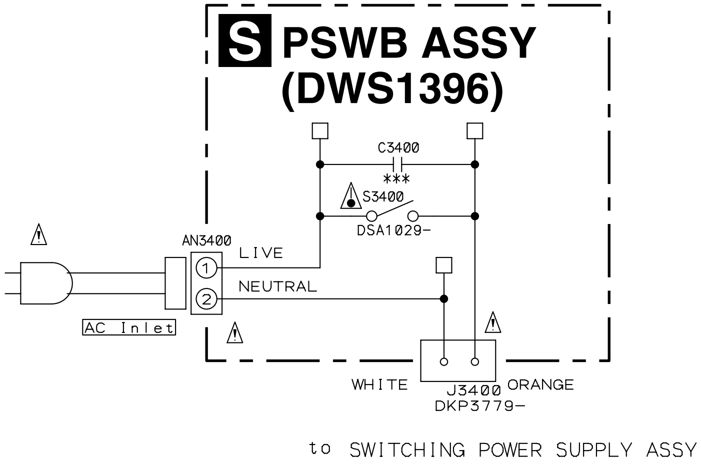
CAPACITOR ⊣⊢ NO MARK : CKSRYB μF

CH : CCSRCH F

*** means STANDBY.

3.20 SLDB, LED1, LED2 and PSWB ASSYS


M SLDB ASSY(DWS1393)
ZERO POINT ADJUST
to DIPB ASSY (CN3004)
I CN3004
CN3300
52492-1020
V+5AD 10
GNDAD 9
ADCT 8
ADIN 7
KEY1A 6
GNDAD 5
GNDAD 4
TRESL 3
GNDD 2
VLED 1
V+5AD
VLED
C3300 0.1
GNDAD
ADCT
ADIN
V+5AD
D3301 1SS355
1SS355
D3302
D3303 1SS355
D3304 1SS355
D3305 ***
C3301 ***
R3300 0
R3304 0
C3302 0.01
C3303 0.1
GNDAD
GNDF
GNDD
FRONT
REAR
VR3300 DCV1013-
R3301 1.8K
VR3301 DCP1105-
R3302 1.8K
C3304 0.1
VLED
R3305 3K
R3306 3K
R3307 3K
TLGE68TG (NP)
D3300
Q3300 DTC124EUA
[T. RESET]
B C E
GNDD
R3303 10k
T. RST S3300 VSG1024-
C3305 100p CH
VLED
D20PYY0205E
J3300
51048-0200
51048-0200
GNDD
L LED1 ASSY (DWG1634)
R3353 0
R3350 2.7k
R3351 2.7k
R3352 2.7k
NSPB500-0008 D3350
C3350 0.01
D3351 ***
D20PYY0225E
J3350
VLED
GNDD
51048-0200
51048-0200
K LED2 ASSY (DWG1635)
R3370 2.7k
R3371 2.7k
R3372 2.7k
NSPB500-0008 D3370
C3370 0.01
D3371 ***


S PSWB ASSY (DWS1396)
C3400 ***
S3400
DSA1029-
AN3400
LIVE
NEUTRAL
AC Inlet
WHITE
J3400
DKP3779-
ORANGE
to SWITCHING POWER SUPPLY ASSY

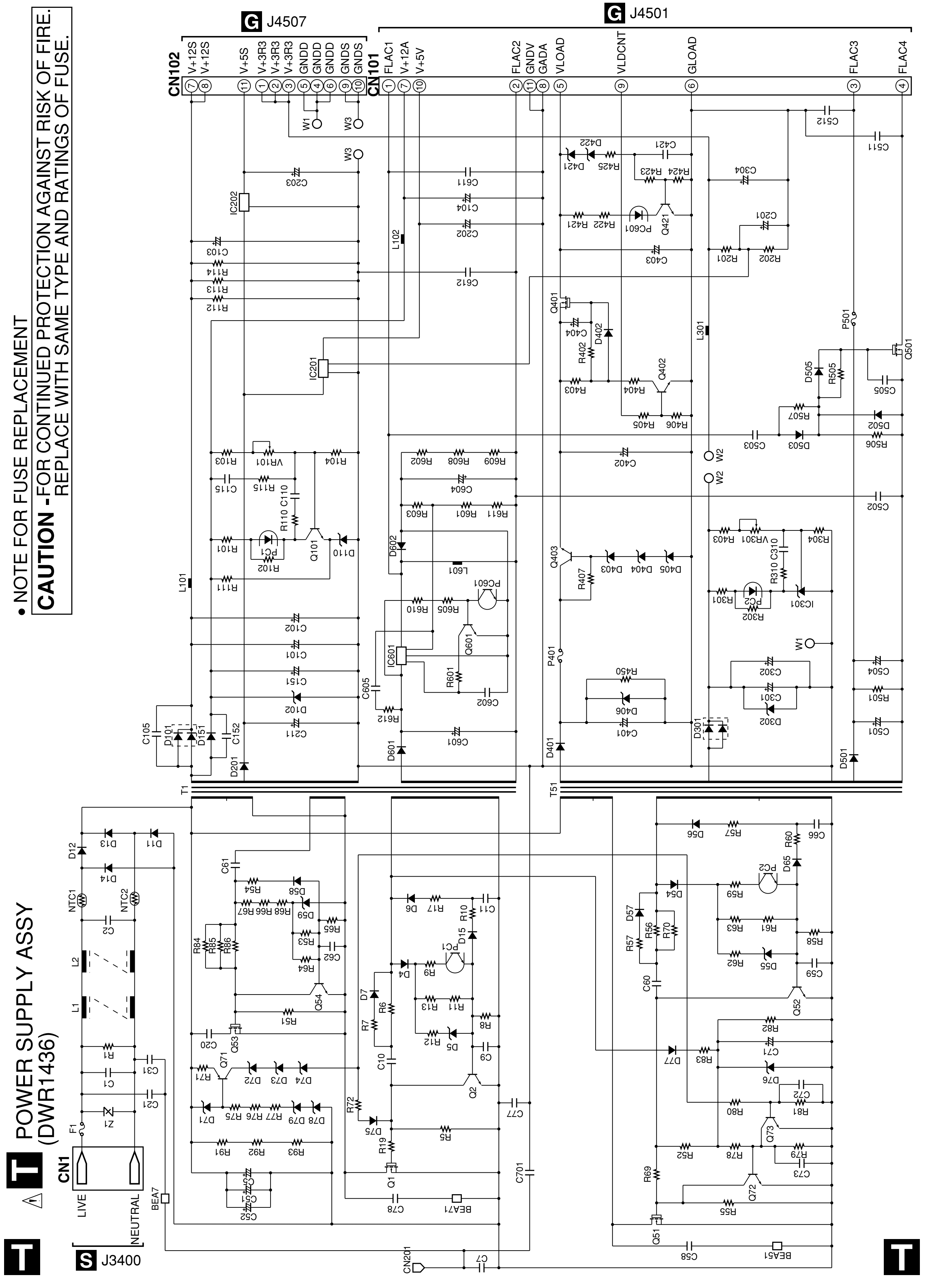
Notes

RESISTORS	-\/\/\/-	NO MARK (ALL)	: RS1/16S***J
CAPACITOR	-||-	NO MARK	: CKSRYB μF
		CH	: CCSRCH F

*** means STANDBY.

The ⚠ mark found on some component parts should be replaced with same parts (safety regulation authorized) of identical designation.

3.21 POWER SUPPLY ASSY



- NOTE FOR FUSE REPLACEMENT

CAUTION - FOR CONTINUED PROTECTION AGAINST RISK OF FIRE. REPLACE WITH SAME TYPE AND RATINGS OF FUSE.

T POWER SUPPLY ASSY (DWR1436)
CN1
LIVE
NEUTRAL
S J3400
G J4507
G J4501
CN102
CN101
V+12S
V+5S
V+3R3
GNDD
GNDS
FLAC1
V+12A
V+5V
FLAC2
GNDV
GADA
VLOAD
VLDCNT
GLOAD
FLAC3
FLAC4
T1
T51
IC201
IC202
IC601
IC301
Q101
Q401
Q402
Q403
Q421
Q501
Q601
Q1
Q2
Q51
Q52
Q53
Q54
Q71
Q72
Q73
PC1
PC2
PC601
F1
Z1
NTC1
NTC2
L1
L2
L101
L102
L301
L601
P401
P501
W1
W2
W3
BEA7
BEA71
BEA51
CN201
VR101
VR301

3.22 WAVEFORMS

■ WAVEFORMS

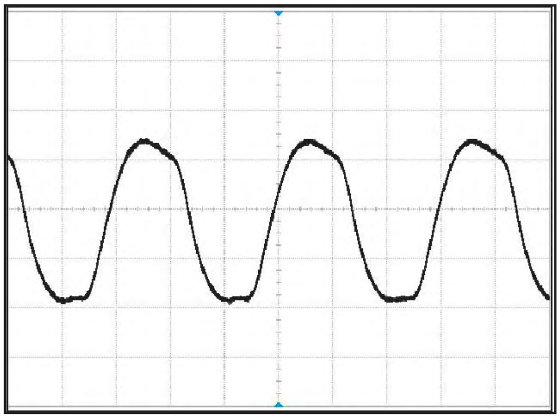
DJ MODE : PLAY, VINAL, SD IN, MASTAR TEMPO : OFF, SRIDER : 0%, HOT CUE : OFF

DISC : DVD-REF-Z1 TITLE35 – 01

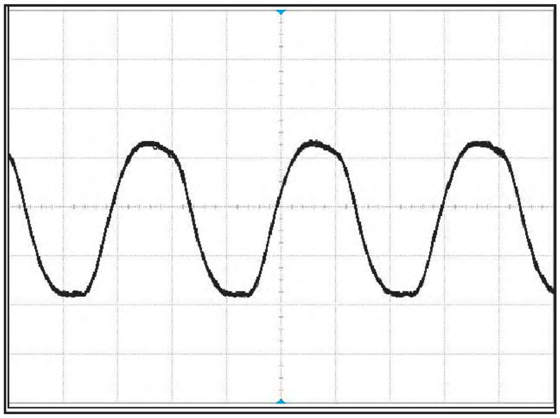
Note : The encircled numbers denote measuring point in the schematic diagram and pcb diagram.

A 1/8 MAIN ASSY

① IC111– 162pin (CLK66M)
V:1V/div. H:5nS/div

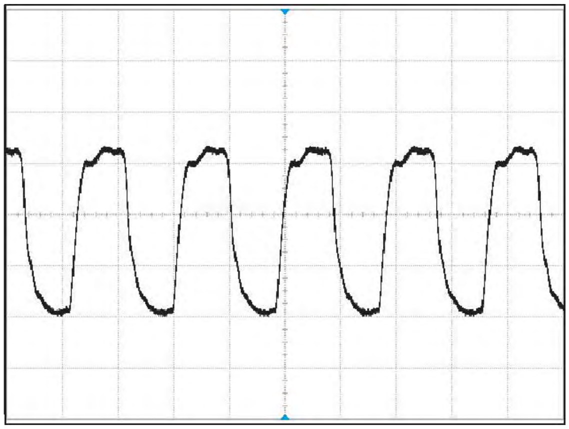


② IC109– 38pin (66MSDR)
V:1V/div. H:5nS/div

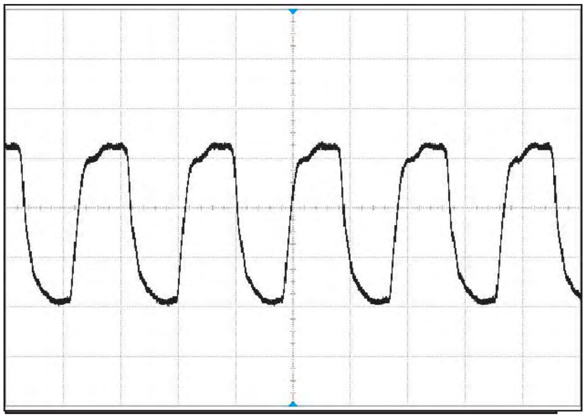


A 2/8 MAIN ASSY

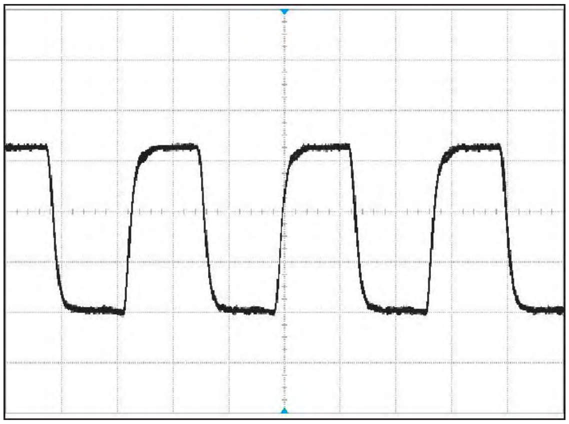
③ IC1707– 7pin (27M_CKGEN)
V:1V/div. H:20nS/div



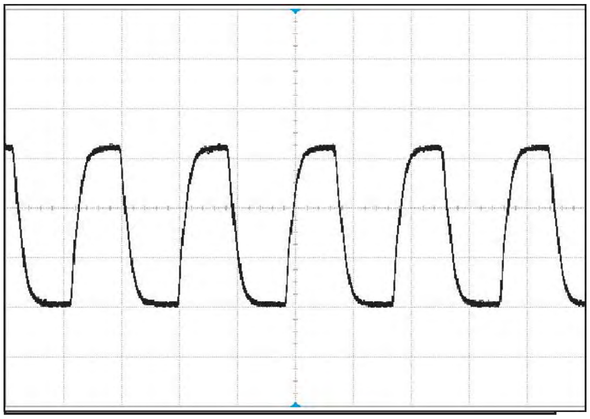
④ IC305– 55pin (27MAV1)
V:1V/div. H:20nS/div



⑤ IC305– 64pin (16/36M)
V:1V/div. H:10nS/div



⑥ IC303– 38pin(MCLK)
V:1V/div. H:10nS/div



A MAIN ASSY

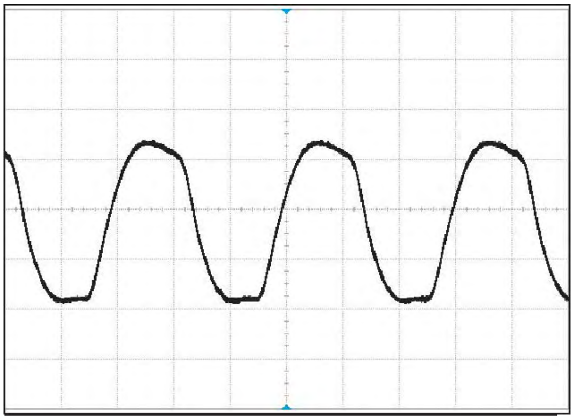
DJ MODE : PLAY, VINAL, SD IN, MASTAR TEMPO :OFF, SRIDER :0%, HOT CUE :OFF

DISC : DVD-REF-Z1 TITLE35– 01

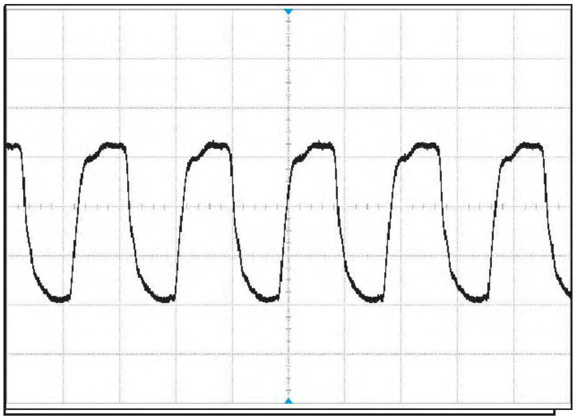
Note : The encircled numbers denote measuring point in the schematic diagram and pcb diagram.

A 3/8 MAIN ASSY

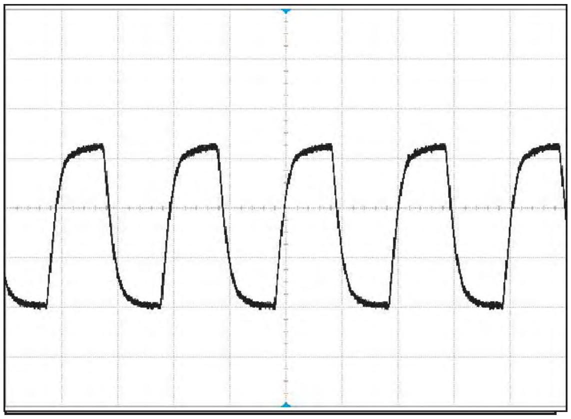
⑦ IC503– 38pin (CARD_66M)
V:1V/div. H:5nS/div



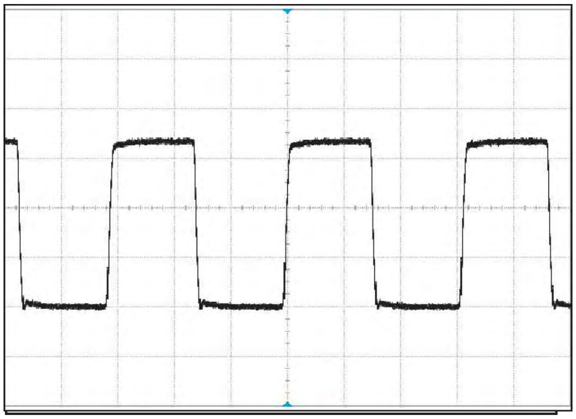
⑧ IC506– 55pin (27M_MOT)
V:1V/div. H:20nS/div



⑨ IC506– 16pin (22/24M)
V:1V/div. H:20nS/div

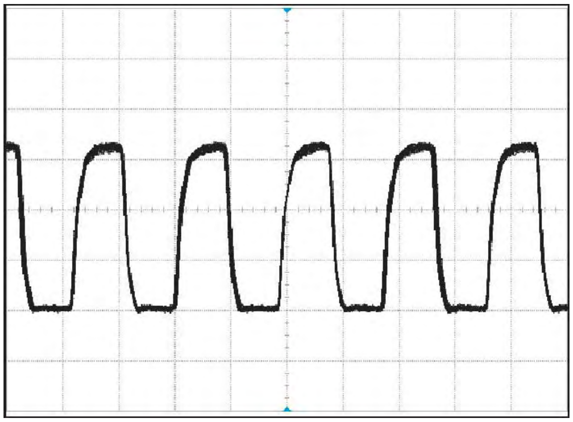


⑩ IC505– 97pin
V:1V/div. H:20nS/div



A 4/8 MAIN ASSY

⑪ IC702– 38pin (SLMCLK)
V:1V/div. H:10nS/div.



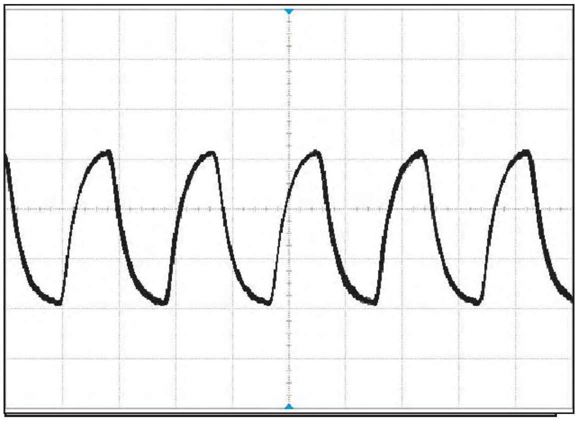
A MAIN ASSY

DJ MODE : PLAY, VINAL, SD IN, MASTAR TEMPO :OFF, SRIDER :0%, HOT CUE :OFF
DISC : DVD-REF-Z1 TITLE35– 01

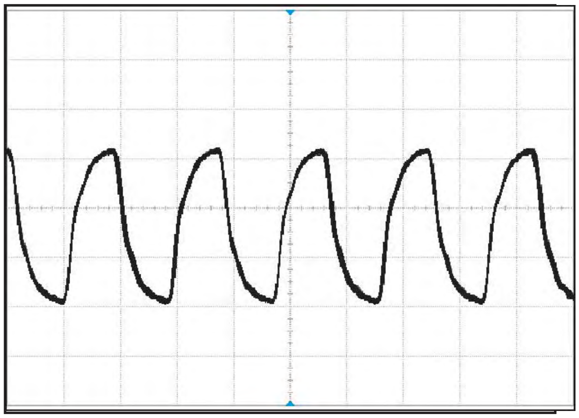
Note : The encircled numbers denote measuring point in the schematic diagram and pcb diagram.

A 5/8 MAIN ASSY

⑫ IC904– 38pin (M1CLK)
V:1V/div. H:2nS/div.

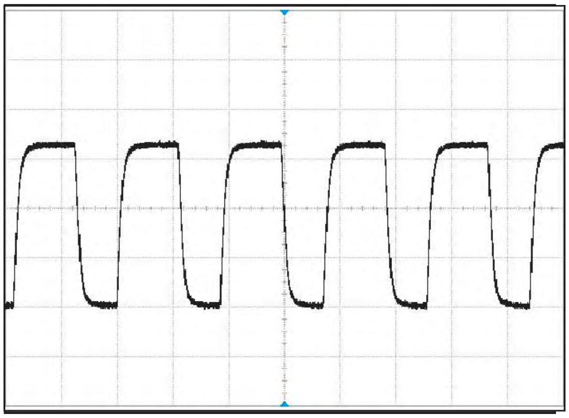


⑬ IC906– 38pin (M2CLK)
V:1V/div. H:2nS/div.

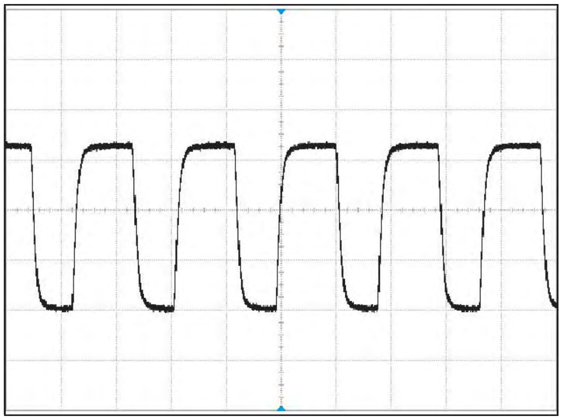


A 6/8 MAIN ASSY

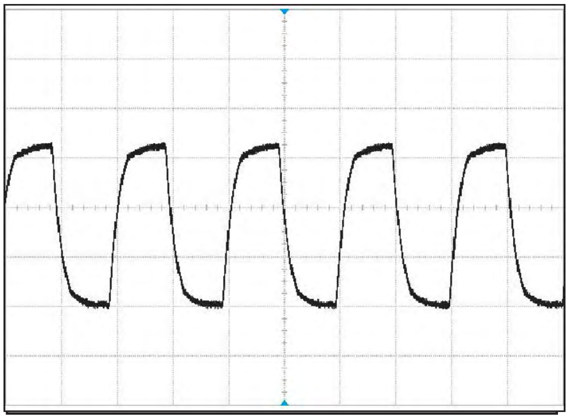
⑭ IC1502– 48pin (M_PRG27M)
V:1V/div. H:20nS/div.



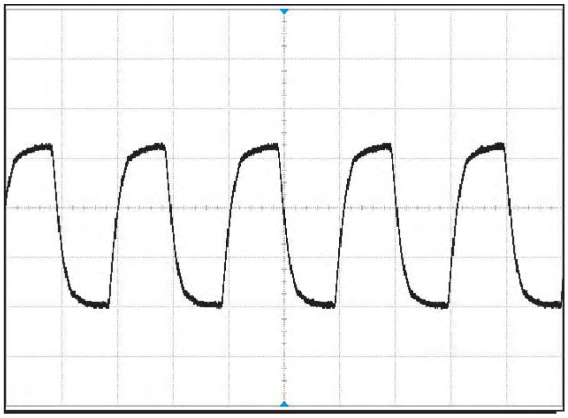
⑮ IC1506– 48pin (S_PRG27M)
V:1V/div. H:20nS/div.



⑯ IC1510– 7pin (22/24M_DAC)
V:1V/div. H:20nS/div.



⑰ IC1513– 6pin (22/24M_AQE)
V:1V/div. H:20nS/div.

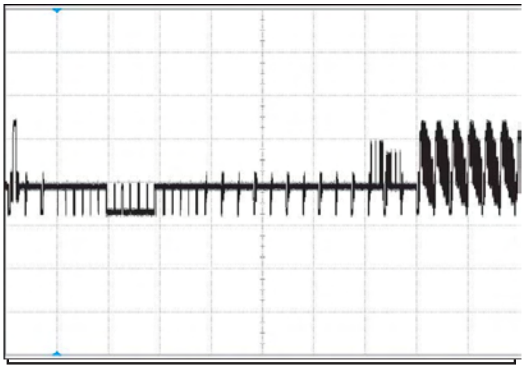


A MAIN ASSY

Note : The encircled numbers denote measuring point in the schematic diagram and pcb diagram.

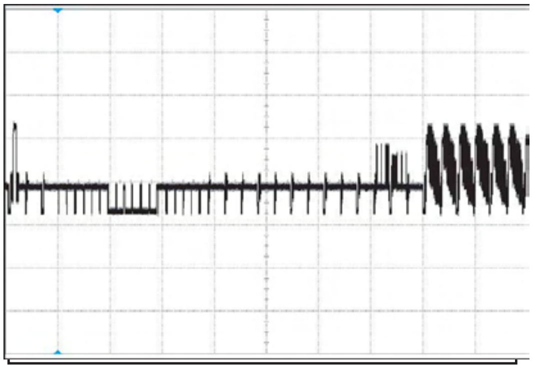
A 7/8 MAIN ASSY

⑱ CN1703-18pin(MAIN_V)
V:1V/div. H:200uS/div.



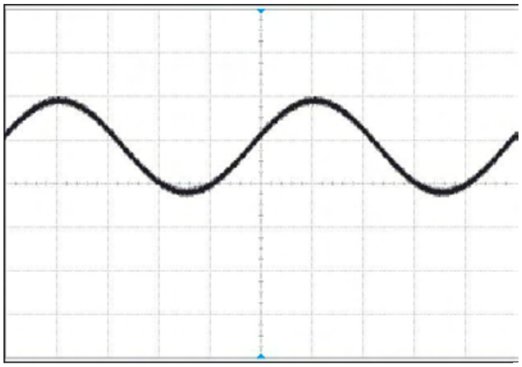
ODD Field

⑲ CN1703-16pin(PRE V)
V:1V/div. H:200uS/div.

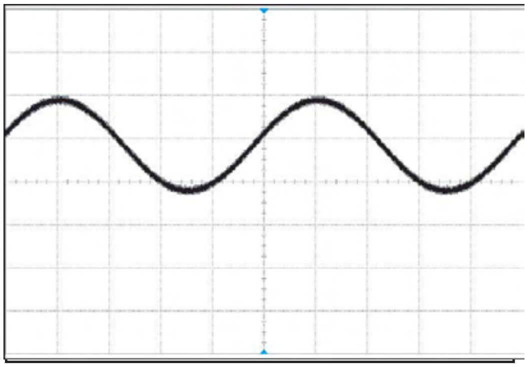


ODD Field

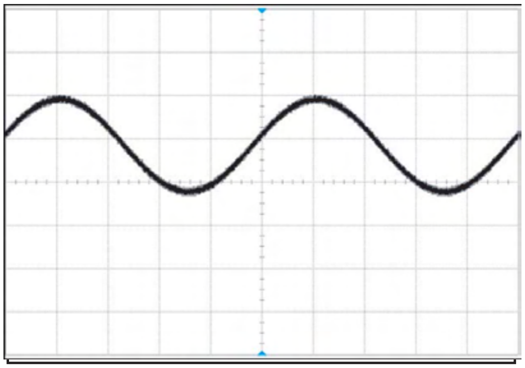
⑳ CN1704-6pin(L-OUT)
V:2V/div. H:200uS/div.



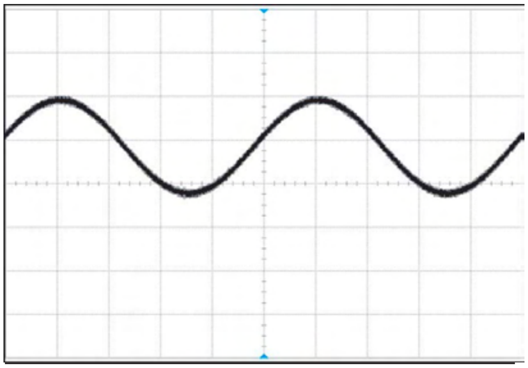
㉑ CN1704-5pin(L+OUT)
V:2V/div. H:200uS/div.



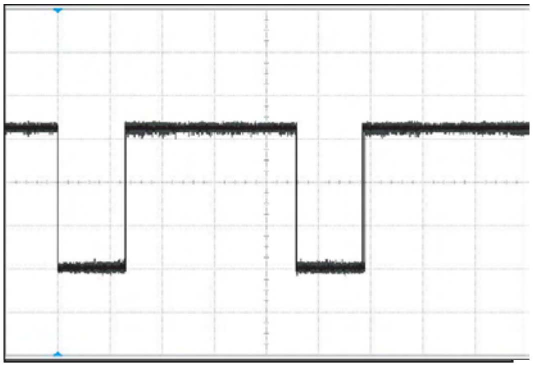
㉒ CN1704-2pin(R+OUT)
V:2V/div. H:200uS/div.



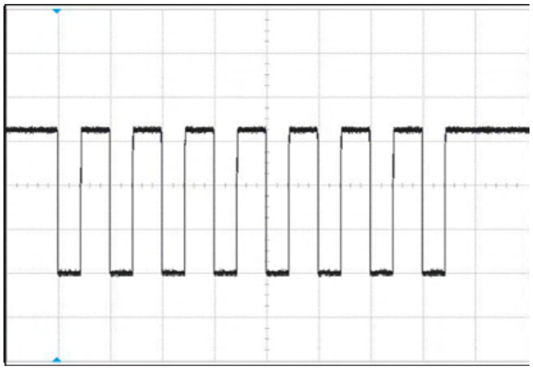
㉓ CN1704-1pin(R-OUT)
V:2V/div. H:200uS/div.



㉔ IC2015-5pin(DIS_XCS)
V:1V/div. H:500uS/div.



㉕ IC2015-7pin(DIS_SCLK)
V:1V/div. H:1uS/div.



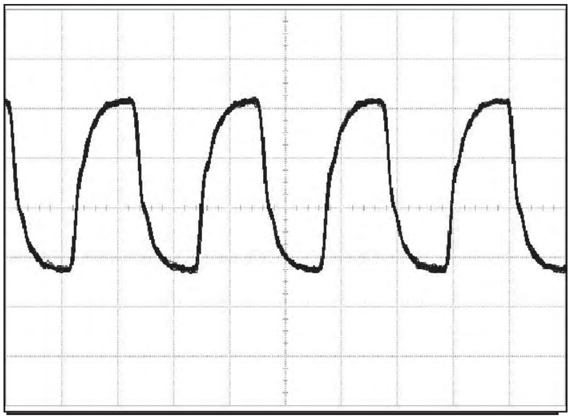
A MAIN ASSY

DJ MODE : PLAY, VINAL, SD IN, MASTAR TEMPO :OFF, SRIDER :0 %, HOT CUE :OFF
DISC : DVD-REF-Z1 TITLE35– 01

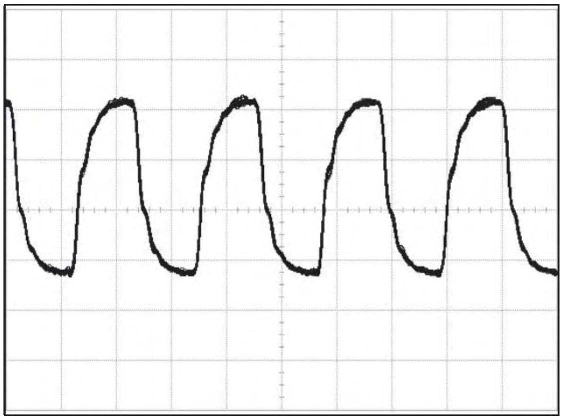
Note : The encircled numbers denote measuring point in the schematic diagram and pcb diagram.

A 8/8 MAIN ASSY

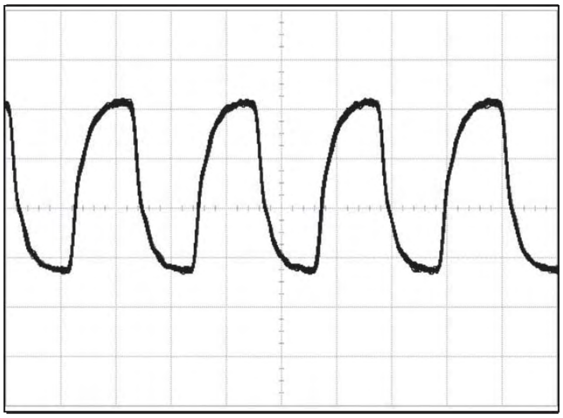
㉖ IC2001-45pin (CK)
V:1V/div. H:2nS/div.



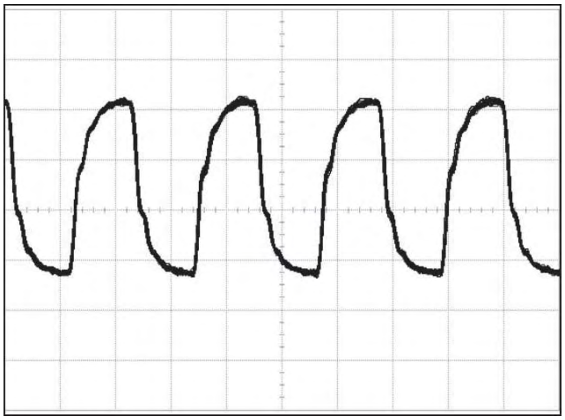
㉗ IC2001-46pin (/CK)
V:1V/div. H:2nS/div.



㉘ IC2003-45pin (CK)
V:1V/div. H:2nS/div.



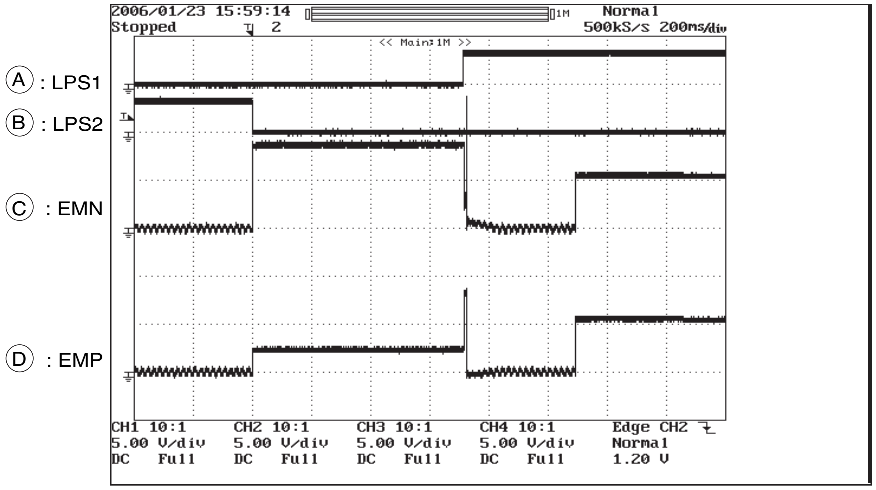
㉙ IC2003-46pin (/CK)
V:1V/div. H:2nS/div.



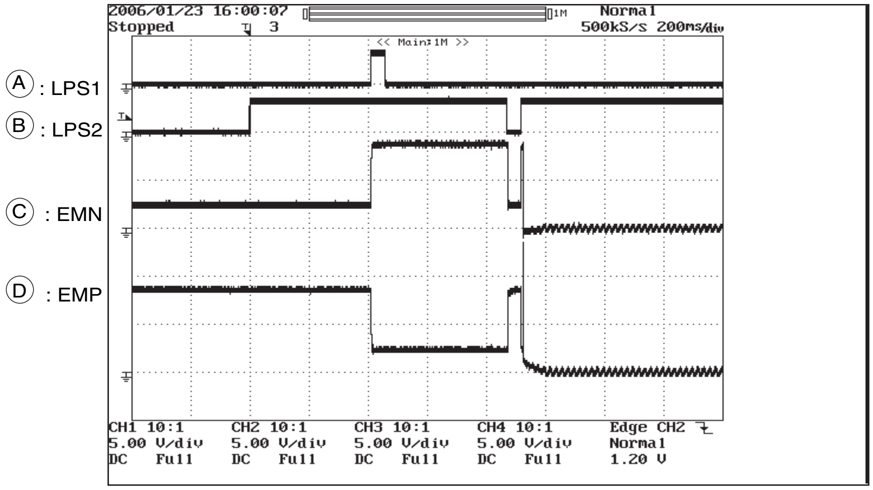
B DVPL ASSY

Note : The encircled numbers denote measuring point in the schematic diagram and pcb diagram.

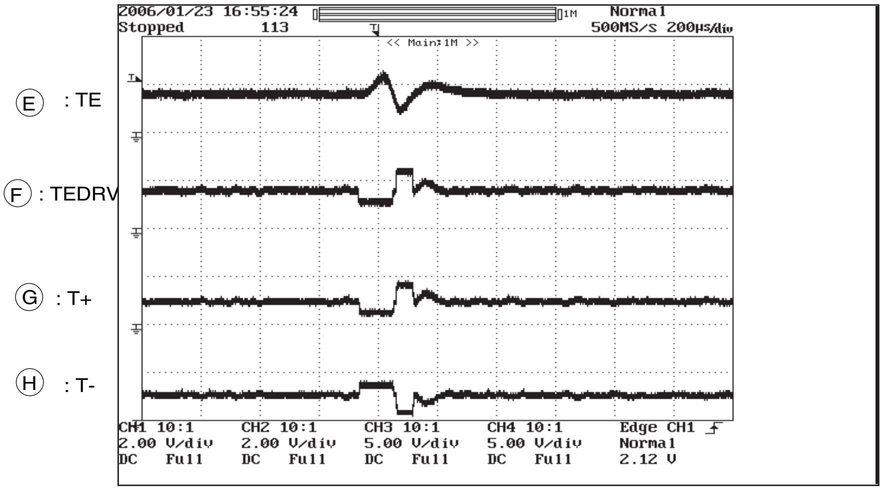
B DVPL ASSY



In a load inn



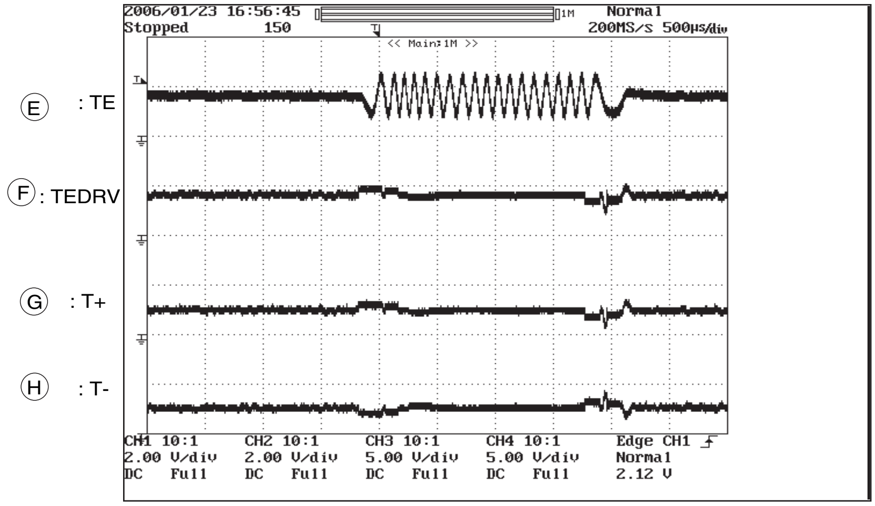
In unloading



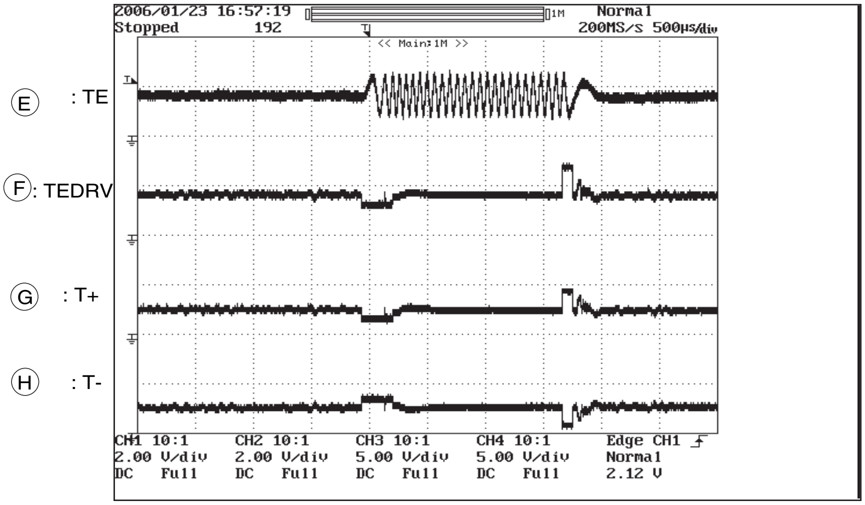
In a CD pose

Note : The encircled numbers denote measuring point in the schematic diagram and pcb diagram.

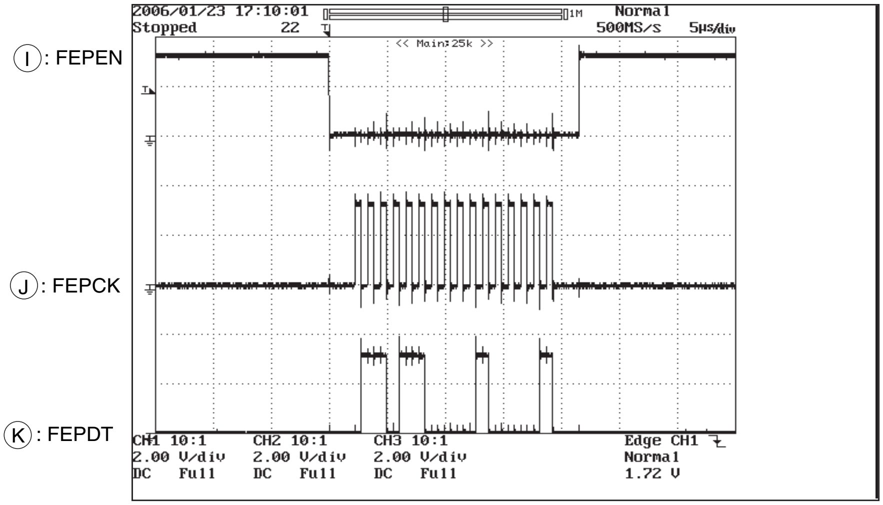
B DVPL ASSY



In CD FWD scan



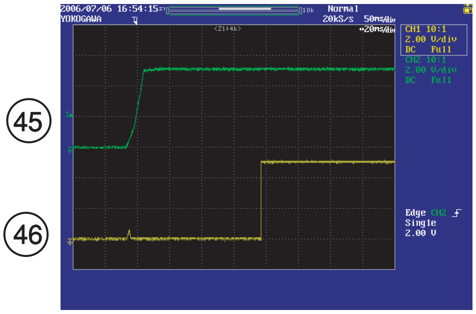
In CD REV scan



WAVEFORMS

Note : The encircled numbers denote measuring point in the schematic diagram and pcb diagram.

I DIPB ASSY



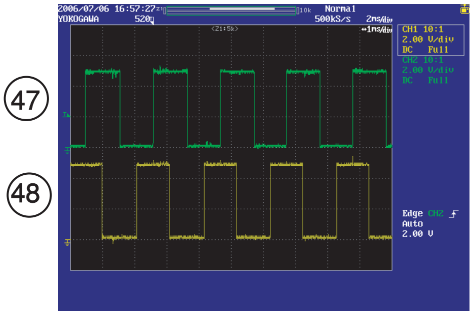
```
2006/07/06 16:54:15 Z1 10k Normal
YOKOGAWA               20kS/s 50ms/div
<Z1:4k>                ↔20ms/div
CH1 10:1
2.00 V/div
DC   Full
CH2 10:1
2.00 V/div
DC   Full
Edge CH2
Single
2.00 V
```

MODE : POWER ON

(45) IC3009-2pin (V+5D)

(46) IC3009-1pin (XRESET)

H JFLB ASSY



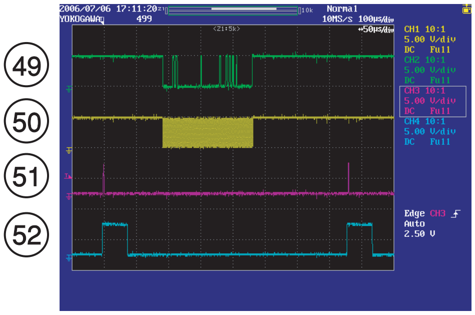
```
2006/07/06 16:57:27 Z1 10k Normal
YOKOGAWA    520        500kS/s 2ms/div
<Z1:5k>                ↔1ms/div
CH1 10:1
2.00 V/div
DC   Full
CH2 10:1
2.00 V/div
DC   Full
Edge CH2
Auto
2.00 V
```

MODE : JOG turns between PLAY

(47) CN4003-1pin (JOG2)

(48) CN4003-2pin (JOG1)

I DIPB ASSY



```
2006/07/06 17:11:20 Z1 10k Normal
YOKOGAWA    499        10MS/s 100µs/div
<Z1:5k>                ↔50µs/div
CH1 10:1
5.00 V/div
DC   Full
CH2 10:1
5.00 V/div
DC   Full
CH3 10:1
5.00 V/div
DC   Full
CH4 10:1
5.00 V/div
DC   Full
Edge CH3
Auto
2.50 V
```

MODE : PLAY

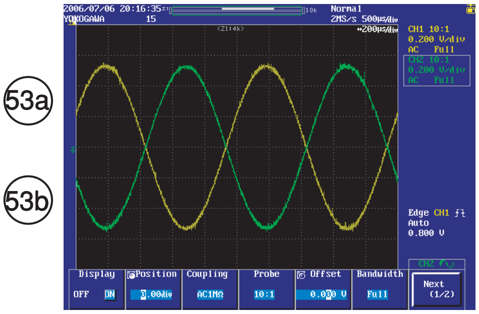
(49) CN3001-8pin (FL_SDO)

(50) CN3001-9pin (FL_SCLK)

(51) CN3001-11pin (FL_LAT)

(52) CN3001-12pin (FL_BK)

Q AUDB ASSY



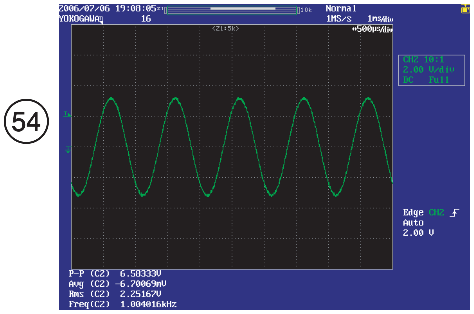
```
2006/07/06 20:16:35 Z1 10k Normal
YOKOGAWA    15         2MS/s 500µs/div
<Z1:4k>                ↔200µs/div
CH1 10:1
0.200 V/div
AC   Full
CH2 10:1
0.200 V/div
AC   Full
Edge CH1
Auto
0.800 V
CH2
Display   Position  Coupling  Probe  Offset   Bandwidth  Next
OFF  ON   0.00div   AC1MΩ     10:1   0.000 V  Full       (1/2)
```

MODE : PLAY (1kHz 0dB)

(53a) CN4300-1pin (R-OUT)

(53b) CN4300-2pin (R+OUT)

R AJCB ASSY



```
2006/07/06 19:08:05 Z1 10k Normal
YOKOGAWA    16         1MS/s  1ms/div
<Z1:5k>                ↔500µs/div
CH2 10:1
2.00 V/div
DC   Full
Edge CH2
Auto
2.00 V
P-P (C2)  6.58333V
Avg (C2) -6.70069mV
Rms (C2)  2.25167V
Freq(C2)  1.004016kHz
```

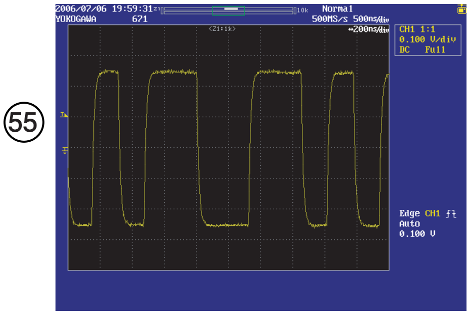
MODE : PLAY (1kHz 0dB)

(54) JA4300-1pin (R)

■ WAVEFORMS

Note : The encircled numbers denote measuring point in the schematic diagram and pcb diagram.

G MJCB ASSY



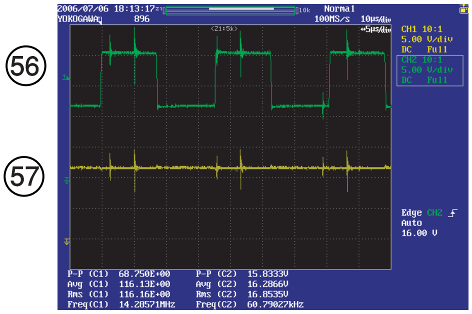
MODE : PLAY

55 JA4503 (D OUT)

Measurement condition: 75 Ω termination

([1.0V] that a level doubles in case of opening)

H JFLB ASSY



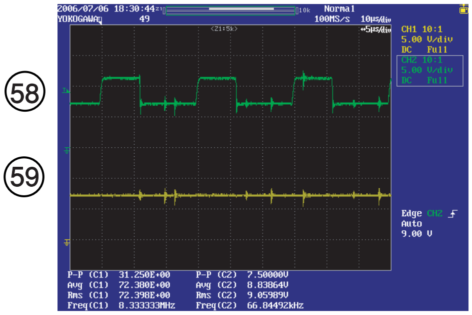
```
P-P (C1)  68.750E+00     P-P (C2)  15.8333V
Avg (C1)  116.13E+00     Avg (C2)  16.2866V
Rms (C1)  116.16E+00     Rms (C2)  16.8535V
Freq(C1)  14.28571MHz    Freq(C2)  60.79027kHz
```

MODE : PLAY

56 CN4000-10pin (FLAC1)

57 CN4000-8pin (FLAC2)

H JFLB ASSY



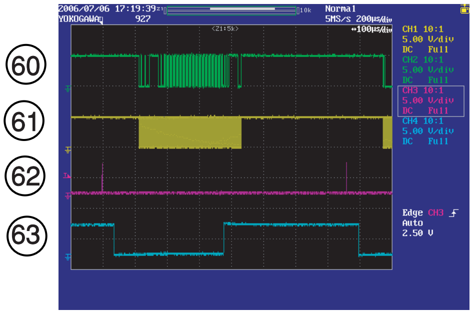
```
P-P (C1)  31.250E+00     P-P (C2)  7.50000V
Avg (C1)  72.380E+00     Avg (C2)  8.83864V
Rms (C1)  72.398E+00     Rms (C2)  9.05989V
Freq(C1)  8.333333MHz    Freq(C2)  66.84492kHz
```

MODE : PLAY

58 CN4000-6pin (FLAC3)

59 CN4000-4pin (FLAC4)

H JFLB ASSY



MODE : PLAY

60 DEL1058-24pin (SI)

61 DEL1058-27pin (CLK)

62 DEL1058-26pin (LAT)

63 DEL1058-25pin (BK)

3.23 VOLTAGES

■ VOLTAGES

DJ MODE : PLAY, VINAL, SD IN, MASTAR TEMPO:OFF, SRIDER:0%, HOT CUE:OFF

A 1/8 MAIN ASSY

IC121(PQ070XZ02ZP)

Pin No.	Voltage(V)
1	3.3
2	3.3
3	2.0
4	1.3
5	0

IC111(HD6417709S)

Pin No.	Voltage(V)
125	3.3
129	3.3
193	3.3

IC101(TC74VHC04FT)

Pin No.	Voltage(V)
2	3.3
4	3.3
10	3.3
12	3.3

IC102(TC74VHC04FT)

Pin No.	Voltage(V)
2	3.3
4	3.3
6	3.3
8	3.3
10	3.3

IC105(TC7SH08FU)

Pin No.	Voltage(V)
4	3.3

A 2/8 MAIN ASSY

IC301(MN1562FF)

Pin No.	Voltage(V)
1	2.5
2	0
3	0
4	1.2
5	3.3
6	0
7	3.3

IC912(NJM2872F33)

Pin No.	Voltage(V)
1	4.9
2	0
3	4.9
4	1.3
5	3.3

A 3/8 MAIN ASSY

IC504(MN1561JF)

Pin No.	Voltage(V)
1	1.8
2	0
3	0
4	0.5
5	3.3
6	0
7	3.3

IC507(MM1661GH)

Pin No.	Voltage(V)
1	1.6
3	0.0
4	0.3
5	3.3
8	3.3

A 5/8 MAIN ASSY

IC902(PQ070XZ02ZP)

Pin No.	Voltage(V)
1	3.3
2	3.3
3	2.5
4	1.3
5	0.0

A 6/8 MAIN ASSY

IC1507(NJM78L05UA)

Pin No.	Voltage(V)
1	5.0
2	0
3	11.9

A 7/8 MAIN ASSY

CN1701(AKM1292–)

Pin No.	Voltage(V)
1	4.9
2	4.9
3	0
4	0
5	11.9
6	0

A 8/8 MAIN ASSY

IC2005(PQ070XZ02ZP)

Pin No.	Voltage(V)
1	3.3
2	3.3
3	2.5
4	1.3
5	0.0

C6081(100uF/6.3V)

Pin No.	Voltage(V)
＋側	1.2

IC2011(LP2996M)

Pin No.	Voltage(V)
1	0.0
2	2.5
3	1.3
4	1.25
5	2.5
6	2.5
7	2.5
8	1.3

VOLTAGES

Measurement condition : DVD Single reconstruction, VINAL, MASTER TEMPO : OFF, SLIDER 0 %

B DVPL ASSY

IC7000 (AN22022A)	
Pin No.	Voltage (V)
1	2.2
2	0.185
3	3.48
4	0.186
5	4.31
6	0.04 – 0.06
7	1.57
8	3.32
9	0
10	3.32
11	0
12	0
13	1.67
14	1.74
15	0
16	2.19
17	1.67
18	1.67
19	0
20	1.61
21	2.23
22	2.21
23	1.92
24	1.92
25	1.71
26	1.51
27	0
28	0
29	1.57
30	2.1
31	1.57
32	1.7
33	2.1
34	2.26
35	2.26
36	2.21
37	2.21
38	4.76
39	2.22
40	2.22
41	3.32
42	2.21
43	2.36
44	2.35
45	4.76
46	2.37
47	2.35
48	2.21

IC7200 (BD7905BFS)	
Pin No.	Voltage (V)
1	3.05
2	3.05
3	3.07
4	3.06
5	3.07
6	3.07
7	1.27
8	0
9	5.3
10	11.9
11	5.31
12	0
13	0
14	0
15	0
16	0
17	0
18	5.3
19	11.9
20	11.9
21	1.59
22	3.3
23	3.3
24	1.77
25	0
26	1.67
27	1.67
28	1.3 – 2.1
29	1.3 – 2.1
30	11.9
31	11.7
32	11.8
33	0.82
34	7 – 12
35	11.5
36	7 – 12
37	6 – 12
38	0
39	0
40	0
41	0
42	0
43	0
44	4.85
45	2.43
46	2.4
47	2.44
48	2.38
49	5.56
50	5.56
51	11.94
52	1.66
53	1.67
54	4.85

IC7100 (MN103S71F)	
Pin No.	Voltage (V)
1	3.3
2	3.3
3	0
4	3.33
5	0
6	3.32
7	0
8	0
9	0
10	1.89
11	1.41
12	1.49
13	1.64
14	1.06
15	0.92
16	3.33
17	0.21
18	2.06
19	1.64
20	0
21	0
22	0.45
23	2.1
24	1.7
25	3.33
26	1.65
27	1.66
28	1.65
29	1.8
30	1.73
31	0
32	3.33
33	2.22
34	2.97
35	2.53
36	2.71
37	2.51
38	2.36
39	2.55
40	2.5
41	0.41
42	1.4
43	1.49
44	0
45	3.33
46	0
47	3.22
48	3.19
49	3.33
50	3.31
51	3.33
52	1.65
53	1.66
54	0
55	1.67
56	1.84
57	1.67
58	1.4
59	0 – 3.2
60	0 – 3.2
61	1.42
62	1.45
63	0
64	0
65	3.33
66	0
67	1.67
68	0
69	0
70	3.33
71	0
72	1.64
73	3.33
74	3.33
75	1.59
76	0
77	1.49
78	0
79	1.2
80	0.18
81	0.21
82	0
83	0.02
84	0
85	1.67
86	1.67
87	3.33
88	0.3

VOLTAGES

B DVPL ASSY

IC 7100 (MN103S71F)

IC7100 (MN103S71F)	
Pin No.	Voltage (V)
89	1.66
90	3.32
91	1.67
92	1.67
93	1.66
94	0
95	1.7
96	1.75
97	0
98	1.63
99	1.65
100	1.67
101	1.5
102	2.21
103	1.52
104	1.66
105	3.32
106	0
107	3.32
108	1.28
109	1.67
110	2.18
111	1.75
112	1.57
113	1.62
114	1.66
115	1.65
116	0
117	0
118	0
119	1.66
120	1.65
121	0
122	3.33
123	0
124	3.33
125	0
126	3.32
127	3.32
128	0
129	3.32
130	0
131	3.28
132	3.28

IC 7100 (MN103S71F)

IC7100 (MN103S71F)	
Pin No.	Voltage (V)
133	0 – 3.2
134	3.28
135	3.33
136	0
137	2.8 – 3.3
138	3.29
139	2.0 – 3.3
140	0
141	3.32
142	1.0 – 1.7
143	3.32
144	0.6 – 1.1
145	0
146	3.33
147	0.7 – 1.2
148	1.2 – 2.0
149	3.29
150	3.33
151	2.88
152	2.88
153	2.9
154	2.9
155	2.9
156	3.33
157	1.49
158	0
159	3.27
160	3.26
161	3.28
162	3.25
163	3.27
164	3.28
165	2.91
166	0
167	3.33
168	2.80 – 3.28
169	2.80 – 3.28
170	2.80 – 3.28
171	2.80 – 3.28
172	4.86
173	3.29
174	0
175	3.28
176	0

IC 7101 (PEA008A8)

IC7101 (PEA008A8)	
Pin No.	Voltage (V)
1	1.86
2	1.42
3	1.63
4	1.04
5	1.12
6	0
7	3.33
8	4.85
9	0
10	0.2
11	2
12	0.6 – 0.9
13	2.18
14	1.7
15	1.62
16	1.68
17	1.67
18	1.67
19	1.88
20	1.8
21	2.24
22	2.98
23	2.5
24	0
25	3.2
26	2.7
27	2.5
28	2.31
29	2.48
30	0.33
31	1.8
32	1.43

IC 7300 (MM1478DFBE)

IC7300 (MM1478DFBE)	
Pin No.	Voltage (V)
1	0
2	3.32
3	4.5
4	4.85
5	3.33
6	0
7	0
8	4.78

IC 7301 (BA15BCOWFP)

IC7301 (BA15BC0WFP)	
Pin No.	Voltage (V)
1	4.85
2	4.43
3	0
4	1.49
5	0

■ VOLTAGES

Measurement condition : DVD Reconstruction (DJ transmission mode), VINYL ON, TEMPO ±10, TEMPO RESET ON, SLIDER 0 %

DIPB ASSY

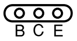
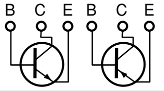
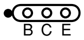
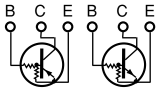
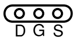
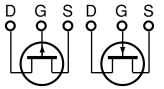
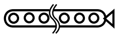
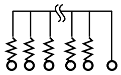
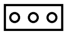
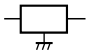
IC3004 (PEG237B-K)	
Pin No.	Voltage (V)
1	0.0
2	0.0
3	5.0
4	0.0
5	0.0
6	5.0
7	0.0
8	0.0
9	0.0
10	0.0
11	0.0
12	5.0
13	2.5
14	0.0
15	2.2
16	5.0
17	5.0
18	0.0
19	0.0
20	0.0
21	0.0
22	0.0
23	0.0
24	0.0
25	0.1
26	5.0
27	5.0
28	3.9
29	0.0
30	1.6
31	0.7
32	5.0
33	4.0
34	5.0
35	0.6
36	2.1
37	4.6
38	3.5
39	0.0
40	0.0
41	0.0
42	0.03
43	0.6
44	0.02
45	2.7
46	5.0
47	5.0
48	5.0
49	5.0
50	0.0

IC3004 (PEG237B-K)	
Pin No.	Voltage (V)
51	5.0
52	0.0
53	0.0
54	0.0
55	0.0
56	0.0
57	0.0
58	0.0
59	0.0
60	0.0
61	0.0
62	5.0
63	0.0
64	0.0
65	0.0
66	5.0
67	5.0
68	0.0
69	0.0
70	5.0
71	5.0
72	0.0
73	0.0
74	5.0
75	0.0
76	0.0
77	0.0
78	0.0
79	0.0
80	5.0
81	5.0
82	5.0
83	5.0
84	5.0
85	5.0
86	5.0
87	5.0
88	2.5
89	2.5
90	2.5
91	5.0
92	5.0
93	5.0
94	5.0
95	5.0
96	0.0
97	5.0
98	5.0
99	5.0
100	0.0

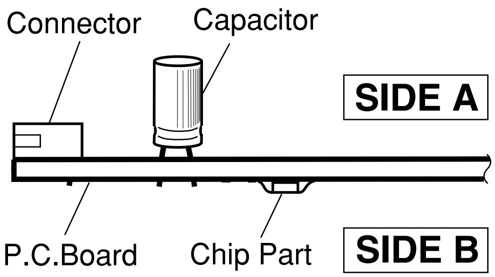
4. PCB CONNECTION DIAGRAM

NOTE FOR PCB DIAGRAMS :

1. Part numbers in PCB diagrams match those in the schematic diagrams.
2. A comparison between the main parts of PCB and schematic diagrams is shown below.

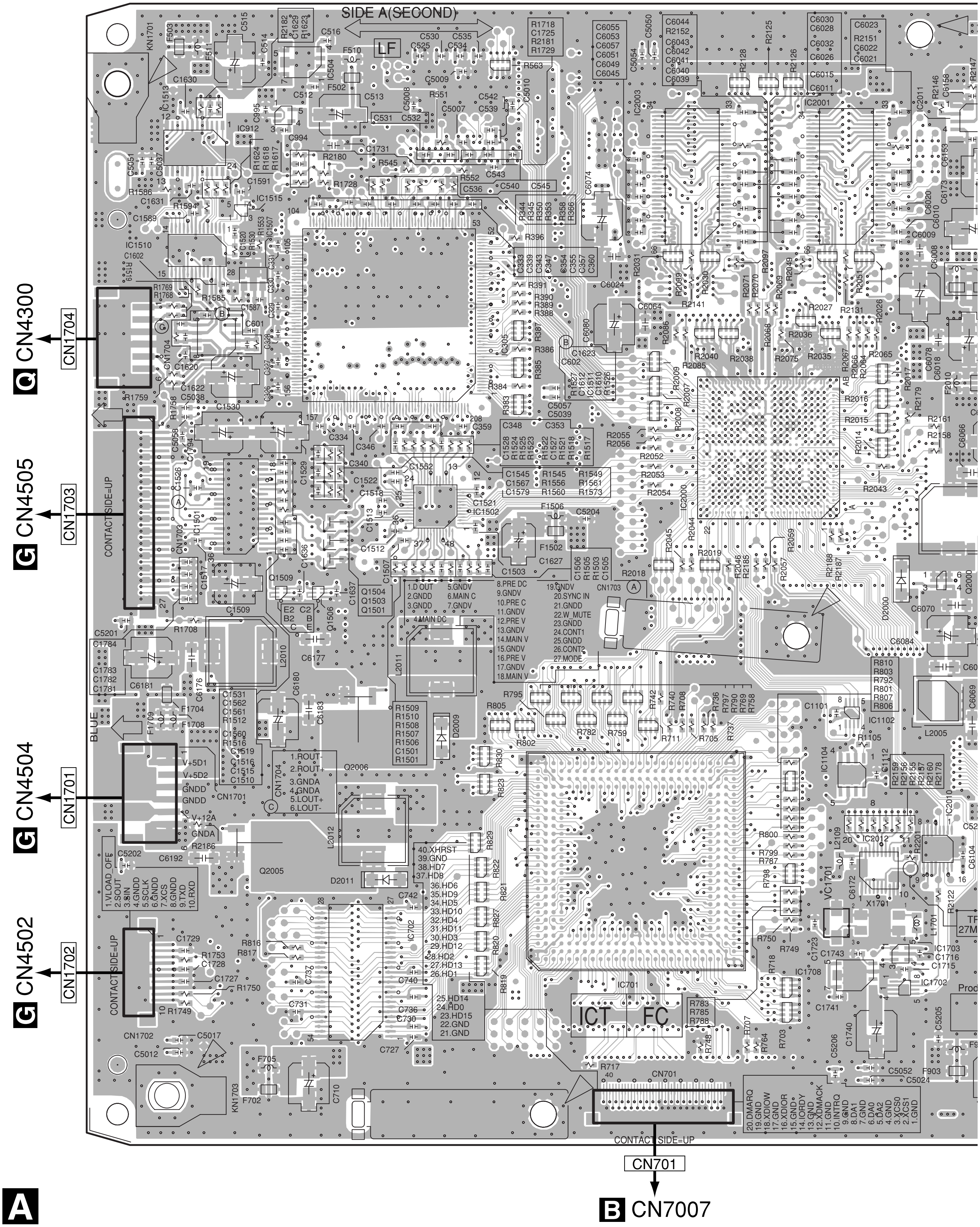
Symbol In PCB Diagrams	Symbol In Schematic Diagrams	Part Name
 B C E	 B C E B C E	Transistor
 B C E	 B C E B C E	Transistor with resistor
 D G S	 D G S D G S	Field effect transistor
		Resistor array
		3-terminal regulator

3. The parts mounted on this PCB include all necessary parts for several destinations.
   For further information for respective destinations, be sure to check with the schematic diagram.
4. View point of PCB diagrams.



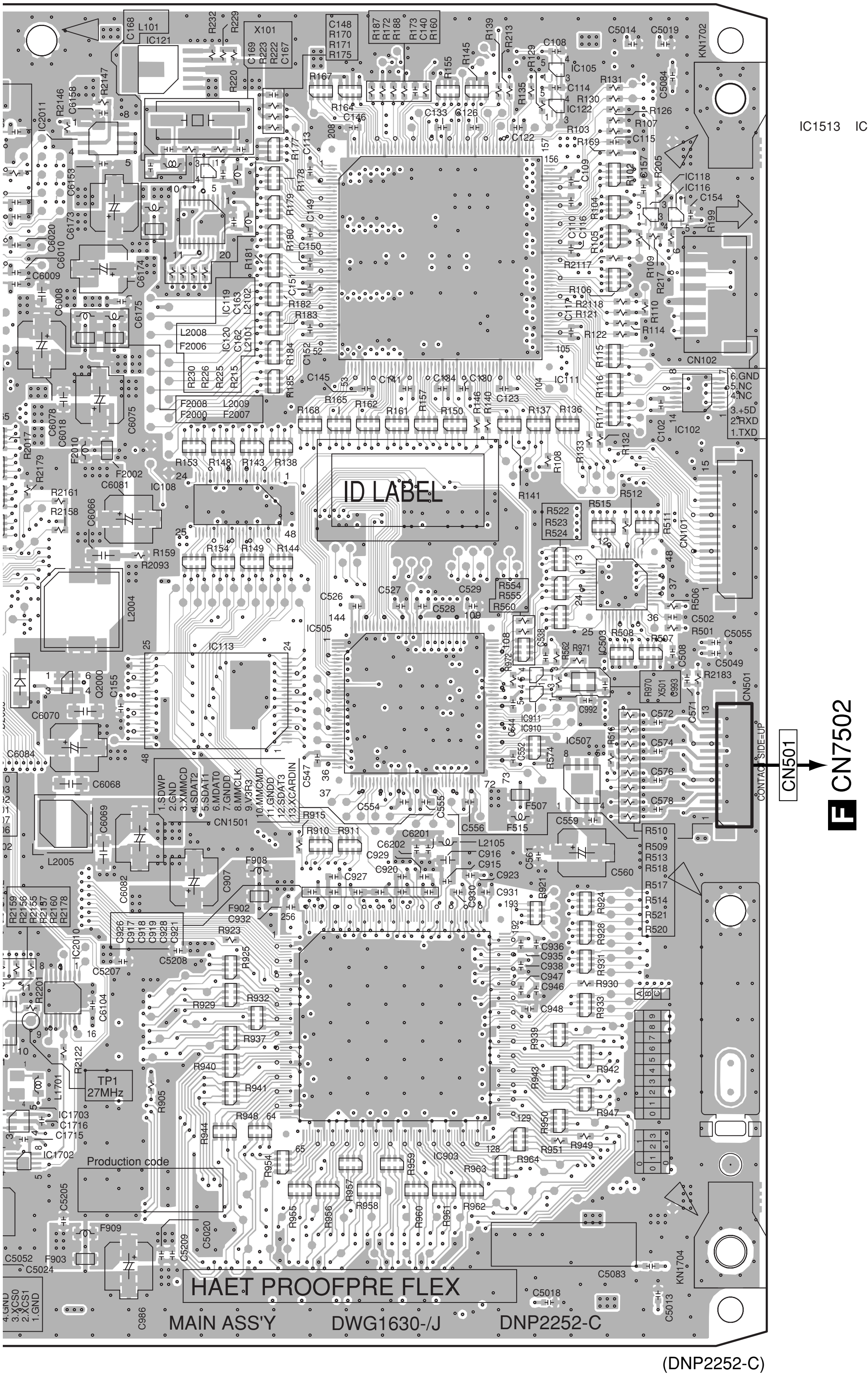
4.1 MAIN ASSY

SIDE A **A** MAIN ASSY


SIDE A(SECOND)
LF
Q CN4300
CN1704
G CN4505
CN1703
CONTACT SIDE=UP
G CN4504
CN1701
BLUE
G CN4502
CN1702
CONTACT SIDE=UP
CN701
CONTACT SIDE=UP
B CN7007
IC2000
IC2001
IC2003
IC701
IC1502
IC1501
IC1510
IC1513
IC1515
IC912
IC504
IC2012
IC2010
IC1101
IC1102
IC1104
IC1703
IC1702
IC1708
IC702
IC305
X1701
VC1701
ICT
FC
KN1701
KN1703
Q2005
Q2006
Q2000
Q1504
Q1503
Q1501
Q1509
Q1506
D2000
D2009
D2011
L2005
L2010
L2011
L2012
1.D OUT
2.GNDD
3.GNDD
4.MAIN DC
5.GNDV
6.MAIN C
7.GNDV
8.PRE DC
9.GNDV
10.PRE C
11.GNDV
12.PRE V
13.GNDV
14.MAIN V
15.GNDV
16.PRE V
17.GNDV
18.MAIN V
19.GNDV
20.SYNC IN
21.GNDD
22.W_MUTE
23.GNDD
24.CONT1
25.GNDD
26.CONT2
27.MODE
V+5D1
V+5D2
GNDD
GNDD
V+12A
GNDA
1.ROUT-
2.ROUT+
3.GNDA
4.GNDA
5.LOUT+
6.LOUT-
1.VLOAD_OFF
2.SOUT
3.SIN
4.GNDD
5.SCLK
6.GNDD
7.XCS
8.GNDD
9.TXD
10.RXD
40.XHRST
39.GND
38.HD7
37.HD8
36.HD6
35.HD9
34.HD5
33.HD10
32.HD4
31.HD11
30.HD3
29.HD12
28.HD2
27.HD13
26.HD1
25.HD14
24.HD0
23.HD15
22.GND
21.GND
20.DMARQ
19.GND
18.XDIOW
17.GND
16.XDIOR
15.GND
14.IORDY
13.GND
12.XDMACK
11.GND
10.INTRQ
9.GND
8.DA1
7.GND
6.DA0
5.DA2
4.XCS0
3.XCS1
1.GND
TP
27M
Prod

A

SIDE A


ID LABEL
HAET PROOFPRE FLEX
MAIN ASS'Y
DWG1630-/J
DNP2252-C
Production code
TP1
27MHz
CN501
CONTACT SIDE=UP
F CN7502
6.GND
5.NC
4.NC
3.+5D
2.RXD
1.TXD
1.SDWP
2.GND
3.XMMCD
4.SDAT2
5.SDAT1
6.MDAT0
7.GNDD
8.MMCLK
9.V3R3
10.MMCMD
11.GNDD
12.SDAT3
13.XCARDIN
CN1501
4.GND
3.XCS0
2.XCS1
1.GND
KN1702
KN1704
CN102
CN101
IC113
IC505
IC108
IC111
IC903

(DNP2252-C)

IC121

IC105

IC504
IC122
IC1513 IC2003 IC2001 IC2011

IC912
IC118
IC116

IC1515

IC1507
IC1510

IC119

IC120

IC111

IC305
IC102

IC1511

IC108

IC2000
IC1502
IC1501
IC505
IC113 IC503

Q1509
Q1504 Q2000
Q1503
IC910 IC911 Q1501
IC507 Q1506

IC1102

IC1104 Q2006

IC2010

IC2012

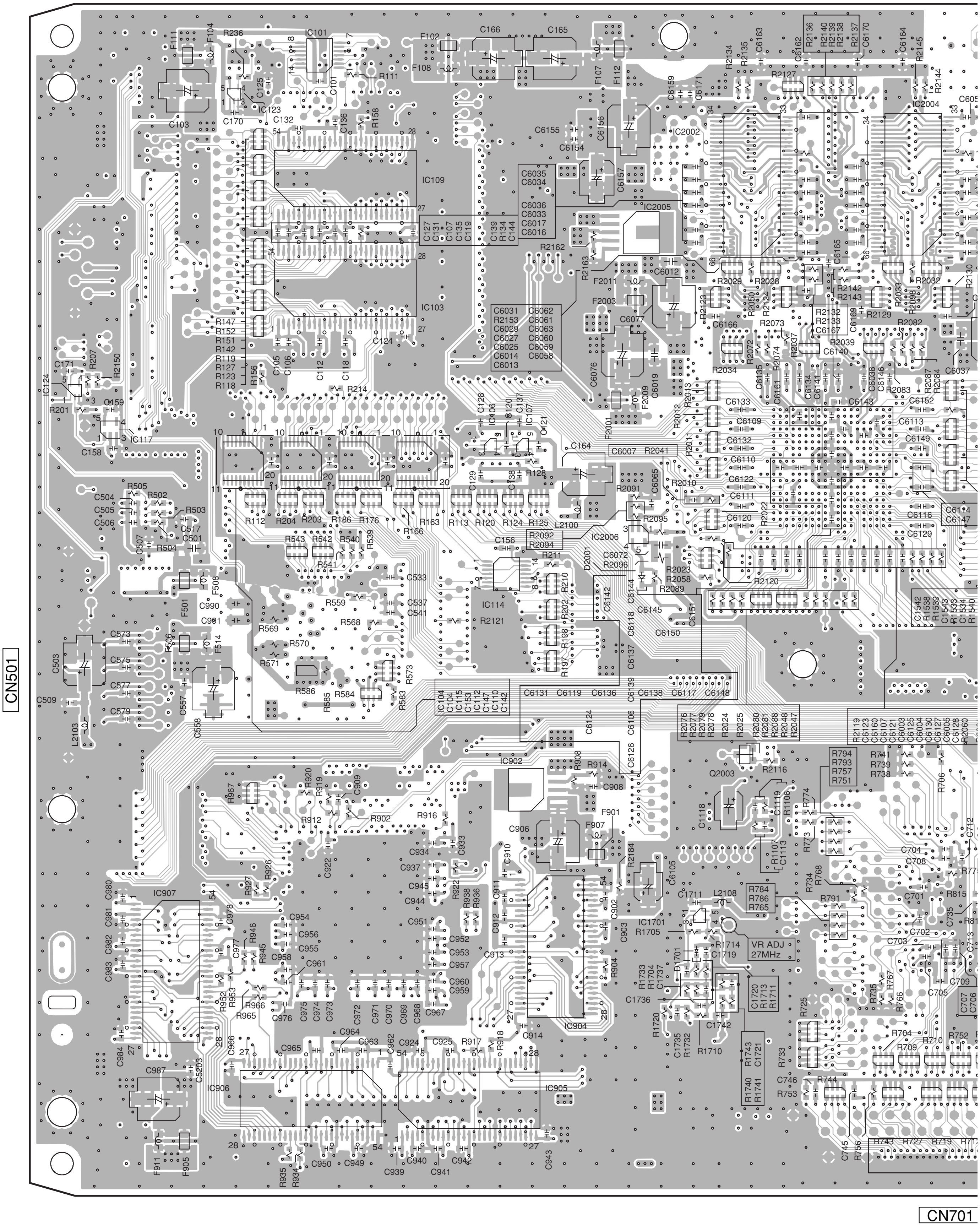
Q2005

IC702
IC1703

IC1708
IC701 IC1702 IC903

SIDE B

A MAIN ASSY


A
B
C
D
E
F
CN501
CN701
IC101
IC103
IC109
IC114
IC117
IC123
IC124
IC902
IC904
IC905
IC906
IC907
IC1701
IC2002
IC2004
IC2005
IC2006
F102
F104
F107
F108
F112
F501
F508
F514
F901
F905
F907
F911
F2001
F2003
F2009
F2011
C103
C165
C166
C170
C164
C6155
C6154
C6156
C6157
C6159
C6171
R236
R111
R158
R2162
R2163
L2100
L2103
L2108
D1701
D2001
Q2003
R2116
VR ADJ
27MHz
R1714
C1719
R1705
C1711
R784
R786
R765
R794
R793
R757
R751
R741
R739
R738
R706
R774
R773
R768
R734
R791
R815
C701
C702
C703
C704
C705
C706
C707
C708
C709
C735
C745
C746
R704
R709
R710
R725
R733
R735
R743
R744
R752
R753
R756
R766
R767
R719
R727
C6007
R2041
R2091
R2092
R2094
R2095
R2096
R2120
R2121
C6131
C6119
C6136
C6139
C6138
C6117
C6148
R2076
R2077
R2079
R2078
R2024
R2025
R2080
R2081
R2088
R2048
R2047
R2119
R2060
C6031
C6062
R2153
C6061
C6029
C6063
C6027
C6060
C6025
C6059
C6014
C6058
C6013
C6035
C6034
C6036
C6033
C6017
C6016
C127
C131
C107
C135
C119
C139
R134
C144
IC104
IC115
C104
C153
C112
C147
C110
C142
R147
R152
R151
R142
R119
R127
R123
R118
R156
R112
R204
R203
R186
R176
R166
R163
R113
R120
R124
R125
R214
R201
R207
R2150
C171
C158
C159
R505
R502
R503
R504
C504
C505
C506
C507
C517
C501
C503
C509
C573
C575
C577
C579
C557
C558
R506
C990
C991
R543
R542
R540
R539
R541
C533
C537
C541
R559
R568
R569
R570
R571
R573
R583
R584
R585
R586
R920
R919
R967
R912
R902
R916
C909
R908
R914
C908
C906
C910
C911
C912
C913
C914
R917
R918
C924
C925
C962
C963
C964
C965
C966
C922
C933
C934
C937
C944
C945
C951
C952
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C955
C956
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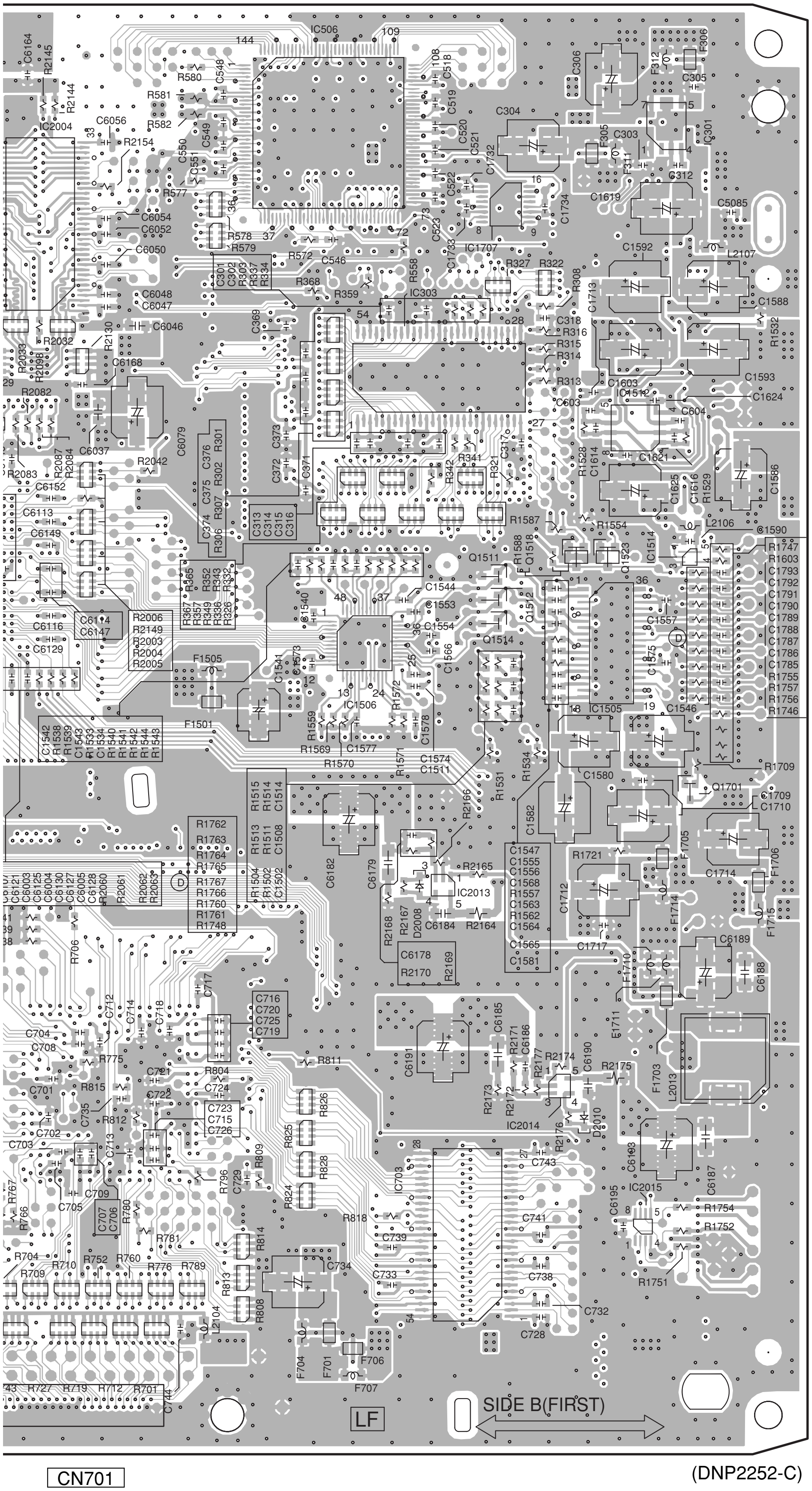
C958
C959
C960
C961
C954
C967
C968
C969
C970
C971
C972
C973
C974
C975
C976
C977
C978
C980
C981
C982
C983
C984
C987
C5203
R926
R927
R945
R946
R952
R953
R965
R966
R922
R936
R938
R934
R935
C939
C940
C941
C942
C943
C949
C950
C902
C903
R904
R2184
C6105
C1736
C1737
R1733
R1704
R1720
C1735
R1732
R1710
R1740
R1741
R1743
C1721
C1742
C1720
R1713
R1711
C1118
C1119
R1106
R1107
C1113

DVJ-1000

SIDE B

A



IC506
IC2004
IC303
IC1506
IC1505
IC2013
IC2014
IC2015
IC703
IC1512
Q1511
Q1512
Q1514
Q1518
Q1523
Q1701
CN1704
CN1703
CN1701
CN1702
F1501
F1505
F701
F704
F706
F707
L2104
L2106
L2107
LF
SIDE B(FIRST)
CN701

(DNP2252-C)

IC506
IC101

IC2004
IC123 IC301
IC2002

IC109

IC2005 IC1707

B

IC303

IC103
IC1512

IC124

IC106 IC107
IC117

IC1514 Q1511 Q1518
Q1523

C

Q1512
Q1514

IC2006

IC1506 IC1505

IC114

Q1701

D

IC104 IC115 IC112 IC110

IC2013

IC902 Q2003

IC907

IC1701 IC2014

E

IC2015
IC703

IC904

IC906 IC905

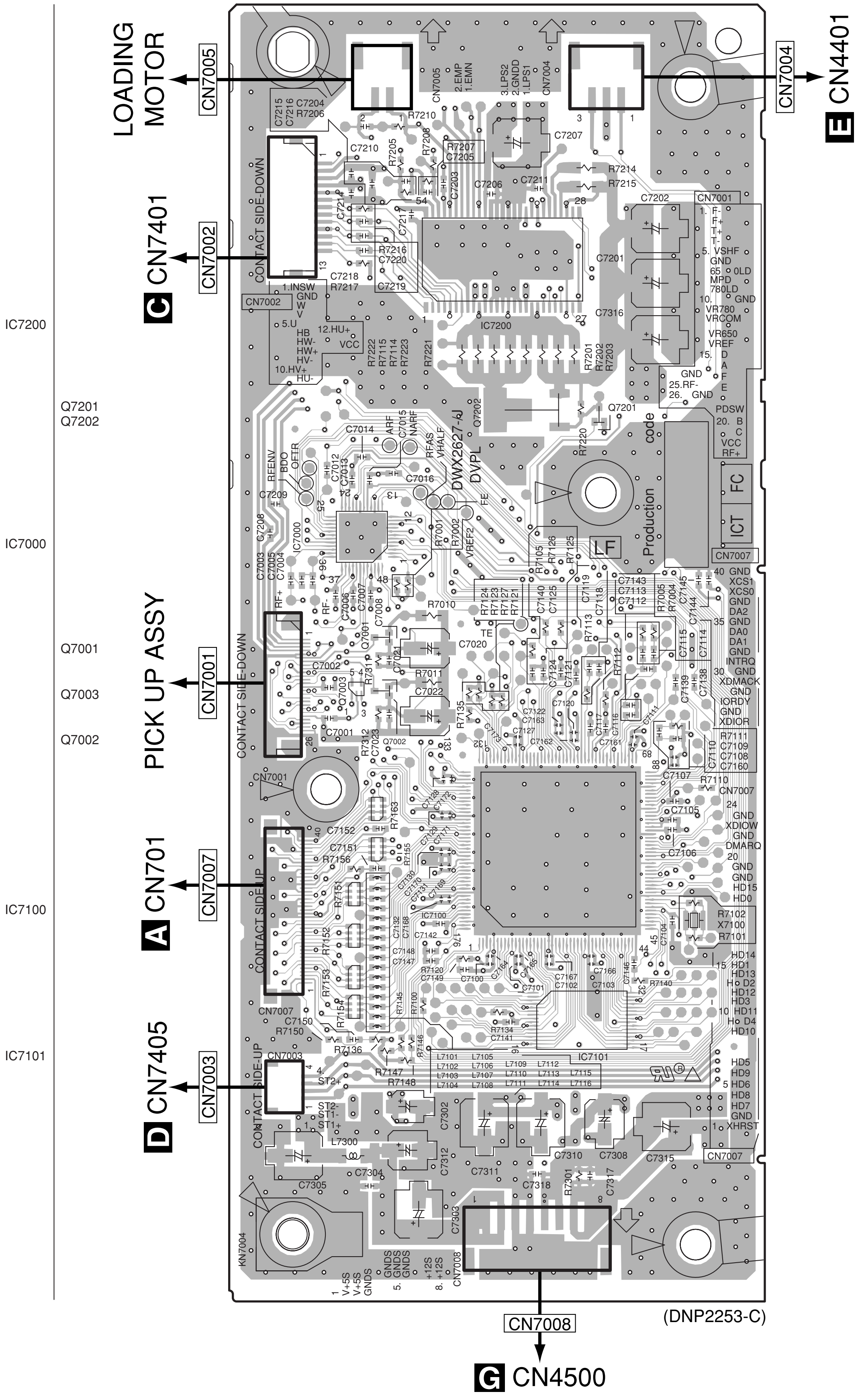
F

A

4.2 DVPL ASSY

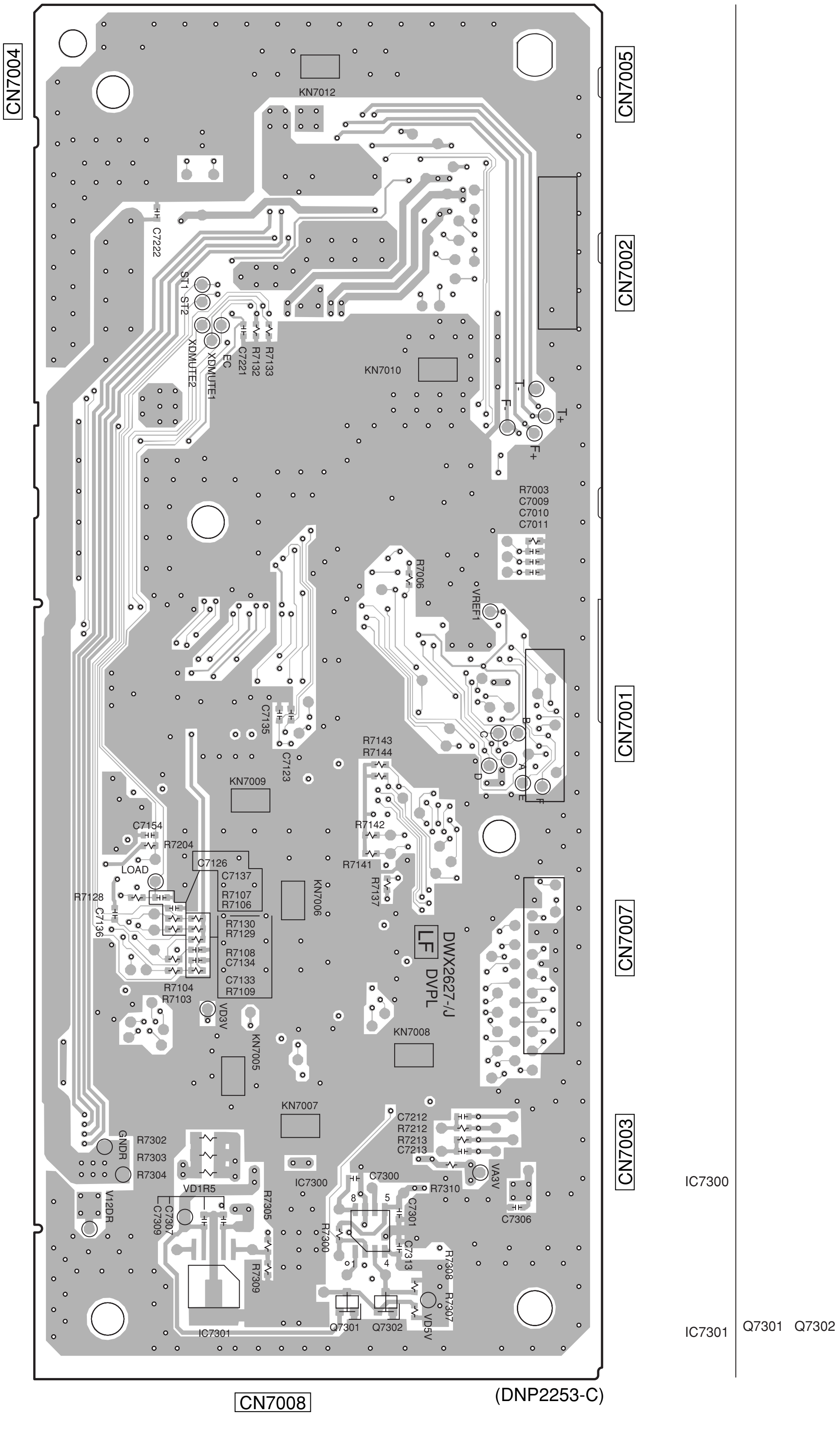
SIDE A

B DVPL ASSY


IC7200
Q7201
Q7202
IC7000
Q7001
Q7003
Q7002
IC7100
IC7101
LOADING MOTOR
CN7005
CN7004
E CN4401
C CN7401
CN7002
PICK UP ASSY
CN7001
A CN701
CN7007
D CN7405
CN7003
CN7008
G CN4500
CONTACT SIDE-DOWN
CONTACT SIDE-UP
DWX2627-J
DVPL
Production code
FC
ICT
LF
KN7004
(DNP2253-C)

SIDE B

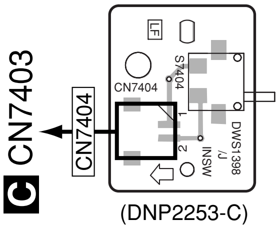
B DVPL ASSY


CN7004
CN7005
CN7002
CN7001
CN7007
CN7003
CN7008
KN7012
KN7010
KN7009
KN7006
KN7008
KN7005
KN7007
C7222
ST1
ST2
EC
XDMUTE1
XDMUTE2
C7221
R7132
R7133
T-
T+
F-
F+
R7003
C7009
C7010
C7011
R7006
VREF1
B
C
A
D
E
F
C7135
C7123
R7143
R7144
R7142
R7141
R7137
C7154
R7204
LOAD
C7126
C7137
R7107
R7106
R7128
C7136
R7130
R7129
R7108
C7134
C7133
R7109
R7104
R7103
VD3V
DWX2627-/J
LF
DVPL
C7212
R7212
R7213
C7213
VA3V
C7306
GNDR
R7302
R7303
R7304
VD1R5
V12DR
C7307
C7309
R7305
R7309
IC7300
C7300
R7310
C7301
C7313
R7300
R7308
R7307
8
5
1
4
VD5V
Q7301
Q7302
IC7301
(DNP2253-C)

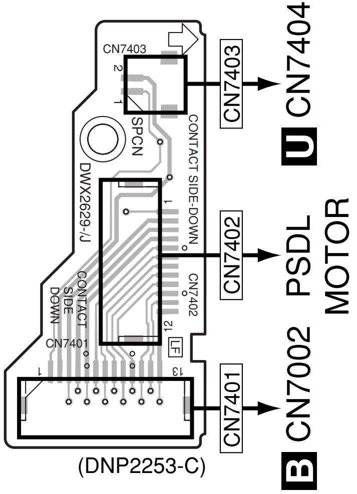
4.3 SPCN, STCN, INSW and SDCB ASSYS

SIDE A

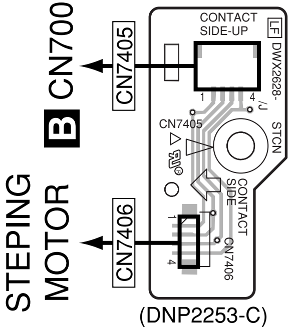
U INSW ASSY


LF
S7404
CN7404
1
2
DWS1398 /J
INSW
CN7404
CN7403
C
(DNP2253-C)

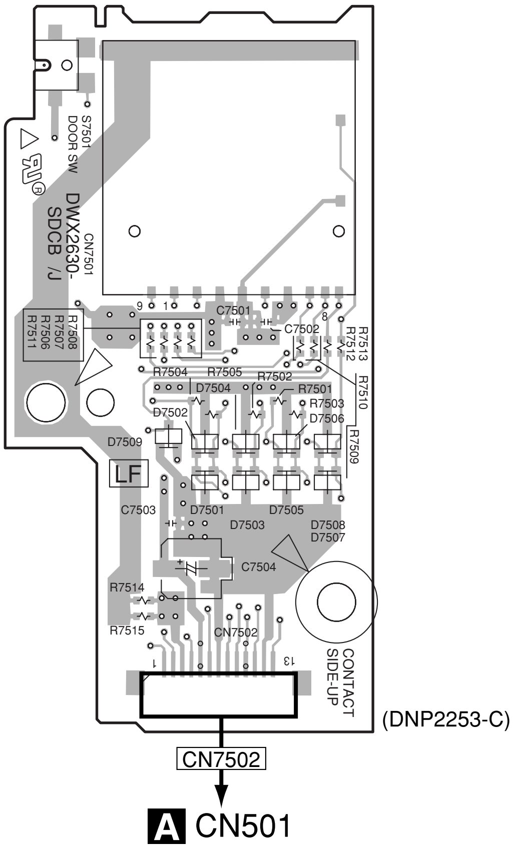
C SPCN ASSY


CN7403
2
1
SPCN
CONTACT SIDE-DOWN
CN7403
CN7404
U
DWX2629-/J
1
CN7402
CN7402
PSDL MOTOR
12
LF
CONTACT SIDE DOWN
CN7401
1
13
CN7401
CN7002
B
(DNP2253-C)

D STCN ASSY

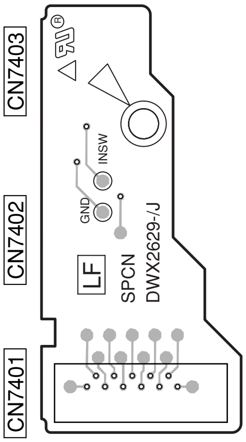

CONTACT SIDE-UP
LF
DWX2628- /J
CN7405
CN7003
B
1
4
CN7405
STCN
CONTACT SIDE
1
4
CN7406
CN7406
STEPING MOTOR
(DNP2253-C)

F SDCB ASSY


S7501 DOOR SW
DWX2630- SDCB /J
CN7501
9
1
8
C7501
C7502
R7508
R7507
R7506
R7511
R7513
R7512
R7504
R7505
R7502
R7510
D7504
R7501
R7503
D7502
D7506
R7509
D7509
LF
C7503
D7501
D7505
D7503
D7508
D7507
C7504
R7514
R7515
CN7502
1
13
CONTACT SIDE-UP
(DNP2253-C)
CN7502
A CN501

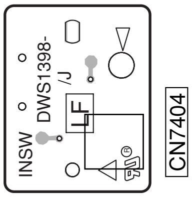
SIDE B

C SPCN ASSY


CN7403
CN7402
CN7401
INSW
GND
LF
SPCN
DWX2629-/J

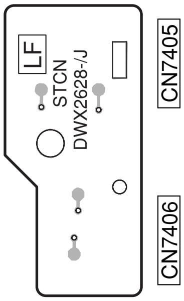
(DNP2253-C)

U INSW ASSY


INSW
DWS1398-
-/J
LF
CN7404

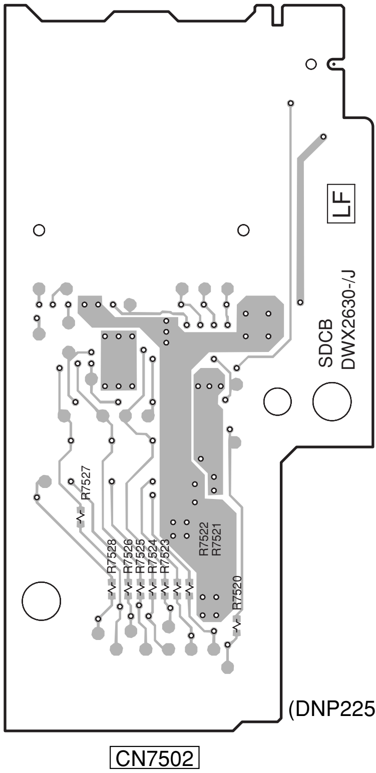
(DNP2253-C)

D STCN ASSY


LF
STCN
DWX2628-/J
CN7405
CN7406

(DNP2253-C)

F SDCB ASSY

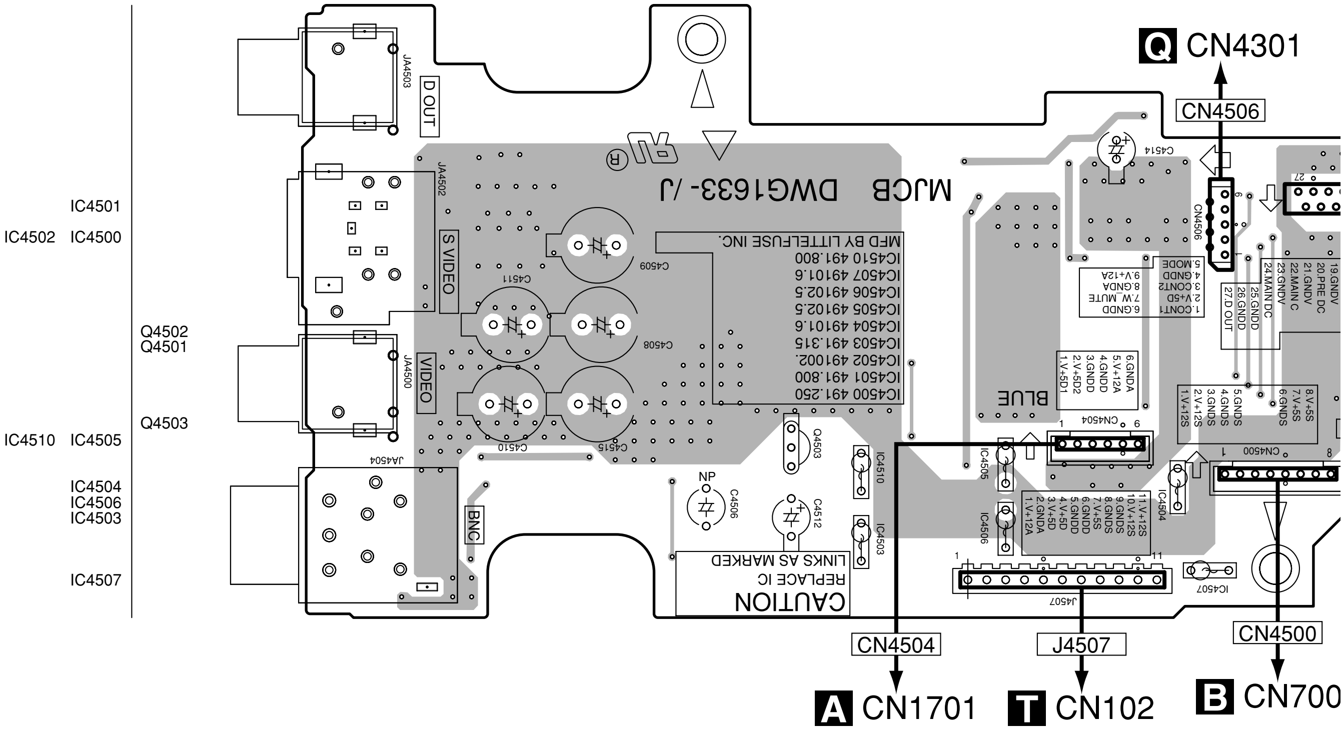

LF
SDCB
DWX2630-/J
R7527
R7528
R7526
R7525
R7524
R7523
R7522
R7521
R7520
CN7502

(DNP2253-C)

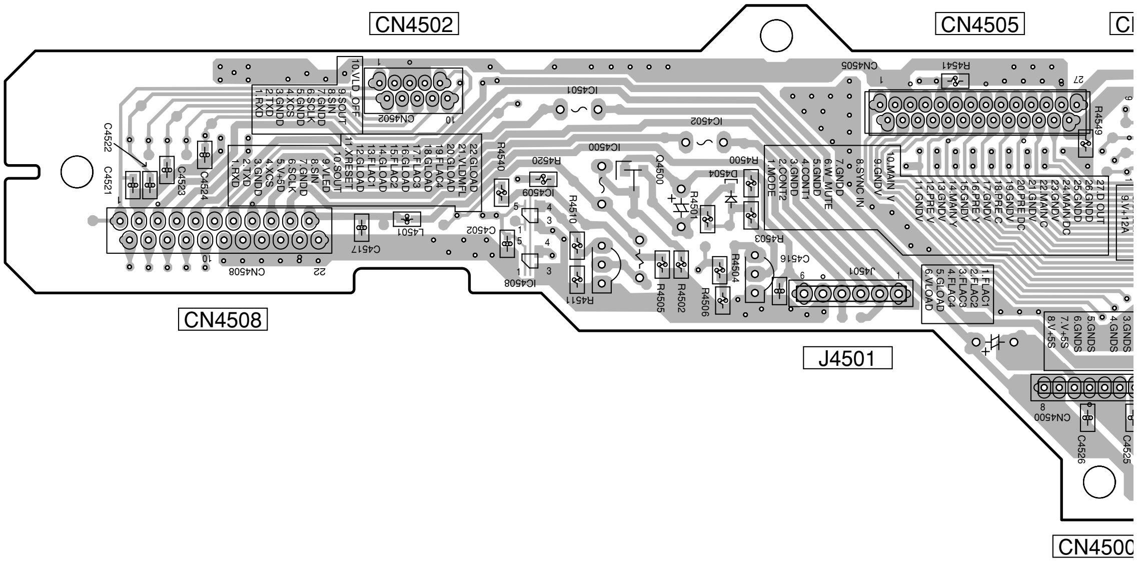
4.4 MJCB ASSY

G MJCB ASSY

SIDE A

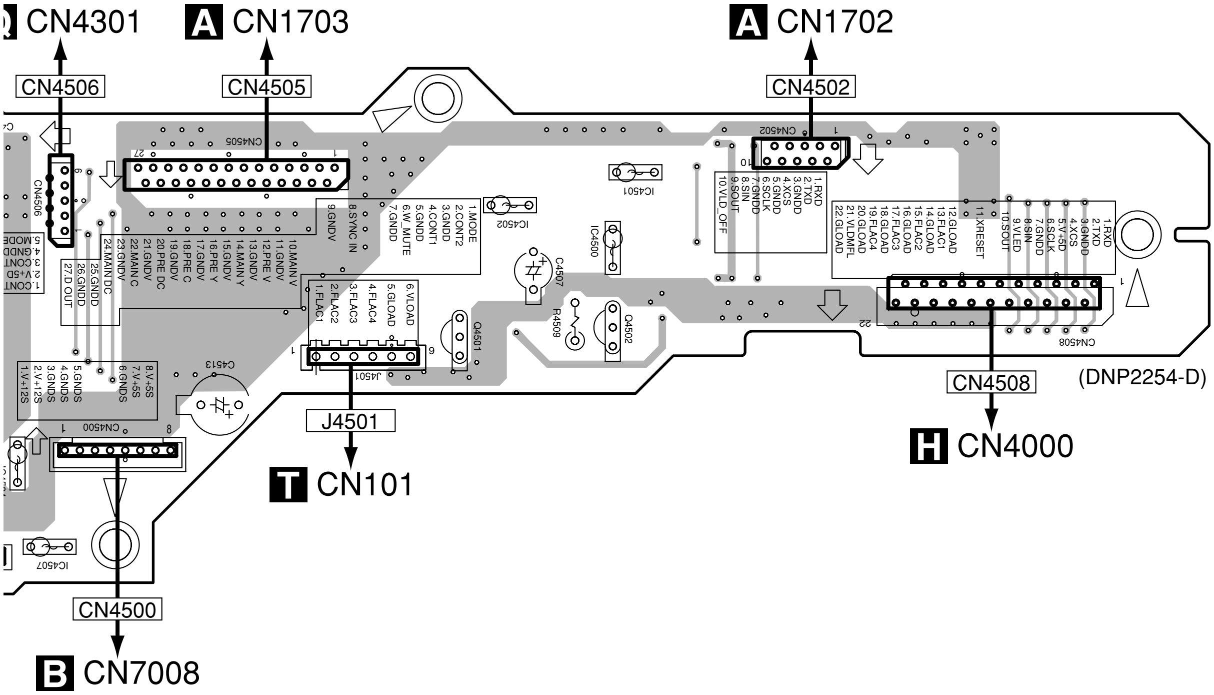

IC4501
IC4502 IC4500
Q4502
Q4501
Q4503
IC4510 IC4505
IC4504
IC4506
IC4503
IC4507
D OUT
S VIDEO
VIDEO
BNC
MJCB DWG1633-/J
MFD BY LITTELFUSE INC.
IC4510 491.800
IC4507 49101.6
IC4506 49102.5
IC4505 49102.5
IC4504 49101.6
IC4503 491.315
IC4502 49102.
IC4501 491.800
IC4500 491.250
CAUTION
REPLACE IC
LINKS AS MARKED
BLUE
NP
Q CN4301
CN4506
CN4504
J4507
CN4500
A CN1701
T CN102
B CN700

SIDE B

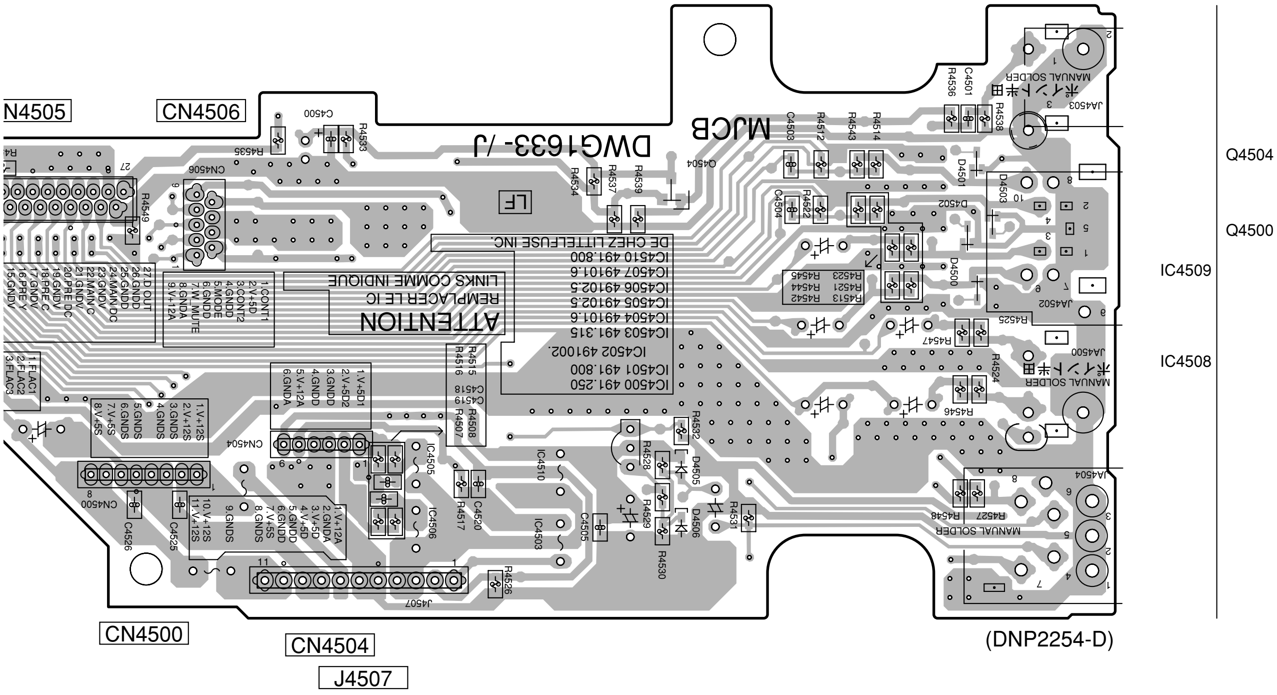

CN4502
CN4505
CN4508
J4501
CN4500

G

SIDE A


CN4301
A CN1703
A CN1702
CN4506
CN4505
CN4502
CN4508
(DNP2254-D)
H CN4000
J4501
T CN101
CN4500
B CN7008
IC4500
IC4501
IC4502
IC4507
Q4501
Q4502
C4507
C4513
R4509

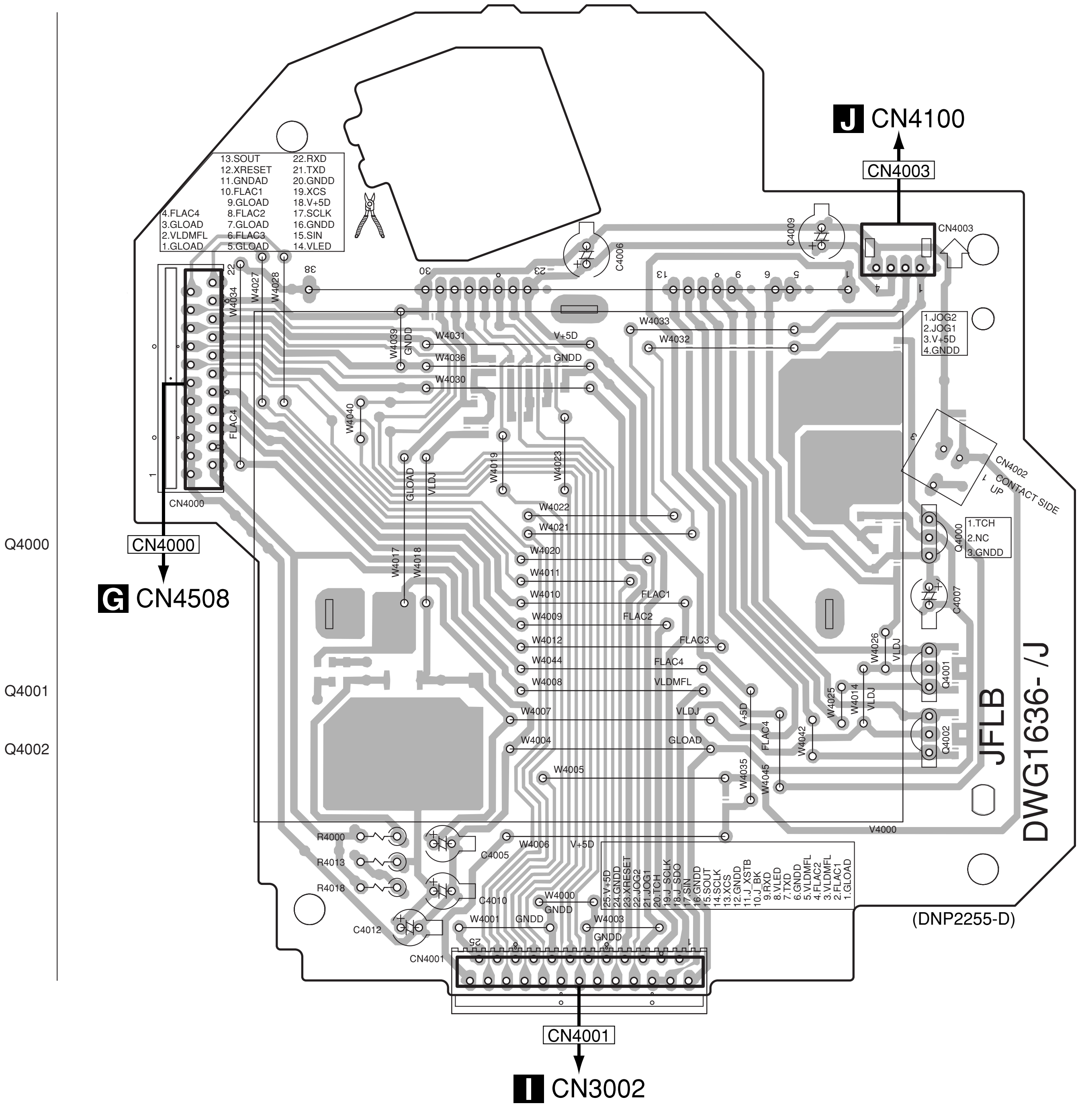
SIDE B


CN4505
CN4506
MJCB
DWG1633-/J
ATTENTION
REMPLACER LE IC
LINKS COMME INDIQUE
IC4500 491.250
IC4501 491.800
IC4502 49100.
IC4503 491.315
IC4504 49101.6
IC4505 49102.5
IC4506 49102.5
IC4507 49101.6
IC4510 491.800
DE CHEZ LITTELFUSE INC.
Q4504
Q4500
IC4509
IC4508
MANUAL SOLDER
JA4503
JA4502
JA4500
JA4504
CN4500
CN4504
J4507
(DNP2254-D)

4.5 JFLB ASSY

SIDE A

H JFLB ASSY


J CN4100
CN4003
CN4003
13.SOUT
12.XRESET
11.GNDAD
10.FLAC1
9.GLOAD
8.FLAC2
7.GLOAD
6.FLAC3
5.GLOAD
4.FLAC4
3.GLOAD
2.VLDMFL
1.GLOAD
22.RXD
21.TXD
20.GNDD
19.XCS
18.V+5D
17.SCLK
16.GNDD
15.SIN
14.VLED
C4009
C4006
1.JOG2
2.JOG1
3.V+5D
4.GNDD
CN4000
CN4000
G CN4508
Q4000
Q4001
Q4002
W4034
W4027
W4028
W4039
GNDD
W4031
V+5D
W4036
GNDD
W4030
W4033
W4032
W4040
FLAC4
GLOAD
VLDJ
W4019
W4023
W4022
W4021
W4020
W4011
W4010
FLAC1
W4009
FLAC2
W4012
FLAC3
W4044
FLAC4
W4008
VLDMFL
W4007
VLDJ
W4004
GLOAD
W4005
W4017
W4018
W4035
W4045
V+5D
FLAC4
W4042
W4025
W4014
VLDJ
W4026
VLDJ
CN4002
CONTACT SIDE
UP
1.TCH
2.NC
3.GNDD
Q4000
C4007
Q4001
Q4002
JFLB
DWG1636- /J
V4000
R4000
R4013
R4018
C4005
C4010
C4012
W4006
V+5D
W4000
GNDD
W4001
GNDD
W4003
GNDD
25.V+5D
24.GNDD
23.XRESET
22.JOG2
21.JOG1
20.TCH
19.J_SCLK
18.J_SDO
17.SIN
16.GNDD
15.SOUT
14.SCLK
13.XCS
12.GNDD
11.J_XSTB
10.J_BK
9.RXD
8.VLED
7.TXD
6.GNDD
5.VLDMFL
4.FLAC2
3.VLDMFL
2.FLAC1
1.GLOAD
CN4001
(DNP2255-D)
CN4001
I CN3002

SIDE B

A

B

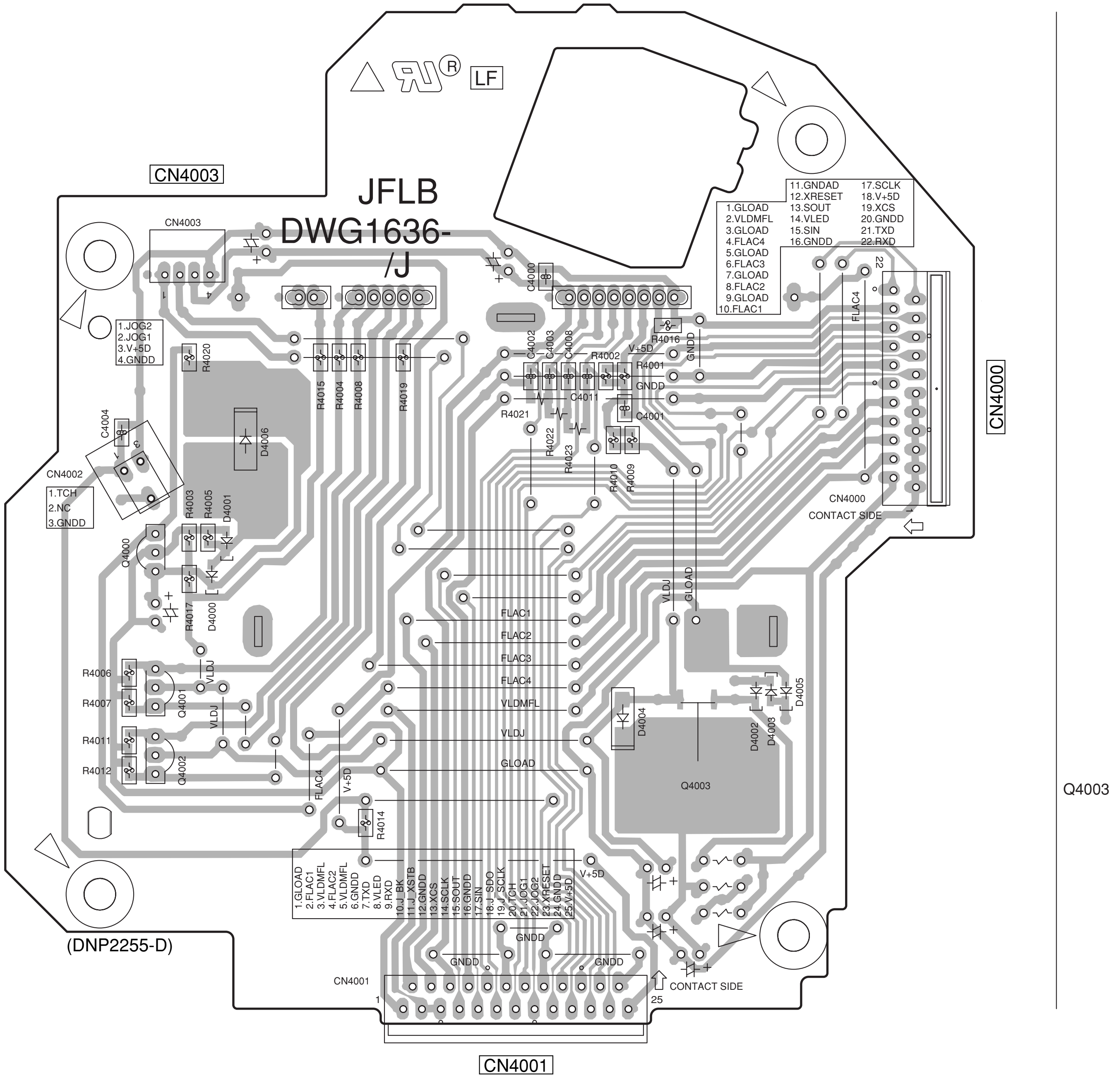
C

D

E

F

H JFLB ASSY

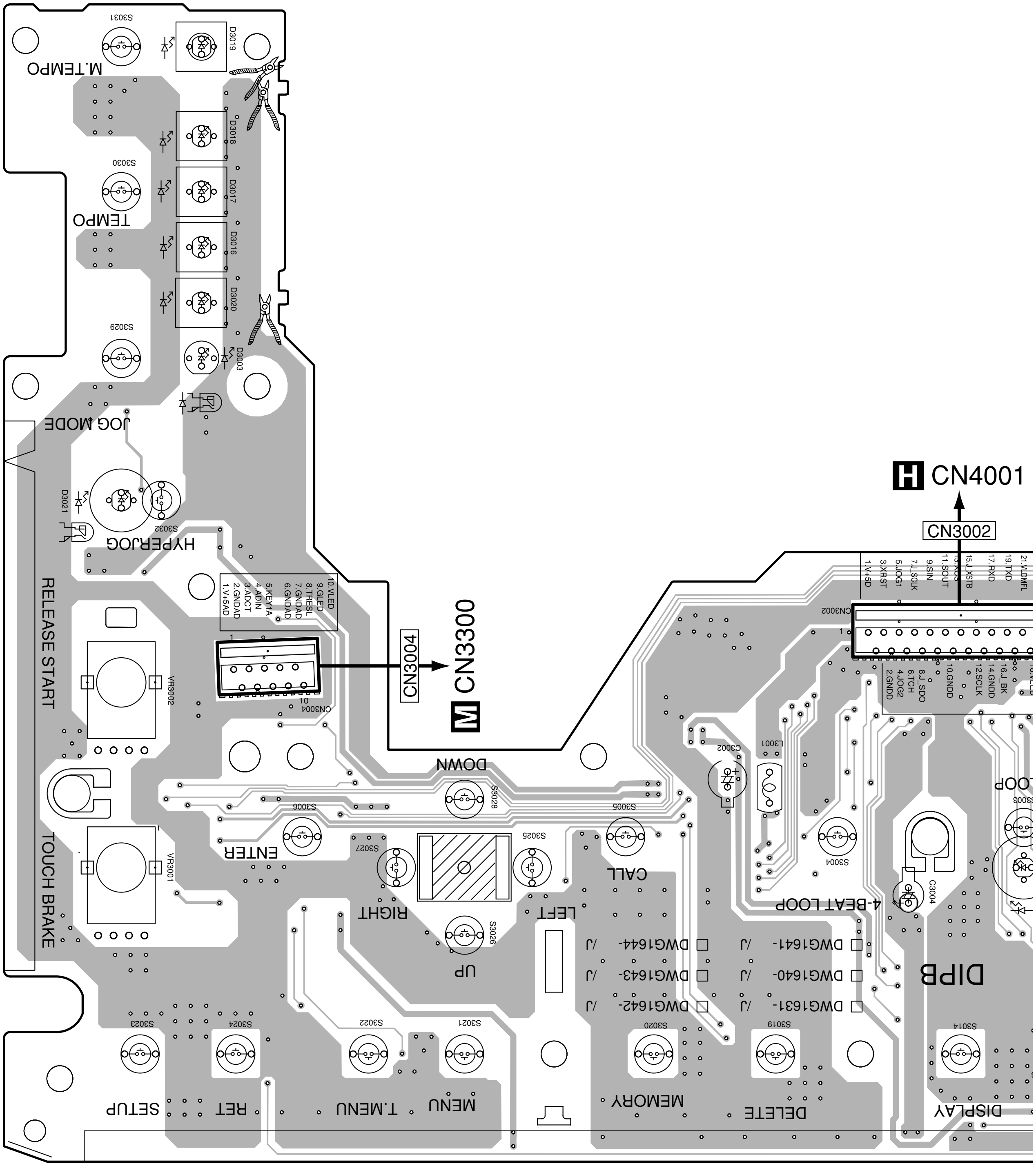

LF
CN4003
JFLB
DWG1636-
/J
CN4003
1
4
1.JOG2
2.JOG1
3.V+5D
4.GNDD
11.GNDAD
12.XRESET
13.SOUT
14.VLED
15.SIN
16.GNDD
17.SCLK
18.V+5D
19.XCS
20.GNDD
21.TXD
22.RXD
1.GLOAD
2.VLDMFL
3.GLOAD
4.FLAC4
5.GLOAD
6.FLAC3
7.GLOAD
8.FLAC2
9.GLOAD
10.FLAC1
22
FLAC4
C4000
R4016
GNDD
V+5D
R4002
R4001
GNDD
C4002
C4003
C4008
C4011
C4001
R4021
R4022
R4023
R4010
R4009
R4020
R4015
R4004
R4008
R4019
D4006
C4004
CN4002
1.TCH
2.NC
3.GNDD
R4003
R4005
D4001
Q4000
R4017
D4000
CN4000
CONTACT SIDE
1
CN4000
VLDJ
GLOAD
FLAC1
FLAC2
FLAC3
FLAC4
VLDMFL
VLDJ
GLOAD
R4006
R4007
Q4001
VLDJ
VLDJ
R4011
R4012
Q4002
FLAC4
V+5D
R4014
D4004
D4002
D4003
D4005
Q4003
1.GLOAD
2.FLAC1
3.VLDMFL
4.FLAC2
5.VLDMFL
6.GNDD
7.TXD
8.VLED
9.RXD
10.J_BK
11.J_XSTB
12.GNDD
13.XCS
14.SCLK
15.SOUT
16.GNDD
17.SIN
18.J_SDO
19.J_SCLK
20.TCH
21.JOG1
22.JOG2
23.XRESET
24.GNDD
25.V+5D
V+5D
GNDD
GNDD
GNDD
CN4001
1
25
CONTACT SIDE
(DNP2255-D)
CN4001
Q4003

H

4.6 DIPB and RSWB ASSYS

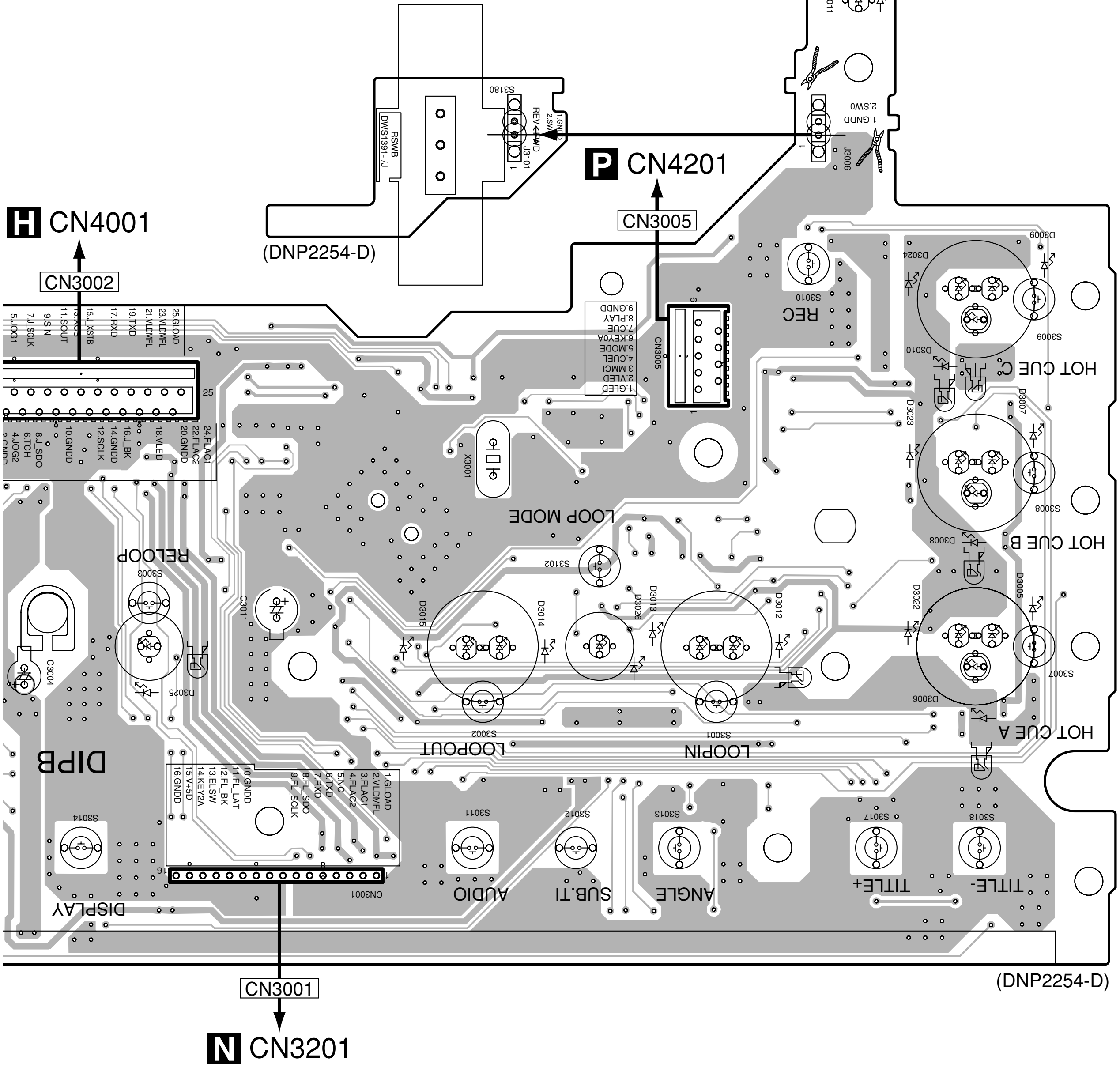
SIDE A

I DIPB ASSY


S3031
D3019
M.TEMPO
D3018
S3030
D3017
TEMPO
D3016
D3020
S3029
D3003
JOG MODE
D3021
S3032
HYPERJOG
10.VLED
9.GLED
8.TRESL
7.GNDAD
6.GNDAD
5.KEY1A
4.ADIN
3.ADCT
2.GNDAD
1.V+5AD
1
10
CN3004
CN3004
M CN3300
RELEASE START
VR3002
VR3001
TOUCH BRAKE
DOWN
S3028
S3006
ENTER
S3027
S3025
S3005
CALL
RIGHT
LEFT
S3026
UP
H CN4001
CN3002
1.V+5D
3.XRST
5.JOG1
7.J_SCLK
9.SIN
11.SOUT
13.XCS
15.J_XSTB
17.RXD
19.TXD
21.VLDMFL
2.GNDD
4.JOG2
6.TCH
8.J_SDO
10.GNDD
12.SCLK
14.GNDD
16.J_BK
CN3002
1
C3002
L3001
S3004
C3004
4-BEAT LOOP
S3003
LOOP
DWG1644- /J
DWG1643- /J
DWG1642- /J
DWG1641- /J
DWG1640- /J
DWG1631- /J
DIPB
S3023
S3024
S3022
S3021
S3020
S3019
S3014
SETUP
RET
T.MENU
MENU
MEMORY
DELETE
DISPLAY

SIDE A

O RSWB ASSY


RSWB
DWS1391-/J
S3180
J3101
1.GNDD
2.SW0
REV
FWD
J3006
D3011
P CN4201
CN3005
(DNP2254-D)
H CN4001
CN3002
1.GLED
2.VLED
3.MMCL
4.CUEL
5.MODE
6.KEY0A
7.CUE
8.PLAY
9.GNDD
25.GLOAD
23.VLDMFL
21.VLDMFL
19.TXD
17.RXD
15.J_XSTB
11.SOUT
9.SIN
7.J_SCLK
5.JOG1
24.FLAC1
22.FLAC2
20.GNDD
18.VLED
16.J_BK
14.GNDD
12.SCLK
10.GNDD
8.J_SDO
6.TCH
4.JOG2
REC
S3010
D3009
D3024
S3009
D3010
HOT CUE C
D3023
D3007
X3001
LOOP MODE
S3008
D3008
HOT CUE B
S3102
D3015
D3014
D3026
D3013
D3012
D3022
D3005
RELOOP
S3003
C3011
C3004
D3025
S3007
D3006
HOT CUE A
LOOPOUT
S3002
LOOPIN
S3001
DIPB
1.GLOAD
2.VLDMFL
3.FLAC1
4.FLAC2
5.NC
6.TXD
7.RXD
8.FL_SDO
9.FL_SCLK
10.GNDD
11.FL_LAT
12.FL_BK
13.ELSW
14.KEY2A
15.V+5D
16.GNDD
S3014
S3011
S3012
S3013
S3017
S3018
CN3001
DISPLAY
AUDIO
SUB.TI
ANGLE
TITLE+
TITLE-
(DNP2254-D)
CN3001
N CN3201

O I

SIDE B

A

B

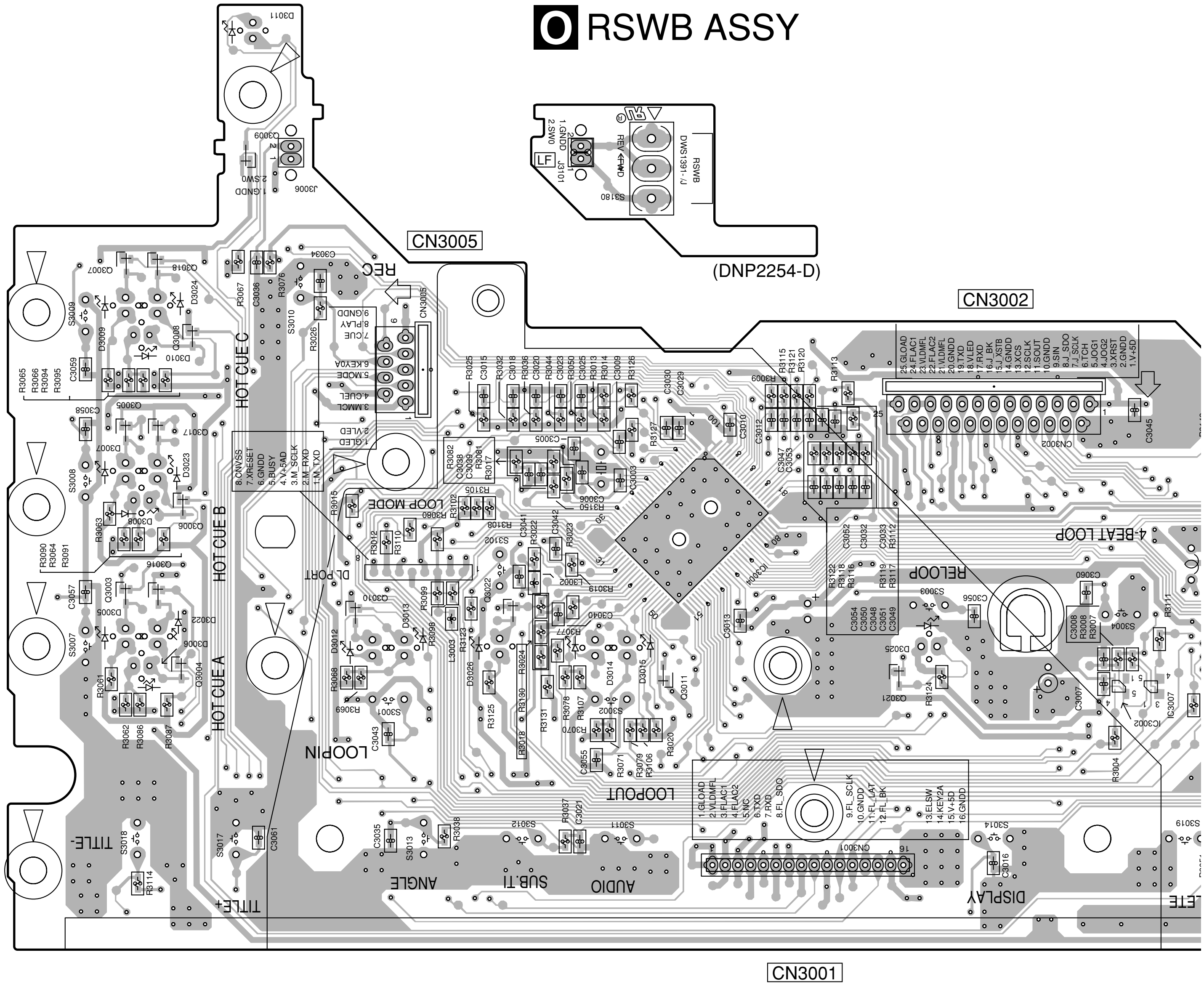
C

D

E

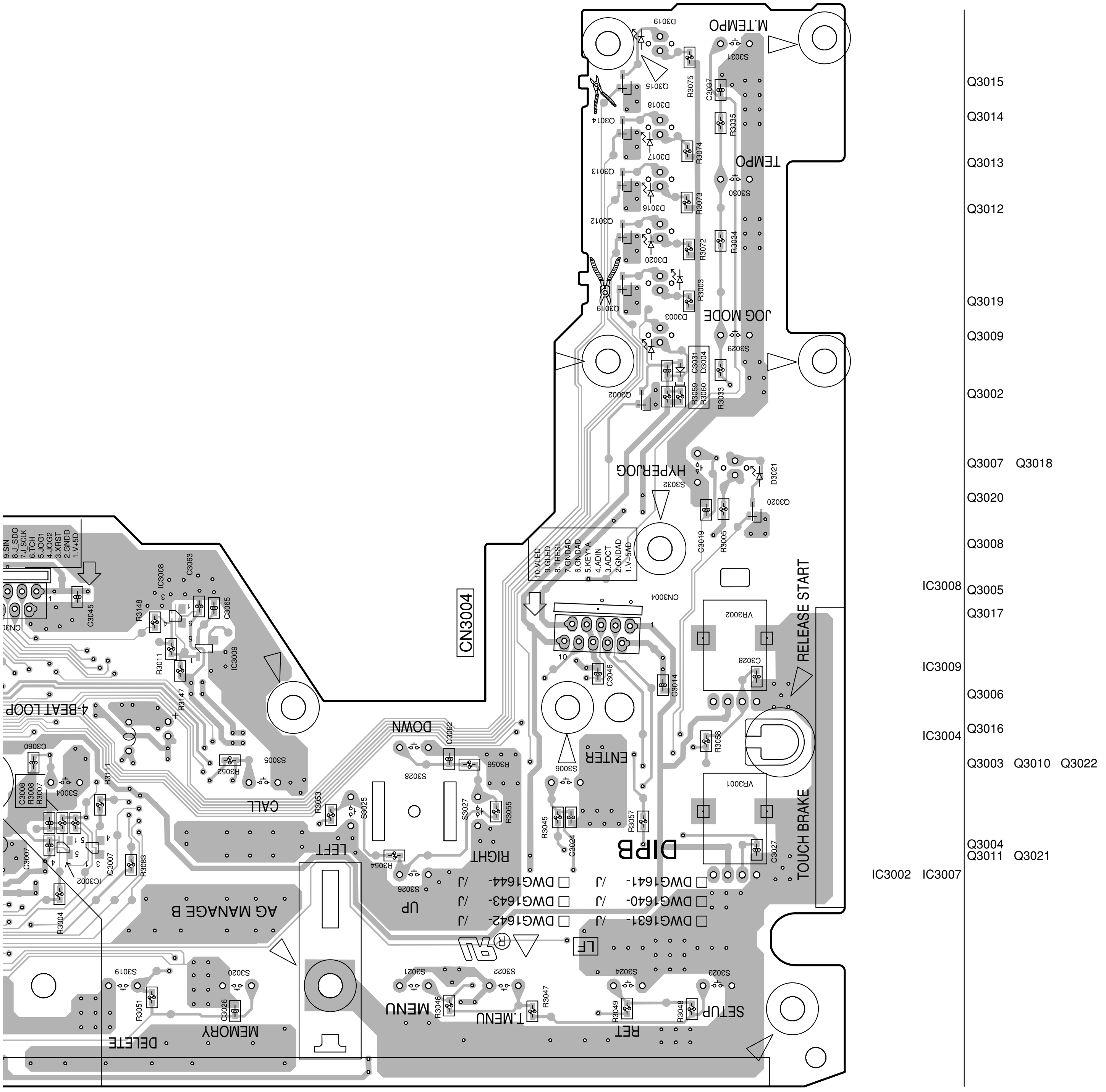
F

O RSWB ASSY


RSWB
DWS1391-/J
REV ◀▶ FWD
S3180
J3101
LF
1.GNDD
2.SW0
(DNP2254-D)
CN3005
CN3002
CN3001
J3006
1.GNDD
2.SW0
Q3009
D3011
REC
1.GLED
2.VLED
3.MMCL
4.CUEL
5.MODE
6.KEY0A
7.CUE
8.PLAY
9.GNDD
8.CNVSS
7.XRESET
6.GNDD
5.BUSY
4.V+5AD
3.M_SCLK
2.M_RXD
1.M_TXD
HOT CUE C
HOT CUE B
HOT CUE A
LOOP MODE
DL PORT
LOOPIN
LOOPOUT
RELOOP
4-BEAT LOOP
IC3004
IC3002
25.GLOAD
24.FLAC1
23.VLDMFL
22.FLAC2
21.VLDMFL
20.GNDD
19.TXD
18.VLED
17.RXD
16.J_BK
15.J_XSTB
14.GNDD
13.XCS
12.SCLK
11.SOUT
10.GNDD
9.SIN
8.J_SDO
7.J_SCLK
6.TCH
5.JOG1
4.JOG2
3.XRST
2.GNDD
1.V+5D
1.GLOAD
2.VLDMFL
3.FLAC1
4.FLAC2
5.NC
6.TXD
7.RXD
8.FL_SDO
9.FL_SCLK
10.GNDD
11.FL_LAT
12.FL_BK
13.ELSW
14.KEY2A
15.V+5D
16.GNDD
TITLE-
TITLE+
ANGLE
SUB.T
AUDIO
DISPLAY
S3007
S3008
S3009
S3010
S3001
S3002
S3003
S3004
S3011
S3012
S3013
S3014
S3017
S3018
S3019

SIDE B

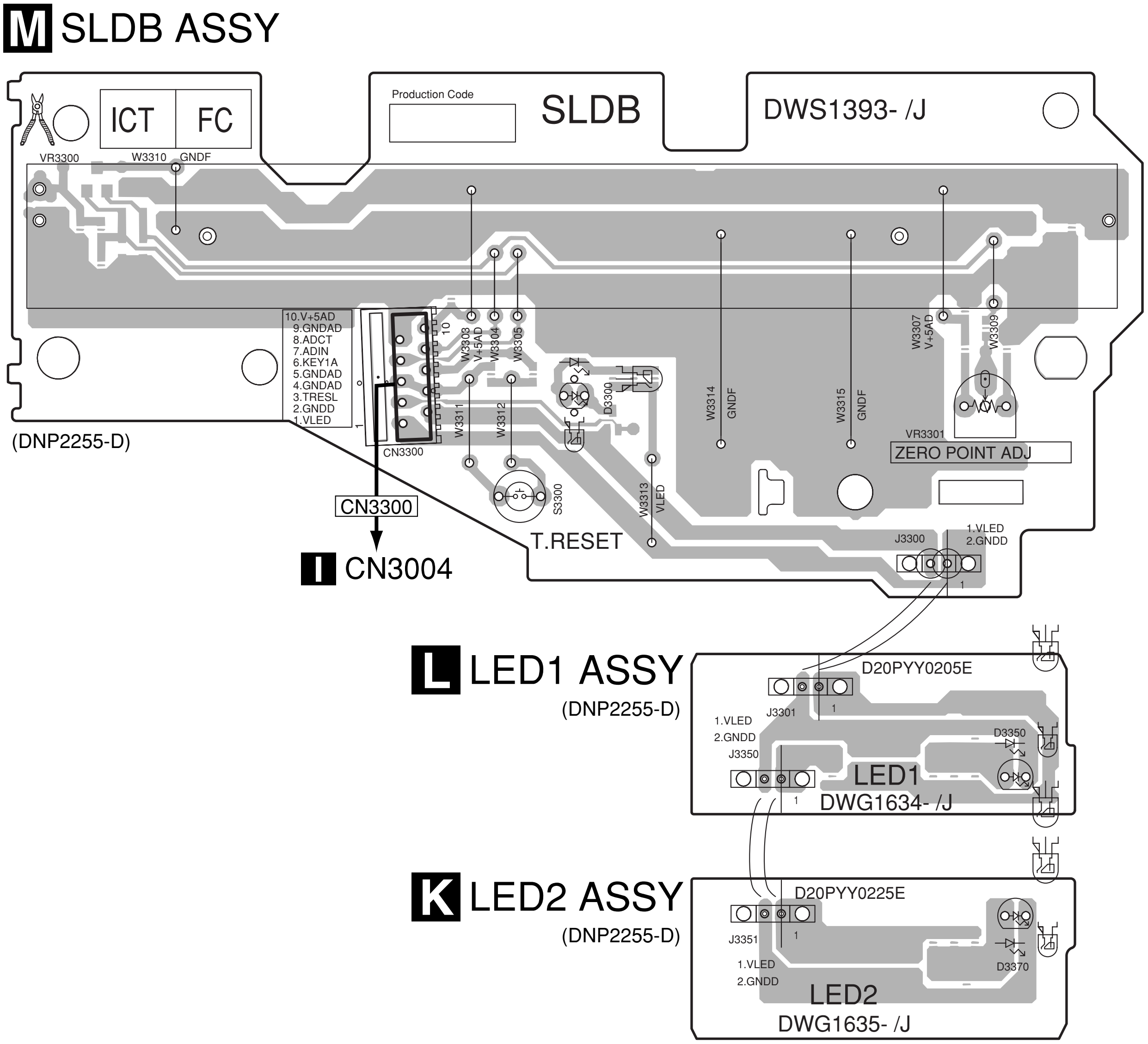
I DIPB ASSY


M.TEMPO
S3031
D3019
Q3015
D3018
Q3014
R3075
C3037
R3035
TEMPO
S3030
D3017
Q3013
R3074
D3016
Q3012
R3073
R3072
R3034
D3020
Q3019
R3003
D3003
JOG MODE
S3029
C3031
D3004
R3059
R3060
R3033
Q3002
HYPERJOG
S3032
D3021
Q3020
C3019
R3005
CN3004
10.VLED
9.GLED
8.TRESL
7.GNDAD
6.GNDAD
5.KEYA
4.ADIN
3.ADCT
2.GNDAD
1.V+5AD
RELEASE START
VR3002
C3028
C3046
C3014
ENTER
S3006
R3058
VR3001
TOUCH BRAKE
C3027
DIPB
R3045
C3024
R3057
DOWN
C3062
R3056
S3028
S3027
R3055
RIGHT
CALL
R3053
S3025
LEFT
R3054
S3026
UP
DWG1644- /J
DWG1643- /J
DWG1642- /J
DWG1641- /J
DWG1640- /J
DWG1631- /J
LF
AG MANAGE B
4-BEAT LOOP
S3005
R3052
IC3008
C3063
C3065
R3148
R3011
IC3009
R3147
C3045
9.SIN
8.J_SDO
7.J_SCLK
6.TCH
5.JOG1
4.JOG2
3.XRST
2.GNDD
1.V+5D
C3060
S3004
R3111
C3008
R3008
R3007
C3007
IC3007
IC3002
R3083
R3004
S3019
S3020
R3051
C3026
MEMORY
DELETE
S3021
S3022
R3046
MENU
R3047
T.MENU
S3024
S3023
R3049
RET
R3048
SETUP
Q3015
Q3014
Q3013
Q3012
Q3019
Q3009
Q3002
Q3007 Q3018
Q3020
Q3008
IC3008 Q3005
Q3017
IC3009
Q3006
Q3016
IC3004
Q3003 Q3010 Q3022
Q3004
Q3011 Q3021
IC3002 IC3007

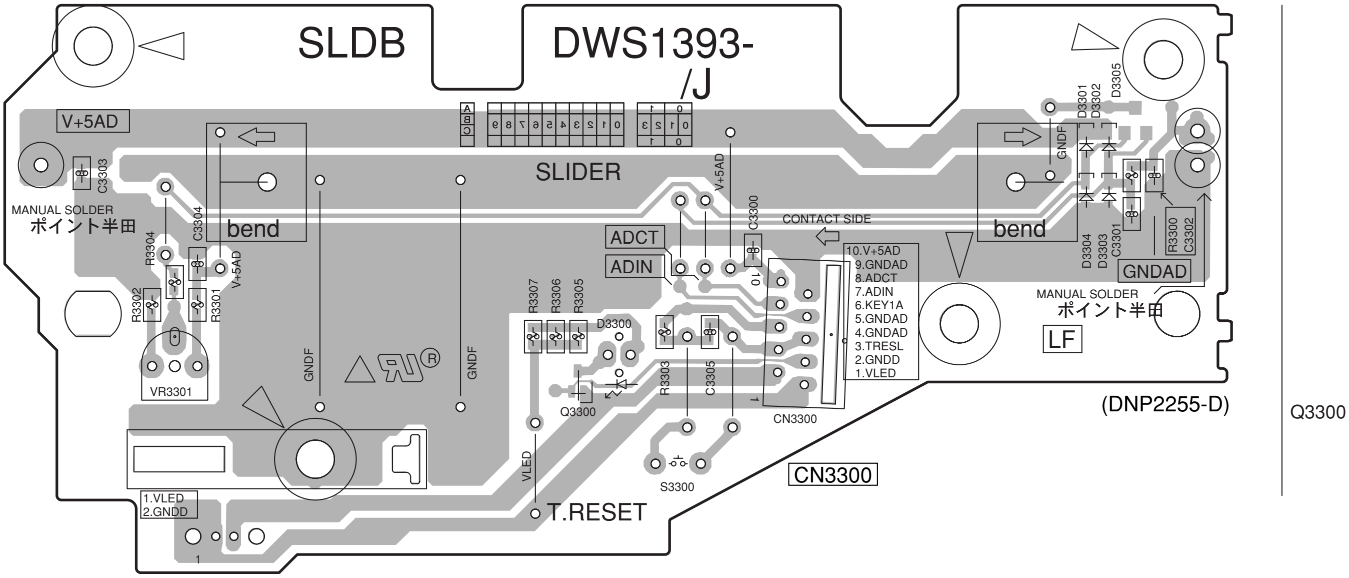
(DNP2254-D)

4.7 SLDB, LED1 and LED2 ASSYS

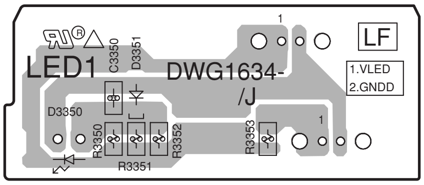
SIDE A


M SLDB ASSY
ICT
FC
Production Code
SLDB
DWS1393- /J
VR3300
W3310
GNDF
10.V+5AD
9.GNDAD
8.ADCT
7.ADIN
6.KEY1A
5.GNDAD
4.GNDAD
3.TRESL
2.GNDD
1.VLED
10
1
CN3300
W3303
V+5AD
W3304
W3305
W3311
W3312
D3300
W3314
GNDF
W3315
GNDF
W3307
V+5AD
W3309
VR3301
ZERO POINT ADJ
S3300
W3313
VLED
T.RESET
J3300
1.VLED
2.GNDD
(DNP2255-D)
CN3300
I CN3004
L LED1 ASSY
(DNP2255-D)
D20PYY0205E
J3301
1.VLED
2.GNDD
J3350
D3350
LED1
DWG1634- /J
K LED2 ASSY
(DNP2255-D)
D20PYY0225E
J3351
1.VLED
2.GNDD
D3370
LED2
DWG1635- /J

M SLDB ASSY

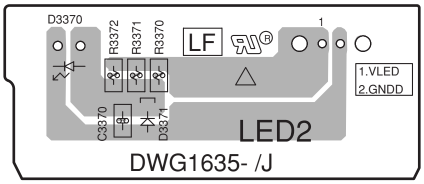

SLDB
DWS1393-
/J
V+5AD
SLIDER
bend
bend
MANUAL SOLDER
ポイント半田
C3303
C3304
R3304
R3302
R3301
V+5AD
VR3301
GNDF
GNDF
V+5AD
C3300
ADCT
ADIN
CONTACT SIDE
10
R3307
R3306
R3305
D3300
Q3300
R3303
C3305
1
CN3300
10.V+5AD
9.GNDAD
8.ADCT
7.ADIN
6.KEY1A
5.GNDAD
4.GNDAD
3.TRESL
2.GNDD
1.VLED
S3300
CN3300
VLED
T.RESET
1.VLED
2.GNDD
1
D3301
D3302
D3305
GNDF
D3304
D3303
C3301
R3300
C3302
GNDAD
MANUAL SOLDER
ポイント半田
LF
Q3300

(DNP2255-D)


LED1
C3350
D3351
DWG1634-
/J
D3350
R3350
R3351
R3352
R3353
1
1
LF
1.VLED
2.GNDD

L LED1 ASSY

(DNP2255-D)


D3370
R3372
R3371
R3370
LF
1
1.VLED
2.GNDD
C3370
D3371
LED2
DWG1635- /J

K LED2 ASSY

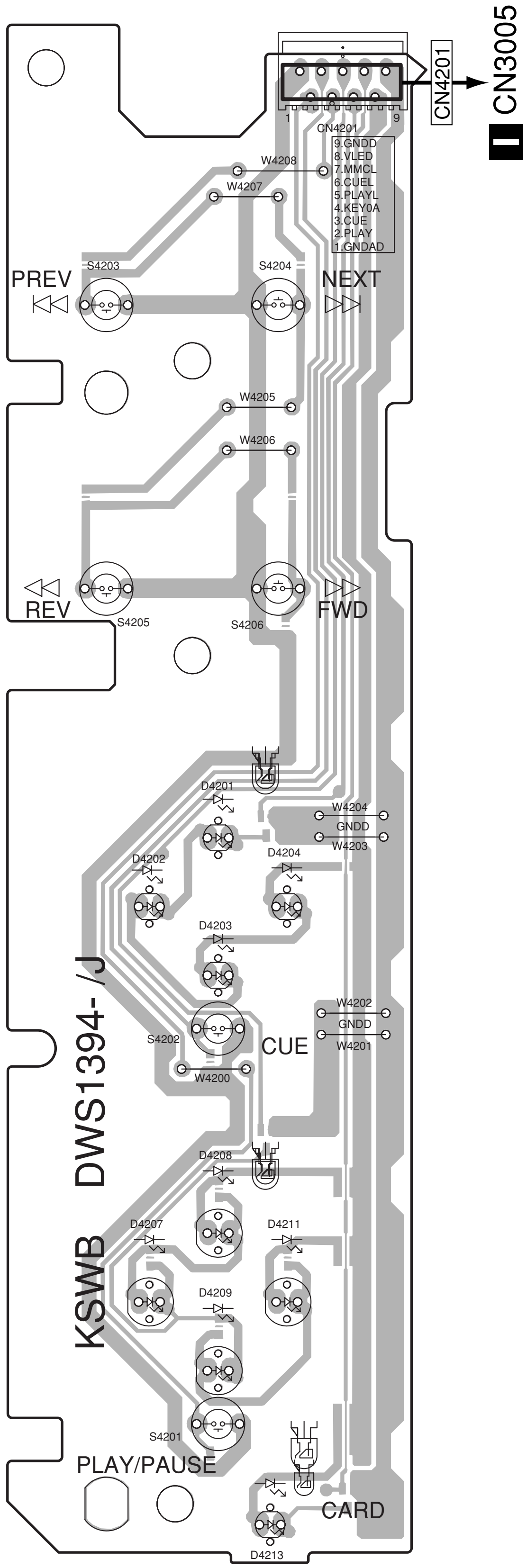
(DNP2255-D)

4.8 KSWB ASSY

SIDE A

A

P KSWB ASSY


CN3005
I
CN4201
1
9
CN4201
9.GNDD
8.VLED
7.MMCL
6.CUEL
5.PLAYL
4.KEY0A
3.CUE
2.PLAY
1.GNDAD
W4208
W4207
S4203
S4204
PREV
NEXT
W4205
W4206
REV
FWD
S4205
S4206
D4201
W4204
GNDD
W4203
D4202
D4204
D4203
W4202
GNDD
W4201
S4202
CUE
W4200
DWS1394- /J
KSWB
D4208
D4207
D4211
D4209
S4201
PLAY/PAUSE
CARD
D4213

(DNP2255-D)

B

C

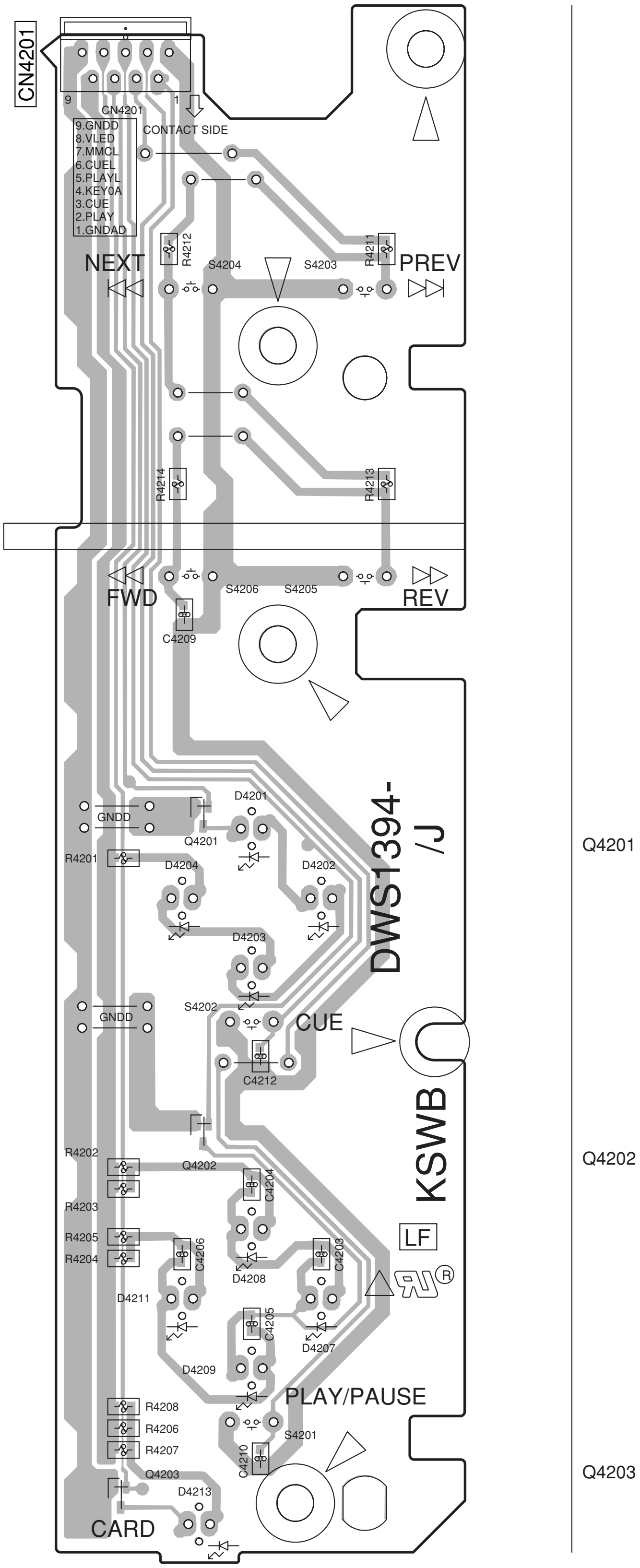
D

E

F

SIDE B

P KSWB ASSY

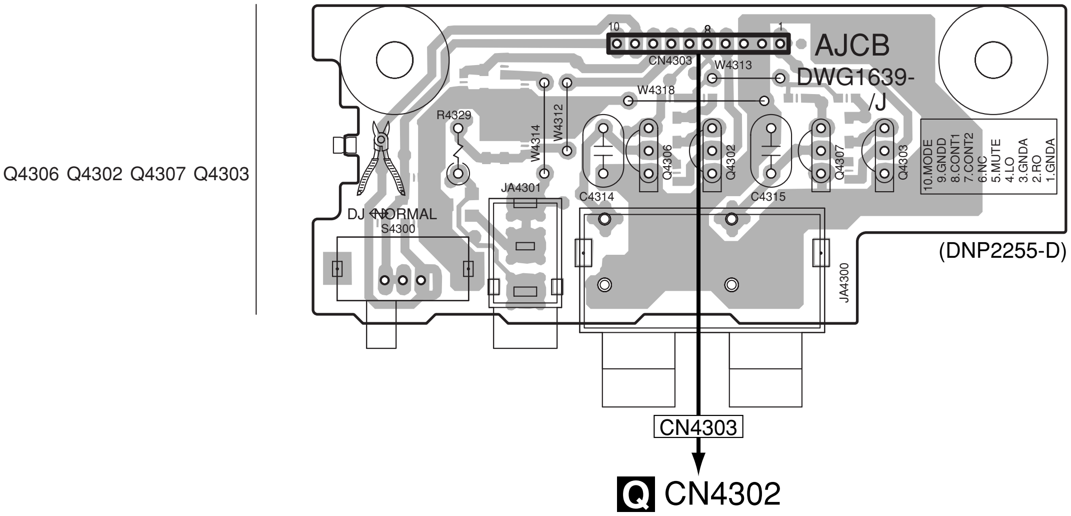

CN4201
9
1
CN4201
CONTACT SIDE
9.GNDD
8.VLED
7.MMCL
6.CUEL
5.PLAYL
4.KEY0A
3.CUE
2.PLAY
1.GNDAD
R4212
S4204
S4203
R4211
NEXT
PREV
R4214
R4213
FWD
S4206
S4205
REV
C4209
D4201
GNDD
Q4201
R4201
D4204
D4202
D4203
DWS1394-
/J
S4202
GNDD
CUE
C4212
KSWB
R4202
Q4202
C4204
R4203
LF
R4205
C4206
C4203
R4204
D4208
D4211
C4205
D4207
D4209
PLAY/PAUSE
R4208
R4206
S4201
R4207
C4210
Q4203
D4213
CARD
Q4201
Q4202
Q4203

(DNP2255-D)

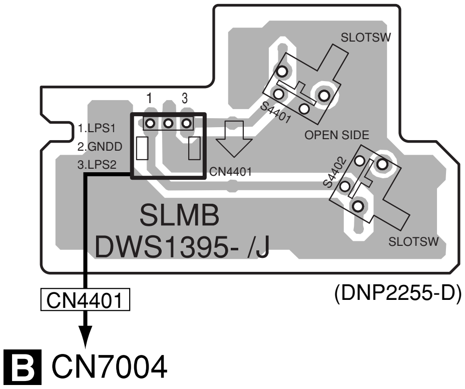
4.9 AJCB, SLMB, JOGB and PSWB ASSYS

SIDE A

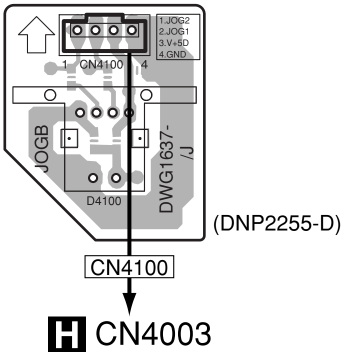
R AJCB ASSY


Q4306 Q4302 Q4307 Q4303
10
8
1
CN4303
AJCB
DWG1639-
/J
W4313
W4318
R4329
W4314
W4312
Q4306
Q4302
Q4307
Q4303
10.MODE
9.GNDD
8.CONT1
7.CONT2
6.NC
5.MUTE
4.LO
3.GNDA
2.RO
1.GNDA
JA4301
C4314
C4315
DJ ⇔ NORMAL
S4300
JA4300
(DNP2255-D)
CN4303
Q CN4302

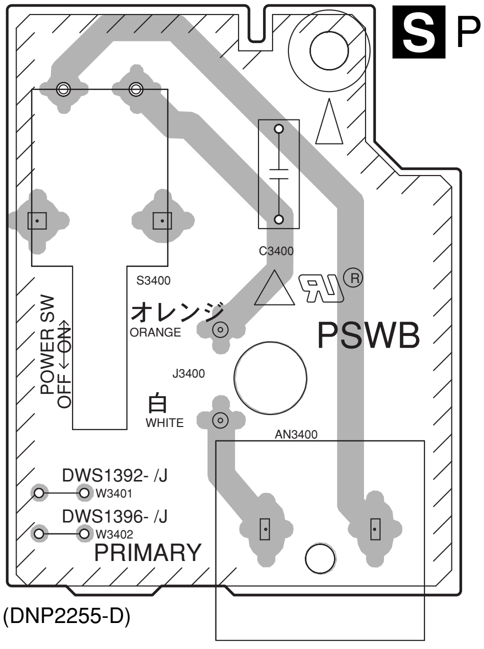
E SLMB ASSY


SLOTSW
1
3
1.LPS1
2.GNDD
3.LPS2
S4401
OPEN SIDE
CN4401
S4402
SLMB
DWS1395- /J
SLOTSW
(DNP2255-D)
CN4401
B CN7004

J JOGB ASSY

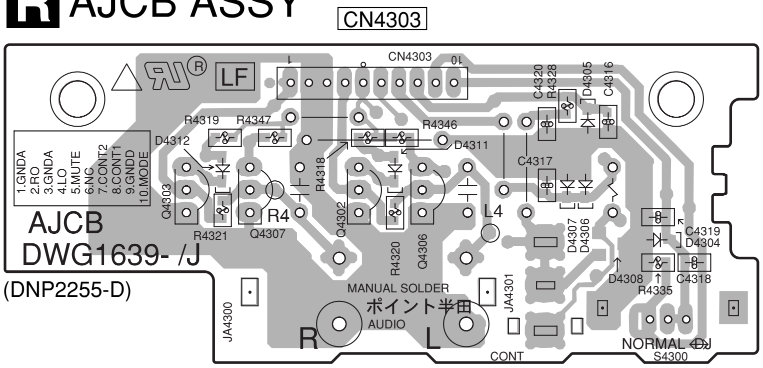

1.JOG2
2.JOG1
3.V+5D
4.GND
1
CN4100
4
JOGB
DWG1637-
/J
D4100
(DNP2255-D)
CN4100
H CN4003

S PSWB ASSY

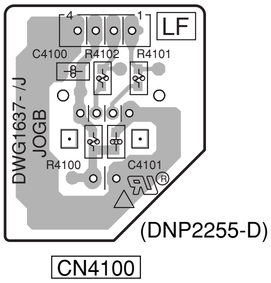

C3400
S3400
オレンジ
ORANGE
POWER SW
OFF ⇔ ON
PSWB
J3400
白
WHITE
AN3400
DWS1392- /J
W3401
DWS1396- /J
W3402
PRIMARY
(DNP2255-D)

E J R S

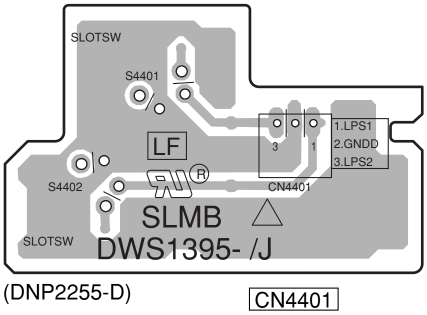
R AJCB ASSY


CN4303
CN4303
1
10
LF
R4319
R4347
R4346
D4312
D4311
R4318
C4320
R4328
D4305
C4316
C4317
1.GNDA
2.RO
3.GNDA
4.LO
5.MUTE
6.NC
7.CONT2
8.CONT1
9.GNDD
10.MODE
Q4303
R4321
Q4307
R4
Q4302
R4320
Q4306
L4
D4307
D4306
C4319
D4304
D4308
R4335
C4318
AJCB
DWG1639- /J
(DNP2255-D)
JA4300
MANUAL SOLDER
ポイント半田
AUDIO
R
L
JA4301
CONT
NORMAL
S4300

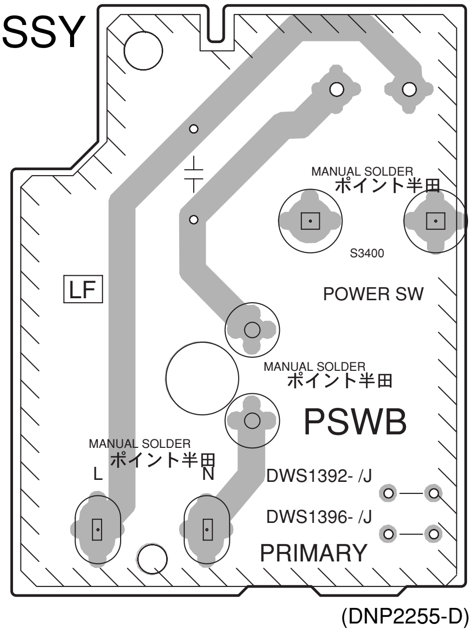
J JOGB ASSY


4
1
LF
C4100
R4102
R4101
DWG1637- /J
JOGB
R4100
C4101
(DNP2255-D)
CN4100

E SLMB ASSY


SLOTSW
S4401
1.LPS1
2.GNDD
3.LPS2
3
1
LF
S4402
CN4401
SLMB
SLOTSW
DWS1395- /J
(DNP2255-D)
CN4401

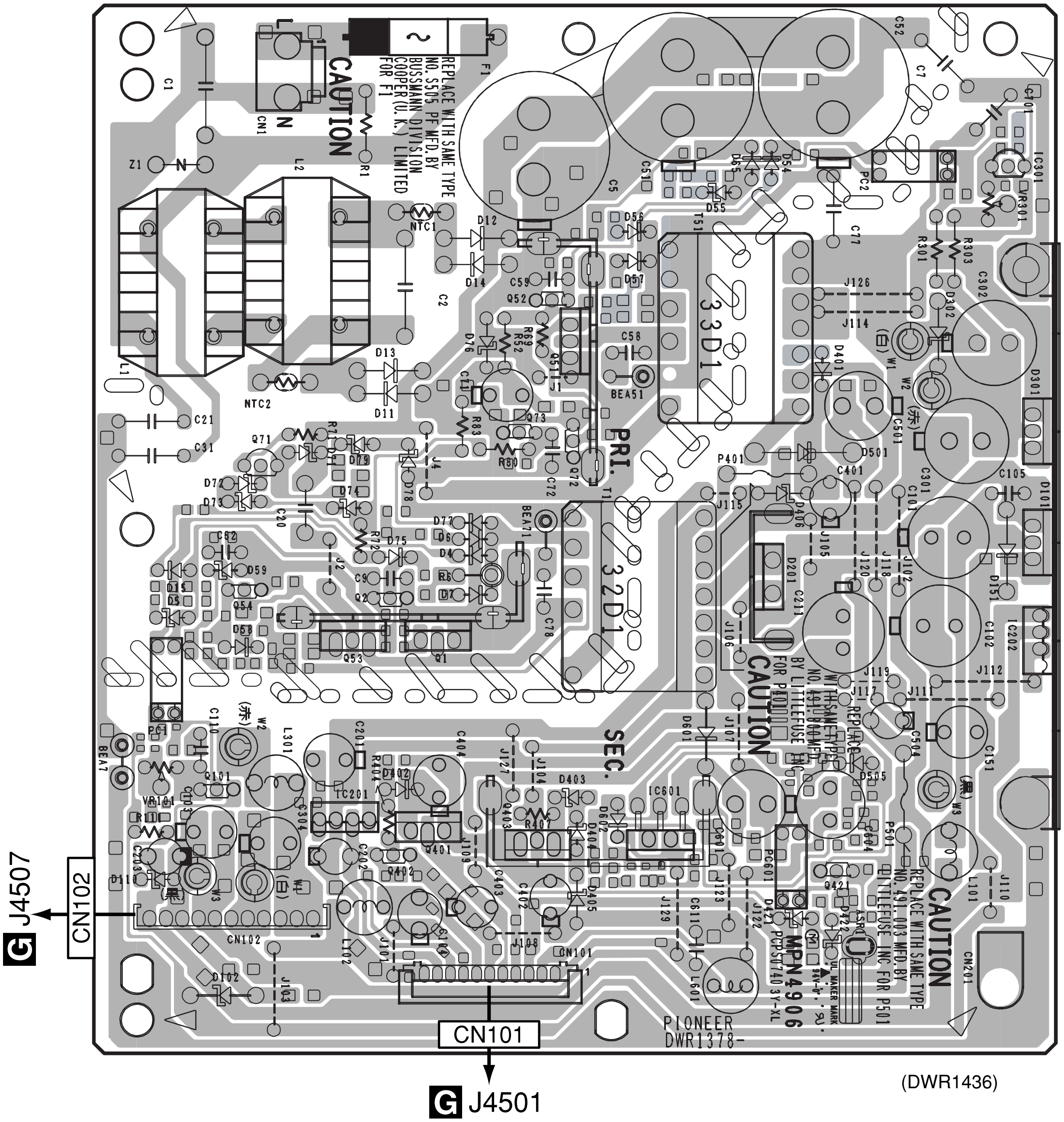
S PSWB ASSY


MANUAL SOLDER
ポイント半田
S3400
POWER SW
LF
MANUAL SOLDER
ポイント半田
PSWB
MANUAL SOLDER
ポイント半田
L
N
DWS1392- /J
DWS1396- /J
PRIMARY
(DNP2255-D)

4.10 POWER SUPPLY ASSY

SIDE A **SIDE A**

T POWER SUPPLY ASSY


CAUTION
REPLACE WITH SAME TYPE
NO. 5505 PF MFD. BY
BUSSMANN DIVISION
COOPER (U.K.) LIMITED
FOR F1
F1
CN1
L N
PRI.
SEC.
33D1
32D1
CAUTION
FOR P401
REPLACE WITH SAME TYPE NO.
BY LITTELFUSE INC
CAUTION
REPLACE WITH SAME TYPE
NO. 491.003 MFD. BY
LITTELFUSE INC FOR P501
MPN4906
PIONEER
DWR1378-
CN102
G J4507
CN101
G J4501

(DWR1436)

SIDE B **SIDE B**

A

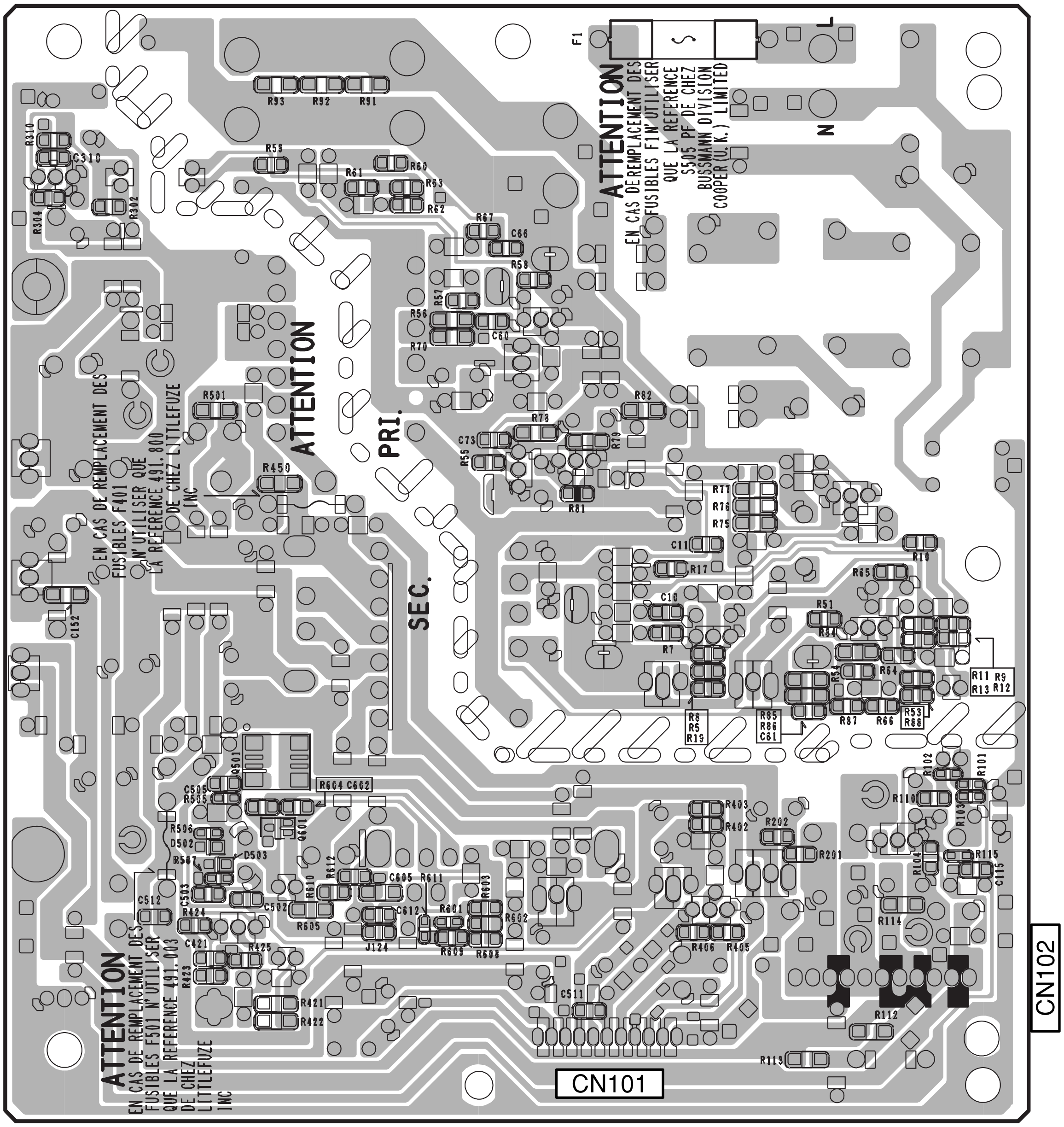
T POWER SUPPLY ASSY

B

C

D

E


F1
ATTENTION
EN CAS DE REMPLACEMENT DES
FUSIBLES F1 N'UTILISER
QUE LA REFERENCE
S505 PF DE CHEZ
BUSSMANN DIVISION
COOPER (U.K.) LIMITED
L
N
ATTENTION
EN CAS DE REMPLACEMENT DES
FUSIBLES F401 N'UTILISER QUE
LA REFERENCE 491.800
DE CHEZ LITTLEFUZE
INC
PRI.
SEC.
ATTENTION
EN CAS DE REMPLACEMENT DES
FUSIBLES F501 N'UTILISER
QUE LA REFERENCE 491.003
DE CHEZ
LITTLEFUZE
INC
R93
R92
R91
R310
C310
R304
R302
R59
R61
R60
R63
R62
R67
C66
R58
R57
R56
C60
R70
R501
R450
R82
R78
R79
C73
R55
R81
R77
R76
R75
C11
R17
R10
R65
C10
R51
R84
R7
R54
R64
R11
R9
R13
R12
R8
R5
R19
R85
R86
C61
R87
R66
R53
R88
C152
Q501
C505
R505
R604
C602
Q601
R506
D502
R507
D503
C503
C512
R424
C421
R423
R425
C502
R610
R612
R605
C605
R611
C612
J124
R601
R609
R603
R602
R608
R421
R422
C511
R403
R402
R202
R201
R406
R405
R102
R101
R110
R103
R104
R115
C115
R114
R112
R113
CN101
CN102

(DWR1436)

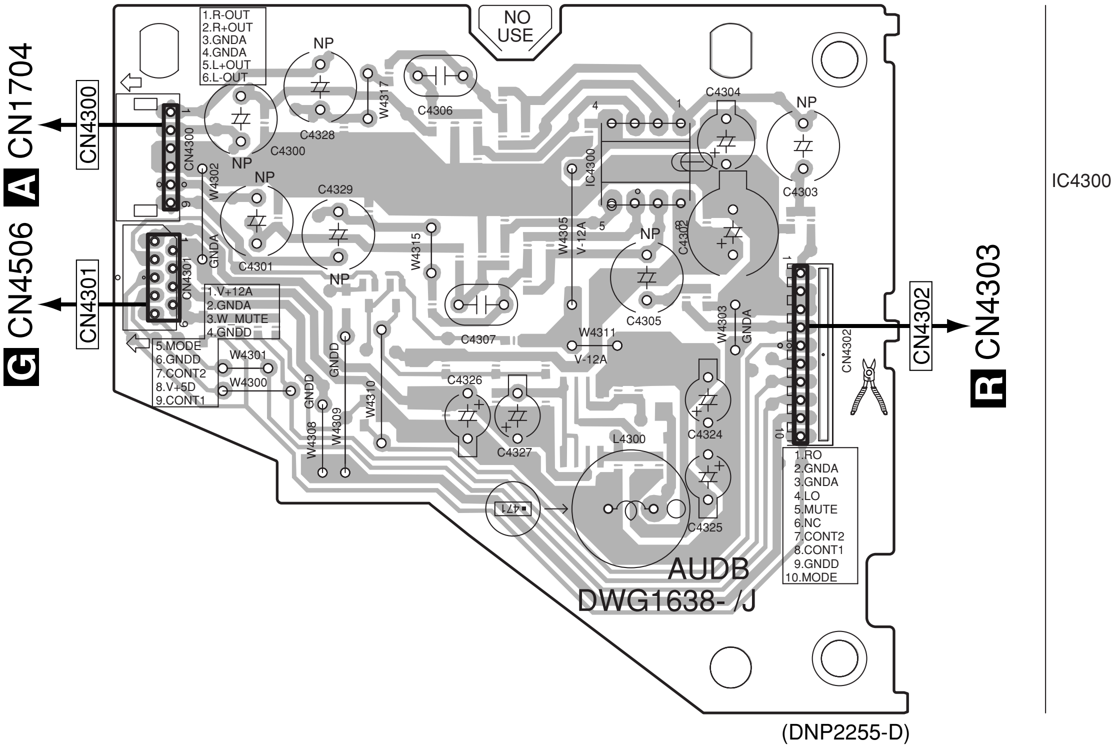
F

T **T**

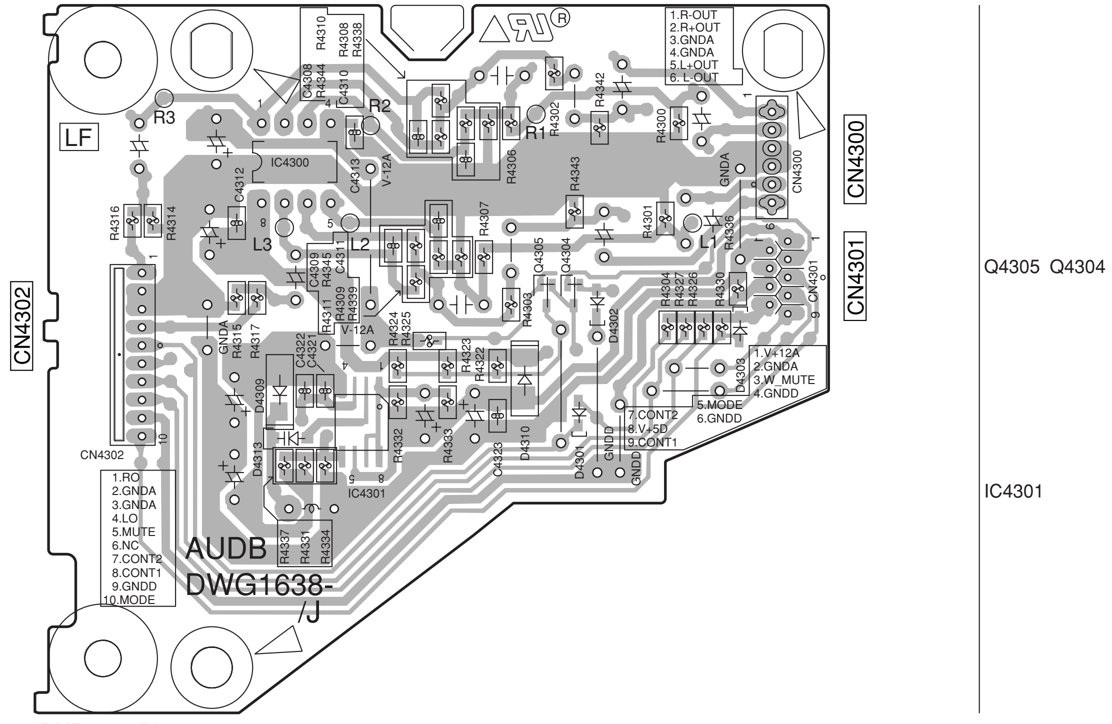
4.11 AUDB ASSY

Q AUDB ASSY

SIDE A **SIDE A**


CN4300
A CN1704
CN4301
G CN4506
CN4302
R CN4303
1.R-OUT
2.R+OUT
3.GNDA
4.GNDA
5.L+OUT
6.L-OUT
1.V+12A
2.GNDA
3.W_MUTE
4.GNDD
5.MODE
6.GNDD
7.CONT2
8.V+5D
9.CONT1
1.RO
2.GNDA
3.GNDA
4.LO
5.MUTE
6.NC
7.CONT2
8.CONT1
9.GNDD
10.MODE
NO USE
NP
C4300
C4301
C4303
C4304
C4305
C4306
C4307
C4324
C4325
C4326
C4327
C4328
C4329
C4302
IC4300
L4300
W4300
W4301
W4302
W4303
W4305
W4308
W4309
W4310
W4311
W4315
W4317
V-12A
GNDA
GNDD
AUDB
DWG1638- /J
IC4300
(DNP2255-D)

SIDE B **SIDE B**


LF
R1
R2
R3
L1
L2
L3
IC4300
IC4301
CN4300
CN4301
CN4302
1.R-OUT
2.R+OUT
3.GNDA
4.GNDA
5.L+OUT
6. L-OUT
1.V+12A
2.GNDA
3.W_MUTE
4.GNDD
5.MODE
6.GNDD
7.CONT2
8.V+5D
9.CONT1
1.RO
2.GNDA
3.GNDA
4.LO
5.MUTE
6.NC
7.CONT2
8.CONT1
9.GNDD
10.MODE
R4300
R4301
R4302
R4303
R4304
R4306
R4307
R4308
R4309
R4310
R4311
R4314
R4315
R4316
R4317
R4322
R4323
R4324
R4325
R4326
R4327
R4330
R4331
R4332
R4333
R4334
R4336
R4337
R4338
R4339
R4342
R4343
R4344
R4345
C4308
C4309
C4310
C4311
C4312
C4313
C4321
C4322
C4323
D4301
D4302
D4303
D4309
D4310
D4313
Q4304
Q4305
V-12A
GNDA
GNDD
AUDB
DWG1638- /J
Q4305 Q4304
IC4301
(DNP2255-D)

Q **Q**

5. PCB PARTS LIST

NOTES: ● *Parts marked by "NSP" are generally unavailable because they are not in our Master Spare Parts List.*

● *The ⚠ mark found on some component parts indicates the importance of the safety factor of the part. Therefore, when replacing, be sure to use parts of identical designation.*

● *When ordering resistors, first convert resistance values into code form as shown in the following examples.*

Ex.1 When there are 2 effective digits (any digit apart from 0), such as 560 ohm and 47k ohm (tolerance is shown by J=5%, and K=10%).

560 Ω → *56* x *10*^{1} → *561* .... *RD1/4PU*$\boxed{5}\boxed{6}\boxed{1}$*J*
47k Ω → *47* x *10*^{3} → *473* .... *RD1/4PU*$\boxed{4}\boxed{7}\boxed{3}$*J*
0.5 Ω → *R50* .... *RN2H*$\boxed{R}\boxed{5}\boxed{0}$*K*
1 Ω → *1R0* .... *RS1P*$\boxed{1}\boxed{R}\boxed{0}$*K*

Ex.2 When there are 3 effective digits (such as in high precision metal film resistors).

5.62k Ω → *562* x *10*^{1} → *5621* .... *RN1/4PC*$\boxed{5}\boxed{6}\boxed{2}\boxed{1}$*F*

■ LIST OF HOLE PCB ASSEMBLIES

Mark	Symbol and Description	DVJ-1000 /KUCXJ	DVJ-1000/ WYXJ5	DVJ-1000/ TLXJ/RD	DVJ-1000/ TLFXJ
	1..MAIN ASSY	DWG1630	DWG1630	DWG1630	DWG1630
NSP	1..DVPB ASSY	DWM2267	DWM2267	DWM2267	DWM2267
	2..DVPB ASSY	DWX2627	DWX2627	DWX2627	DWX2627
	2..STCN ASSY	DWX2628	DWX2628	DWX2628	DWX2628
	2..SPCN ASSY	DWX2629	DWX2629	DWX2629	DWX2629
	2..SDCB ASSY	DWX2630	DWX2630	DWX2630	DWX2630
	2..INSW ASSY	DWS1398	DWS1398	DWS1398	DWS1398
NSP	1..SUB 1 ASSY	DWM2268	DWM2269	DWM2271	DWM2270
	2..DIPB ASSY	DWG1631	DWG1640	DWG1642	DWG1641
	2..DFLB ASSY	DWG1632	DWG1632	DWG1632	DWG1632
	2..MJCB ASSY	DWG1633	DWG1633	DWG1633	DWG1633
	2..RSWB ASSY	DWS1391	DWS1391	DWS1391	DWS1391
NSP	1..SUB 2 ASSY	DWM2274	DWM2275	DWM2275	DWM2275
	2..LED 1 ASSY	DWG1634	DWG1634	DWG1634	DWG1634
	2..LED 2 ASSY	DWG1635	DWG1635	DWG1635	DWG1635
	2..JFLB ASSY	DWG1636	DWG1636	DWG1636	DWG1636
	2..JOGB ASSY	DWG1637	DWG1637	DWG1637	DWG1637
	2..AUDB ASSY	DWG1638	DWG1638	DWG1638	DWG1638
	2..AJCB ASSY	DWG1639	DWG1639	DWG1639	DWG1639
	2..PSWB ASSY	DWS1392	DWS1396	DWS1396	DWS1396
	2..SLDB ASSY	DWS1393	DWS1393	DWS1393	DWS1393
	2..KSWB ASSY	DWS1394	DWS1394	DWS1394	DWS1394
	2..SLMB ASSY	DWS1395	DWS1395	DWS1395	DWS1395
⚠	1..POWER SUPPLY ASSY	DWR1436	DWR1436	DWR1436	DWR1436

■ CONTRAST OF PCB ASSEMBLIES

I DIPB ASSY

DWG1631, DWG1640, DWG1642 and DWG1641 are constructed the same except for the following :

Mark	Symbol and Description	DWG1631	DWG1640	DWG1642	DWG1641
	R 3014	RS1/16S0R0J	Not used	RS1/16S183J	RS1/16S332J
	R 3013	Not used	RS1/16S0R0J	RS1/16S332J	RS1/16S182J

S PSWB ASSY

DWS1392 and DWS1396 are constructed the same except for the following :

Mark	Symbol and Description	DWS1392	DWS1396
⚠	AN3400 AC INLET ASSY	AKP7032	Not used
⚠	AN3400 1P AC INLEET	Not used	BKP1046

■ PCB PARTS LIST FOR DVJ-1000 UNLESS OTHER WISE NOTED

A MAIN ASSY

MISCELLANEOUS

Mark	No.	Description	Part No.
	L101		LCYA1R5J2520
	L1701		LCYA2R2J2520
	L2004,2010–2013		CTH1254
	L2005		ATH7011
	L2100–2109		CTF1305
	F102,111,112,305		CCG1160
	F306,501–503,506		CCG1160
	F507,701,702,706		CCG1160
	F901–903,905,1501		CCG1160
	F1502,1703–1706		CCG1160
	F1708–1711,1714,1715		DTF1070
	F2000–2003,2006,2007		CCG1160
	KN1701–1704		VNF1109
	VC1701		VCM1012
	X101	CRYSTAL RES. (65.97MHz)	DSS1144
	X501	CERAMIC OSCILLATOR	DSS1161
	X1701	CRYSTAL RES. (27MHz)	VSS1146
	CN102,1704	PH CNNECTOR	AKM1292
	CN501	13P FFC CONNECTOR	VKN1305
	CN701	40P FFC CONNECTOR	VKN1811
	CN1701	PH CNNECTOR	DKN1482
	CN1702	10P FFC CNNECTOR	VKN1302
	CN1703	27P FFC CNNECTOR	VKN1319
	0	ID LABEL ASSY	VXW1006

SEMICONDUCTORS

Mark	No.	Description	Part No.
	IC101,102		TC74VHC04FTS1
	IC104,110,112,115		TC74LCX541FTS1
	IC105,116,118,123		TC7SH08FUS1
	IC106,107		TC7SZ08FU
	IC108		TC74LCX16245AFT
	IC111		HD6417709SHF200B
	IC113		DYW1756
	IC114		TC74LCX32FTS1
	IC117		BU4230G
	IC119,910		TC7SZU04FU
	IC120,2012		TC74LCX541FTS1
⚠	IC121,902,2005		PQ070XZ02ZP
	IC122,911		TC7SH04FUS1
	IC124,1514,1515		TC7SH08FUS1
⚠	IC301		MM1562FF
	IC303,904–907		K4S641632K-UC75
	IC305		M65774BFPA
	IC503		TE4300PF
⚠	IC504,1708		MM1561JF
	IC505		TMS320DA150PGE16D
	IC506		DSPD56367PV150
⚠	IC507		MM1661GH
	IC103,109,702,703		K4S561632H-UC75
	IC701		PE5337B
	IC903		PD6449A
⚠	IC912		NJM2872BF33
	IC1102,2015		TC7WH34FU
	IC1104		LM1881M
	IC1501,1505		LA73054
	IC1502,1506		ADV7172KSTZ
⚠	IC1507		NJM78L05UA
	IC1510		PCM1738EG-3
	IC1511,1512		NJM5532MD
	IC1513		PD0274A
	IC1701		TC7SHU04FUS1
	IC1702		TC4W53FU
	IC1703		NJU7013F
	IC1707		SM8707HV
	IC2000		EP2C20F484C8N
	IC2001–2004		EDD5108AFTA-6B-E
⚠	IC2006,2013,2014		R1224N102H
	IC2010		SN0406014PW
⚠	IC2011		LP2996M
	Q1501,1503,1504,1511		2SA1576A
	Q1506,1509		RN4982
	Q1512,1514		2SA1576A
	Q1518		2SD2114K
	Q1523,2003		DTC114YK
	Q1701		DTC124EUA
	Q2000		CPH6314
⚠	Q2005,2006		2SJ598
	D1701		KV1837K
	D2000,2009,2011		RB051L-40
	D2001,2008,2010		1SS355

RESISTORS

Mark	No.	Description	Part No.
	R102,104–106,113		RAB4C103J
	R112,203,204		RAB4C560J
	R115–119,123,127		RAB4C220J
	R120,124,125,145		RAB4C103J
	R136–138,141-144		RAB4C220J
	R147–154,157,159		RAB4C220J
	R155,164,167,177		RAB4C103J
	R161–163,165,166		RAB4C220J
	R168,176,178-186		RAB4C220J
	R197,198,202,210		RAB4C220J
	R220		RS1/16S3300F
	R229		RS1/16S2700F
	R232,908,914,2162		RS1/16S1001F
	R301,302,306,307		RAB4C220J
	R322,326,327,332		RAB4C220J
	R336,349,352,357		RAB4C220J
	R365,367,383,385		RAB4C220J
	R387,542,543,560		RAB4C220J
	R507,508,563		RAB4C223J
	R511		RAB4C0R0J
	R515,522–524		RAB4C330J
	R573,574,584,586		RAB4C472J
	R578,579,704		RAB4C220J
	R701,712,719,727		RAB4C102J
	R703,718,725,733		RAB4C103J
	R709–711,752		RAB4C220J
	R743,744,808,813		RAB4C102J
	R758–760,769,776		RAB4C220J
	R782,789,790,795		RAB4C220J
	R797,798,802,803		RAB4C220J
	R805,823,830,910		RAB4C220J
	R814		RAB4C102J
	R819–822,824–829		RAB4C560J

Mark	No. Description	Part No.
	R911,2003–2019	RAB4C220J
	R921,924,928,931	RAB4C101J
	R925,929,932,937	RAB4C560J
	R933	RAB4C101J
	R939–944,947,948	RAB4C560J
	R950,954–964	RAB4C560J
	R967	RAB4C103J
	R1504,1513,1515,1549	RS1/16S1500F
	R1509,1543	RS1/16S6800F
	R1510,1544	RS1/16S5101F
	R1519	RN1/16SE1602D
	R1525,1572	RS1/16S4701F
	R1526–1529	RN1/16SE2200D
	R1561,1573,1740,1741	RS1/16S1500F
	R1743	RS1/16S3301F
	R2022–2040	RAB4C220J
	R2091	RS1/16S1502F
	R2094	RS1/16S7502F
	R2096,2164,2175	RS1/10S100J
	R2123–2130	RAB4C221J
	R2163	RS1/16S1001F
	R2166,2177	RS1/16S2202F
	R2170,2171	RS1/16S5102F
	Other Resistors	RS1/16S###J

CAPACITORS

Mark	No. Description	Part No.
	C101,102,104–108	CKSRYB104K16
	C103,164–166,304	CEHVKW101M6R3
	C109,110,162,163	CKSRYB105K6R3
	C112–114,116–124	CKSRYB104K16
	C115,312,516,1119	CCSRCH471J50
	C125,140,159,318	CKSRYB103K50
	C126–139,141,142	CKSRYB104K16
	C144–158,170,171	CKSRYB104K16
	C167	CCSRCJ3R0C50
	C168	CCSRCH5R0C50
	C169	CCSRCH102J50
	C301–303,305	CKSRYB104K16
	C306,503,513,515	CEHVKW101M6R3
	C313–317,326–331	CKSRYB104K16
	C333,334,339,340	CKSRYB104K16
	C343,346–348	CKSRYB104K16
	C353–355,357,359	CKSRYB104K16
	C360,369,371–376	CKSRYB104K16
	C501,916,1731,6008	CKSQYB106K6R3
	C502,538,539,546	CKSRYB103K50
	C504–507,517,543	CKSRYB105K6R3
	C508,509,512,514	CKSRYB104K16
	C518–523,525–537	CKSRYB104K16
	C540,541,544,545	CKSRYB104K16
	C542	CKSRYB222K50
	C547–552,554–557	CKSRYB104K16
	C558,560,710,734	CEHVKW101M6R3
	C559	CKSRYB471K50
	C561,994,995,1101	CKSRYB104K16
	C601–604,1610,1612	CCSRCH222J50
	C701–709,712–728	CKSRYB105K6R3
	C729,909,1501,1502	CKSRYB103K50
	C730–733,736–746	CKSRYB105K6R3
	C902,903,908	CKSRYB105K6R3

Mark	No. Description	Part No.
	C906,907,986,987	CEHVKW101M6R3
	C910–915,917–978	CKSRYB105K6R3
	C980–984,990,991	CKSRYB105K6R3
	C992,993	CCSRCH180J50
	C1112,1113,1503,1506	CKSRYB104K16
	C1118	DCH1260
	C1507,1511–1513,1515	CKSRYB104K16
	C1508,1514,1520,1534	CKSRYB103K50
	C1509,1530,1546,1580	CEHVW470M10
	C1510,1517,1526,1531	CKSRYB105K10
	C1516,1518,1519,1521	CKSRYB104K16
	C1522,1527,1528,1540	CKSRYB104K16
	C1541,1627,6157	CEHVW470M6R3
	C1543,1544,1552–1556	CKSRYB104K16
	C1545,1567,1579,1629	CKSRYB103K50
	C1547,1557,1575,1581	CKSRYB105K6R3
	C1566,1568,1573,1574	CKSRYB104K16
	C1577,1578,1587,1590	CKSRYB104K16
	C1586,1592,1593,1622	CEHVW470M16
	C1588,1603,1619,1740	CEHVKW101M6R3
	C1589,1623,1721,6003	CKSRYB105K6R3
	C1591,1620,1621,1630	CKSRYB104K16
	C1614,1616	CCSRCH222J50
	C1625,1710,1712,1714	CEHVW470M16
	C1631,1636,1637,1709	CKSRYB104K16
	C1711,1713,1715–1717	CKSRYB104K16
	C1719,1732–1734,1736	CKSRYB104K16
	C1720	CCSRCH470J50
	C1723	CCSRCK2R0C50
	C1725,6005,6007,6015	CKSRYB103K50
	C1727–1729	CCSRCH221J50
	C1735	CKSRYB332K50
	C1737	CKSRYB472K50
	C1741,1742,5007–5010	CKSRYB104K16
	C1743	CCSRCH471J50
	C1794	CCSRCH680J50
	C5012–5014,5017–5020	CKSRYB104K16
	C5024,5037–5039	CKSRYB104K16
	C5049–5052,5054,5055	CKSRYB104K16
	C5057,5058,5083–5085	CKSRYB104K16
	C5201–5209,6004,6010	CKSRYB104K16
	C6009,6011,6013,6016	CKSRYB105K6R3
	C6012,6018,6019,6024	CKSQYB106K6R3
	C6014,6021,6023	CKSRYB104K16
	C6017,6020,6025,6041	CKSRYB103K50
	C6022,6029,6039,6043	CKSRYB105K6R3
	C6026–6028,6030–6036	CKSRYB104K16
	C6037,6046,6064	CKSQYB106K6R3
	C6038,6040,6042,6044	CKSRYB104K16
	C6045,6047,6054,6058	CKSRYB105K6R3
	C6048,6051–6053	CKSRYB104K16
	C6049,6050,6059,6110	CKSRYB103K50
	C6055–6057,6060–6063	CKSRYB104K16
	C6065,6178,6186	CCSRCH121J50
	C6066,6068–6070,6176	DCH1165
	C6072,6184,6190	CKSQYB104K25
	C6074–6082,6084,6156	CEHVKW101M6R3
	C6104,6106,6107,6109	CKSRYB104K16
	C6105	CEHVW220M6R3
	C6111,6113,6117,6119	CKSRYB104K16

Mark	No.	Description	Part No.
	C6114,6123,6126,6131		CKSRYB105K6R3
	C6116,6118,6120,6124		CKSRYB103K50
	C6121,6122,6125,6128		CKSRYB104K16
	C6127,6129,6132,6140		CKSRYB103K50
	C6130,6133,6134,6141		CKSRYB104K16
	C6135–6139,6142,6145		CKSRYB105K6R3
	C6143,6146,6147,6151		CKSRYB104K16
	C6144,6149,6150,6160		CKSRYB103K50
	C6148,6152,6172,6175		CKSRYB105K6R3
	C6153–6155,6158,6159		CKSRYB104K16
	C6161		CKSRYB103K50
	C6162–6171,6195		CKSRYB104K16
	C6173,6174,6180–6182		CEHVKW101M6R3
	C6177,6179,6183,6185		DCH1165
	C6187,6188,6192		DCH1165
	C6189,6191,6193		CEHVKW101M6R3
	C6201,6202		CKSRYB105K6R3

B DVPL ASSY

SEMICONDUCTORS

Mark	No.	Description	Part No.
	IC7000		AN22022A
	IC7100		MN103S71F
	IC7101		PEA008C8
	IC7200		BD7905BFS
⚠	IC7300		MM1478DFBE
⚠	IC7301		BA15BC0WFP
	Q7001,7002		2SA1577
	Q7003		UM5K1N
	Q7201		DTC124EUA
	Q7202		2SJ598
	Q7301,7302		DTC114YK

MISCELLANEOUS

Mark	No.	Description	Part No.
	L7101–7116	CHIP BEAD	CTF1334
	L7300	CHIP COIL	OTL1009
	KN7004	EARTH METAL FITING	VNF1109
	X7100	CHIP CERALOCK(16MHz)	DSS1107
	CN7001	26P FFC CONNECTOR	DKN1448
	CN7002	13P FFC CONNECTOR	DKN1275
	CN7003	4P FFC CONNECTOR	DKN1288
	CN7004	3P PH CONNECTOR	AKM1289
	CN7005	2P PH CONNECTOR	AKM1288
	CN7007	40P FFC CONNECTOR	VKN1811
	CN7008	8P PH CONNECTOR	AKM1294

RESISTORS

Mark	No.	Description	Part No.
	R7010		RS1/4SA120J
	R7011,7302–7304		RS1/4SA240J
	R7112		RS1/16S1502F
	R7114,7115,7201–7203		DCN1143
	R7151–7155,7163		RAB4C330J
	R7214,7215		RS1/4SA2R0J
	R7221–7223		DCN1143
	Other Resistors		RS1/16S###J

CAPACITORS

Mark	No.	Description	Part No.
	C7001–7004,7006–7008		CKSRYB104K16
	C7009		CKSRYB561K50
	C7010,7112		CKSRYB273K16
	C7011,7134		CKSRYB473K16
	C7013,7015,7021,7023		CKSRYB104K16
	C7014,7114,7115,7121		CKSRYB102K50
	C7016,7204		CCSRCH151J50
	C7020,7022,7308,7312		CEHVW470M6R3
	C7100,7103,7167		CKSSYB105K6R3
	C7104,7300		CKSRYB105K6R3
	C7105–7107,7110,7116		CKSRYB104K16
	C7108,7111,7120,7122		CKSSYB104K10
	C7109,7211		CKSRYB103K50
	C7113,7144,7145,7147		CCSRCH101J50
	C7117,7138,7141,7142		CKSRYB104K16
	C7118		CKSRYB122K50
	C7119,7208,7209		CKSRYB472K50
	C7123,7135		CKSRYB471K50
	C7124,7136,7137,7140		CKSRYB182K50
	C7125		CKSRYB222K50
	C7126		CKSRYB562K50
	C7127–7130,7132,7165		CKSSYB104K10
	C7133		CKSRYB474K10
	C7139,7149,7154		CKSRYB102K50
	C7148		CCSRCH101J50
	C7160–7164,7166,7168		CCSSCH221J50
	C7169		CKSSYB104K10
	C7170–7173		CCSSCH221J50
	C7201,7202,7315,7316		CEHVW470M16
	C7203,7206,7217,7221		CKSRYB104K16
	C7205		CCSRCH121J50
	C7207,7303,7305,7310		CEHVW101M6R3
	C7218–7220		CKSRYB102K50
	C7301,7304,7306,7307		CKSRYB104K16
	C7302		CCH1426
	C7309,7313,7317		CKSRYB104K16
	C7311		CEHVW101M6R3

C SPCN ASSY

MISCELLANEOUS

Mark	No.	Description	Part No.
	CN7401	13P FFC CONNECTOR	DKN1275
	CN7402	12P FFC CONNECTOR	DKN1205
	CN7403	2P ZH CONNECTOR	VKN1940

D STCN ASSY

MISCELLANEOUS

Mark	No.	Description	Part No.
	CN7405	4P FFC CONNECTOR	DKN1288
	CN7406	4P FFC CONNECTOR	VKN1409

E SLMB ASSY

MISCELLANEOUS

Mark	No.	Description	Part No.
	S4401,4402	SWICHES	DSG1017
	CN4401	KR CONNECTOR	S3B-PH

F SDCB ASSY

MISCELLANEOUS

Mark	No.	Description	Part No.
	CN7501	SD CONNECTOR	DKP3748
	CN7502	13P CONNECTOR	VKN1305
	S7501	SWICHE	CSN1068

SEMICONDUCTORS

Mark	No.	Description	Part No.
	D7501,7503,7505,7507		DAP202K
	D7502,7504,7506,7508		DAN202K
	D7509		DAN217

CAPACITORS

Mark	No.	Description	Part No.
	C7501,7503		CKSRYB104K16
	C7504		CEHVW101M6R3

RESISTORS

Mark	No.	Description	Part No.
	All Resistors		RS1/16S###J

G MJCB ASSY

SEMICONDUCTORS

Mark	No.	Description	Part No.
⚠	IC4500	(250mA)	AEK7002
⚠	IC4501,4510	(800mA)	AEK7008
⚠	IC4502	(2A)	AEK7013
⚠	IC4503	(315mA)	AEK7003
⚠	IC4504,4507	(1.6A)	AEK7012
⚠	IC4505,4506	(2.5A)	AEK7014
	IC4508		TC7SET08FUS1
	IC4509		TC7SET04FUS1
⚠	Q4500	MOS FET	CPH3323
	Q4501,4503		2SC1740S
	Q4502		2SD1859X
	Q4504		2SC2412K
	D4500–4503		DF3A5.6FU
	D4504–4506		1SS355

MISCELLANEOUS

Mark	No.	Description	Part No.
	L4501	CHIP BEAD	VTL1169
	J4501	CONNECTOR ASSY 6P	DKP3771
	J4507	11P CONNECTOR ASSY	DKP3772
	JA4500	2P PIN JACK	VKB1123
	JA4502	4P MINI DIN SOCKET	AKP7116
	JA4503	1P PIN JACK	VKB1074
	JA4504	BNC CONNECTOR	DKN1415
	CN4500	KR CONNECTOR	B8B-PH
	CN4502	10P FFC CONNECTOR	VKN1241
	CN4504	KR CONNECTOR	B6B-PH-K-E
	CN4505	27P FFC CONNECTOR	VKN1258
	CN4506	9P FFC CONNECTOR	VKN1240
	CN4508	22P FFC CONNECTOR	HLEM22S-1

RESISTORS

Mark	No.	Description	Part No.
	R4509		RD1/2VM470J
	R4513,4514,4521		RS1/16S1100F
	R4523–4525,4527		RS1/16S1100F
	R4531		RS1/16S75R0F
	R4534		RS1/16S1801F
	R4537		RS1/16S3901F
	R4542–4548		RS1/16S2000F
	Other Resistors		RS1/16S###J

CAPACITORS

Mark	No.	Description	Part No.
	C4500		CKSRYB103K50
	C4501–4505		CKSRYB104K16
	C4506		CEANP220M10
	C4507		CEHAT100M50
	C4508–4511,4515		CEAT102M10
	C4512		CEAT470M10
	C4513		CEHAT221M25
	C4514		CEAT470M16
	C4516,4517		CCSRCH220J50

H JFLB ASSY

MISCELLANEOUS

Mark	No.	Description	Part No.
	0	FL HOLDER	DNF1735

SEMICONDUCTORS

Mark	No.	Description	Part No.
	Q4001,4002		2SC1815
⚠	Q4003		2SD1760F5
	D4002,4003,4005		UDZS9R1(B)
	D4004		1SR154-400
	D4006		PTZ6R8(B)

MISCELLANEOUS

Mark	No.	Description	Part No.
	V4000	FL TUBE	DEL1058
	CN4000	22P FFC CONNECTOR	52492-2220
	CN4001	25P FFC CONNECTOR	52492-2520
	CN4002	CONNECTOR	CKS1072
	CN4003	KR CONNECTOR	S4B-PH

RESISTORS

Mark	No.	Description	Part No.
	R4000		RD1/2VM272J
	R4013,4018		RD1/2VM220J
	R4021–4023		RS1/10S0R0J
	Other Resistors		RS1/16S###J

CAPACITORS

Mark	No.	Description	Part No.
	C4000		CKSRYB103K50
	C4001		CKSRYB104K50
	C4002,4003,4008,4011		CCSRCH221J50
	C4004		CKSRYB102K50
	C4005,4007,4010,4012		CEHAZL100M50
	C4006,4009		CEHAR470M16

I DIPB ASSY

MISCELLANEOUS

Mark	No.	Description	Part No.
	X3001	CERAMIC RESO.(16MHz)	DSS1149
	0	2P CABLE HOLDER	51048-0200
	CN3005	FFC CONNECTOR 9P	52492-0920
	CN3004	FFC CONNECTOR 10P	52492-1020
	S3001-S3003, S3007-S3009		DSG1079
	S3004-S3006, S3010-S3014		VSG1024
	S3017-S3032, S3102		VSG1024
	CN3002	FFC CONNECTOR 25P	52492-2520
	CN3001	B TO B CONNECTOR 16P	DKN1468
	L3002, 3003	CHIP INDUCTOR	CTF1306
	L3001	INDUCTOR	LRCA331J

SEMICONDUCTORS

Mark	No.	Description	Part No.
	IC3004		PEG237B
	IC3009		BU4242G
	Q3002-Q3022		DTC124EUA
	IC3002, 3007, 3008		TC7SET08FUSI
	D3005, 3007, 3009, 3011, 3019, 3021		SLI-343URCW(RST)
	D3012- 3015, 3022- 3026		SLI-343YYW(RST)
	D3003		SLR-343BBT(HJKL)
	D3006, 3008, 3010		SLR-343EBT(KLMN)
	D3016- 3018, 3020		TLGE68TG

CAPACITORS

Mark	No. Description	Part No.
	C3005, 3006	CCSRCH150J50
	C3002	CEAT331M10
	C3004, 3011	CEHAT470M16
	C3012, 3015, 3018, 3020, 3023	CKSRYB102K50
	C2025, 3047, 3048, 3049, 3050	CKSRYB102K50
	C3051- 3054	CKSRYB102K50
	C3007, 3009, 3027- 3031, 3036	CKSRYB103K50
	C3045, 3063, 3065	CKSRYB103K50
	C3003, 3013, 3014, 3032, 3033	CKSRYB104K16
	C3010	CKSRYB105K10
	C3008	CKSRYB221K50

RESISTORS

Mark	No. Description	Part No.
	VR3001, VR3002 (10KB)	DCS1045
	Other Resistors	RS1/16S###J

J JOGB ASSY

SEMICONDUCTORS

Mark	No. Description	Part No.
	D4100	GP1A038RBK

MISCELLANEOUS

Mark	No. Description	Part No.
	CN4100 KR CONNECTOR	B4B-PH
	All Resistors	RS1/16S###J

CAPACITORS

Mark	No. Description	Part No.
	C4100,4101	CKSRYB104K16

K LED2 ASSY

MISCELLANEOUS

Mark	No. Description	Part No.
0	2P CABLE HOLDER	51048-0200

SEMICONDUCTORS

Mark	No. Description	Part No.
	D3370	NSPB500-0008

MISCELLANEOUS

Mark	No. Description	Part No.
	All Resistors	RS1/16S273J

CAPACITORS

Mark	No. Description	Part No.
	C3370	CKSRYB103K50

L LED1 ASSY

MISCELLANEOUS

Mark	No. Description	Part No.
0	2P CABLE HOLDER	51048-0200

SEMICONDUCTORS

Mark	No. Description	Part No.
	D3350	NSPB500-0008

RESISTORS

Mark	No. Description	Part No.
	All Resistors	RS1/16S###J

CAPACITORS

Mark	No. Description	Part No.
	C3350	CKSRYB103K50

M SLDB ASSY

MISCELLANEOUS

Mark	No. Description	Part No.
0	2P CABLE HOLDER	51048-0200

SEMICONDUCTORS

Mark	No. Description	Part No.
	Q3300	DTC124EUA
	D3300	TLGE68TG(NP)
	D3301–3304	1SS355

MISCELLANEOUS

Mark	No. Description	Part No.
	VR3300	DCV1013
	VR3301	DCP1105
	S3300	VSG1024
	CN3300	52492-1020
	All Resistors	RS1/16S###J

CAPACITORS

Mark	No. Description	Part No.
	C3300,3303,3304	CKSRYB104K16
	C3302	CKSRYB103K50
	C3305	CCSRCH101J50

N DFLB ASSY

MISCELLANEOUS

Mark	No. Description	Part No.
0	FL HOLDER	DNF1737

SEMICONDUCTORS

Mark	No. Description	Part No.
	D3202	PTZ11(B)

MISCELLANEOUS

Mark	No. Description	Part No.
	JV3200	DEL1062
	S3200	DSH1049
	S3201–3204	VSG1024
	CN3200	S6B-PH
	CN3201	VKN1368
	All Resistors	RS1/16S###J

CAPACITORS

Mark	No. Description	Part No.
	C3200	CCSRCH101J50
	C3201	CKSRYB103K50
	C3202–3204	CKSRYB104K16
	C3205	CEHAT470M50
	C3206	CEHAT100M50
	C3207	CEHAR470M16
	C3208–3211	CCSRCH221J50

O RSWB ASSY

MISCELLANEOUS

Mark	No. Description	Part No.
0	2P CABLE HOLDER	51048–0200
	S3180	DSK1047

P KSWB ASSY

SEMICONDUCTORS

Mark	No. Description	Part No.
	Q4201–4203	DTC124EUA
	D4201–4204	SLI-343YYW(RST)
	D4207–4209,4211	SLR-343BBT(HJKL)
	D4213	TLGE68TG(NP)

MISCELLANEOUS

Mark	No. Description	Part No.
	S4201,4202 SWITCH	DSG1079
	S4203–4206 SWITCH	VSG1024
	CN4201 9P FFC CONNECTOR	52492-0920
	All Resistors	RS1/16S###J

CAPACITORS

Mark	No.	Description	Part No.
	C4203–4206		CKSRYB103K50
	C4209,4210,4212		CCSRCH101J50

Q AUDB ASSY

SEMICONDUCTORS

Mark	No.	Description	Part No.
	IC4300		NJM4580D
⚠	IC4301		NJM2374AM
	Q4304		DTA124EUA
	Q4305		DTC124EUA
	D4301–4303		1SS355
	D4309		EC10QS04
	D4310		1SR154-400
	D4313		EP05Q04

MISCELLANEOUS

Mark	No.	Description	Part No.
	L4300	INDUCTER	DTL1122
	CN4300	KR CONNECTOR	S6B-PH
	CN4301	9P FFC KR CONNECTOR	VKN1269
	CN4302	B TO B CONNECTOR	VKN1362

RESISTORS

Mark	No.	Description	Part No.
	R4300,4301,4314,4315		RN1/16SE4702D
	R4302,4303,4306–4309		RN1/16SE3301D
	R4310,4311,4338,4339		RN1/16SE1002D
	R4331,4337		RS1/16S5101F
	R4334		RS1/16S2202F
	R4342,4343		RN1/16SE4702D
	R4344,4345		RN1/16SE3301D
	Other Resistors		RS1/16S###J

CAPACITORS

Mark	No.	Description	Part No.
	C4300,4301,4303,4305		CEAT101M50
	C4302		CEAT471M25
	C4304		CEAT221M16
	C4306,4307		CQMBA561J50
	C4308–4311,4321		CCSRCH220J50
	C4312,4313,4323		CKSRYB104K16
	C4322		CCSRCH181J50
	C4324,4326,4327		CEHAT101M16
	C4325		CEHAT330M25
	C4328,4329		CEAT101M50

R AJCB ASSY

SEMICONDUCTORS

Mark	No.	Description	Part No.
	Q4302,4303,4306,4307		2SD2144S
	D4304,4311,4312		1SS355
	D4305,4306		UDZS5R6(B)
	D4308		NNCD6.2MF

MISCELLANEOUS

Mark	No.	Description	Part No.
	JA4300	2P PIN JACK	DKB1074
	JA4301	REMOTE CONTROL JACK	RKN1004
	S4300	SWITCH	DSH1025
	CN4303	B TO B CONNECTOR	DKN1477

RESISTORS

Mark	No.	Description	Part No.
	R4329		RD1/2VM271J
	Other Resistors		RS1/16S###J

CAPACITORS

Mark	No.	Description	Part No.
	C4314,4315		CQMBA102J50
	C4316,4317		CKSRYB103K50
	C4318,4319		CKSRYB104K16

S PSWB ASSY

MISCELLANEOUS

Mark	No.	Description	Part No.
⚠	AN3400	1P AC INLET	BKP1046
⚠	J3400	CONNECTOR ASSY	DKP3779
⚠	S3400	AC SWITCH	DSA1029

U INSW ASSY

MISCELLANEOUS

Mark	No.	Description	Part No.
	S7404		CSN1031
	CN7404		VKN1940

T POWER SUPPLY ASSY

There is no service parts.

6. ADJUSTMENT

6.1 TEMPO ZERO POINT ADJUSTMENT

A

■ Slider Zero Point Adjustment

■ Necessary Adjustment Points

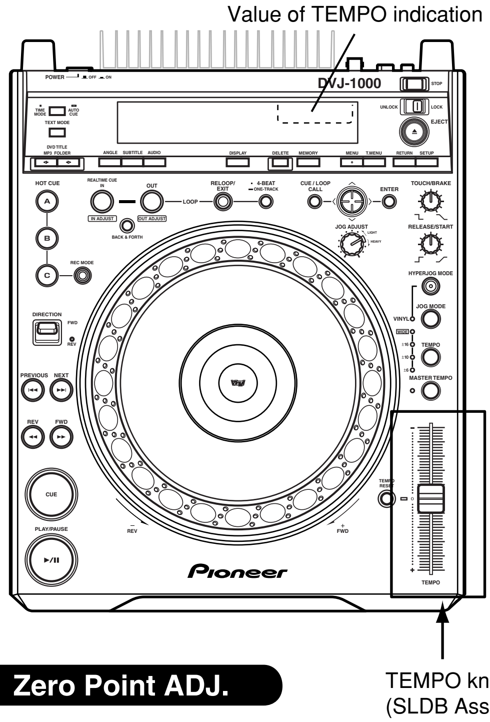
B

When		Adjustment points
Exchange the SLIDER VR	➡	VR3301 (Zero Point ADJ.)

Adjustment and Check Points

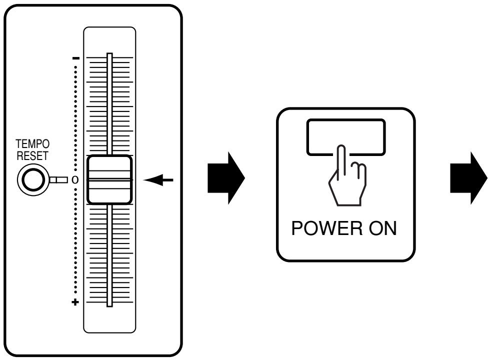
C

D


Value of TEMPO indication
DVJ-1000
Pioneer
TEMPO knob
(SLDB Assy)

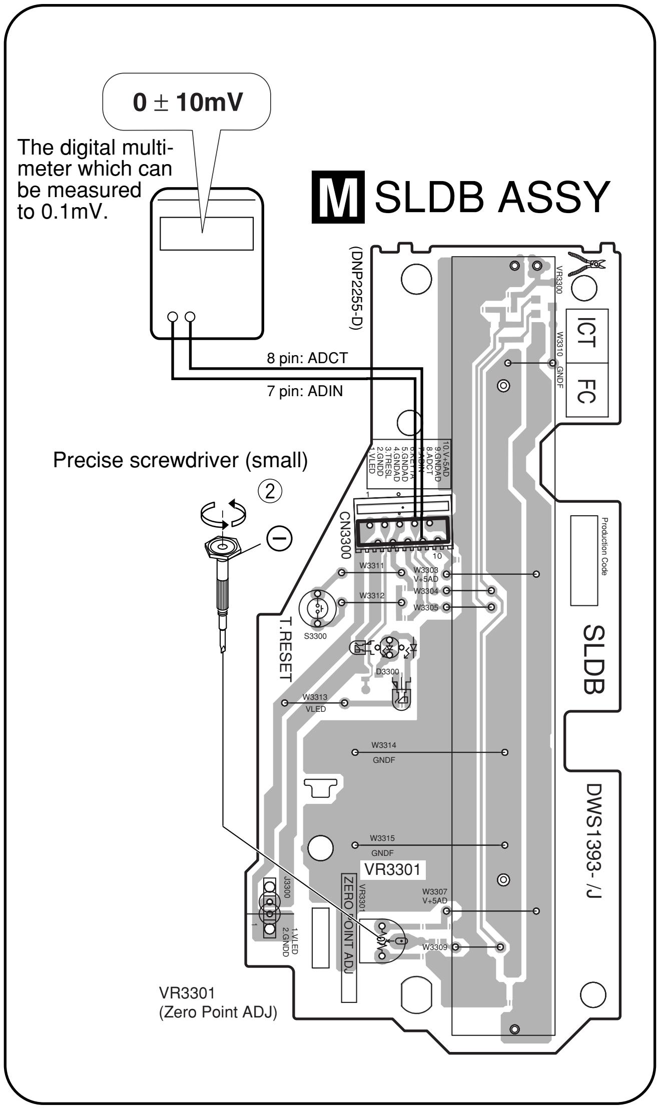
E

Zero Point ADJ.


TEMPO
RESET
0
–
+
POWER ON

F

Set the TEMPO knob to the center "0" point.


0 ± 10mV
The digital multi-meter which can be measured to 0.1mV.
M SLDB ASSY
(DNP2255-D)
8 pin: ADCT
7 pin: ADIN
Precise screwdriver (small)
②
ICT
FC
1.VLED
2.GNDD
3.TRESL
4.GNDAD
5.GNDAD
6.KEYTA
7.ADIN
8.ADCT
9.GNDAD
10.V+5AD
CN3300
W3311
W3303 V+5AD
W3304
W3305
W3312
S3300
T.RESET
D3300
W3313 VLED
W3314 GNDF
W3315 GNDF
VR3301
ZERO POINT ADJ
W3307 V+5AD
W3309
SLDB
DWS1393- /J
Production Code
VR3301
(Zero Point ADJ)

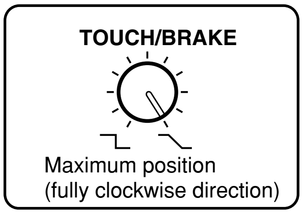
6.2 MODE FOR CHECKING THE LOAD ON THE JOG DIAL

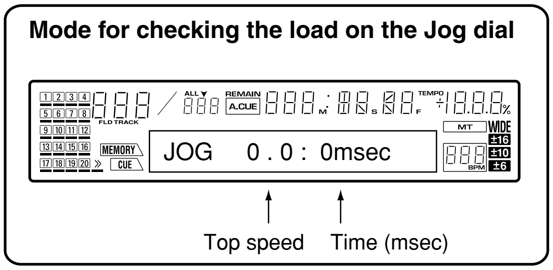
• Check mode of the load of JOG dial

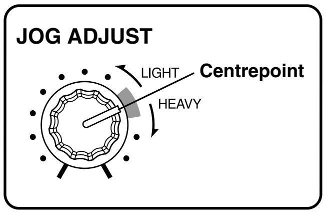
• It is the mode which judges the load (light/-- heavy) numerically when rotating JOG dial.

It will become this mode if a power supply is switched on pushing the TEMPO button and the MEMORY button together.
If it goes into this mode, it will be displayed on FL dot-matrix part as "SERVICE. MODE".
In this mode, it can check whether a display microcomputer can be made to turn on partially as follows, the input of each button and volume is normal, and a display is normal. In addition, a display is turned on only while pushing the button.

JOG Check Mode : ON


TOUCH/BRAKE
Maximum position
(fully clockwise direction)


Mode for checking the load on the Jog dial
MEMORY
CUE
A.CUE
REMAIN
WIDE
±16
±10
±6
JOG 0 . 0 : 0msec
Top speed
Time (msec)


JOG ADJUST
LIGHT
Centrepoint
HEAVY

Measuring method

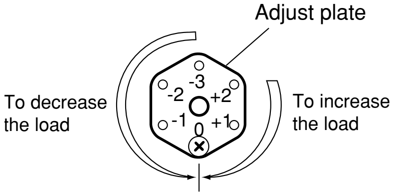
Enter this mode, and a number will be displayed if JOG dial is turned with sufficient vigor. The rotation direction -- clockwise direction and counterclockwise direction -- either is O.K.
For example, when displayed as "JOG 8.6: 115msec", the following contents are shown, respectively.
8.6 = What time speed came out by the highest. (The time of turning one rotation in 1.8 seconds is made into 1 time.)
115 = Time taken for rotation to fall by 1.5 times from 3 times (msec)

It is necessary to make it rotate top speed to 7.0 or more times to measure the rotation fall time required.
When it carries out continuously several times, the time which took the average with a part for greatest ever 4 time is displayed 2nd henceforth.
Measurement which absorbed variation can be performed by performing this.

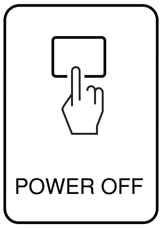
Jog Management Value : 150 ± 25 (msec) ← It judges by the 3rd measured value.

Load adjustment method

Remove the screw fixing the adjust plate, then screw it into the hole corresponding to the value (-1, -2, -3, +1, or +2) for a load to be added:
-1, -2, -3 : To decrease the load
+1, +2 : To increase the load


Adjust plate
To decrease the load
To increase the load
-3
-2
+2
-1
+1
0

JOG Check Mode : CANCEL



6.3 MAIN ASSY ADJUSTMENT

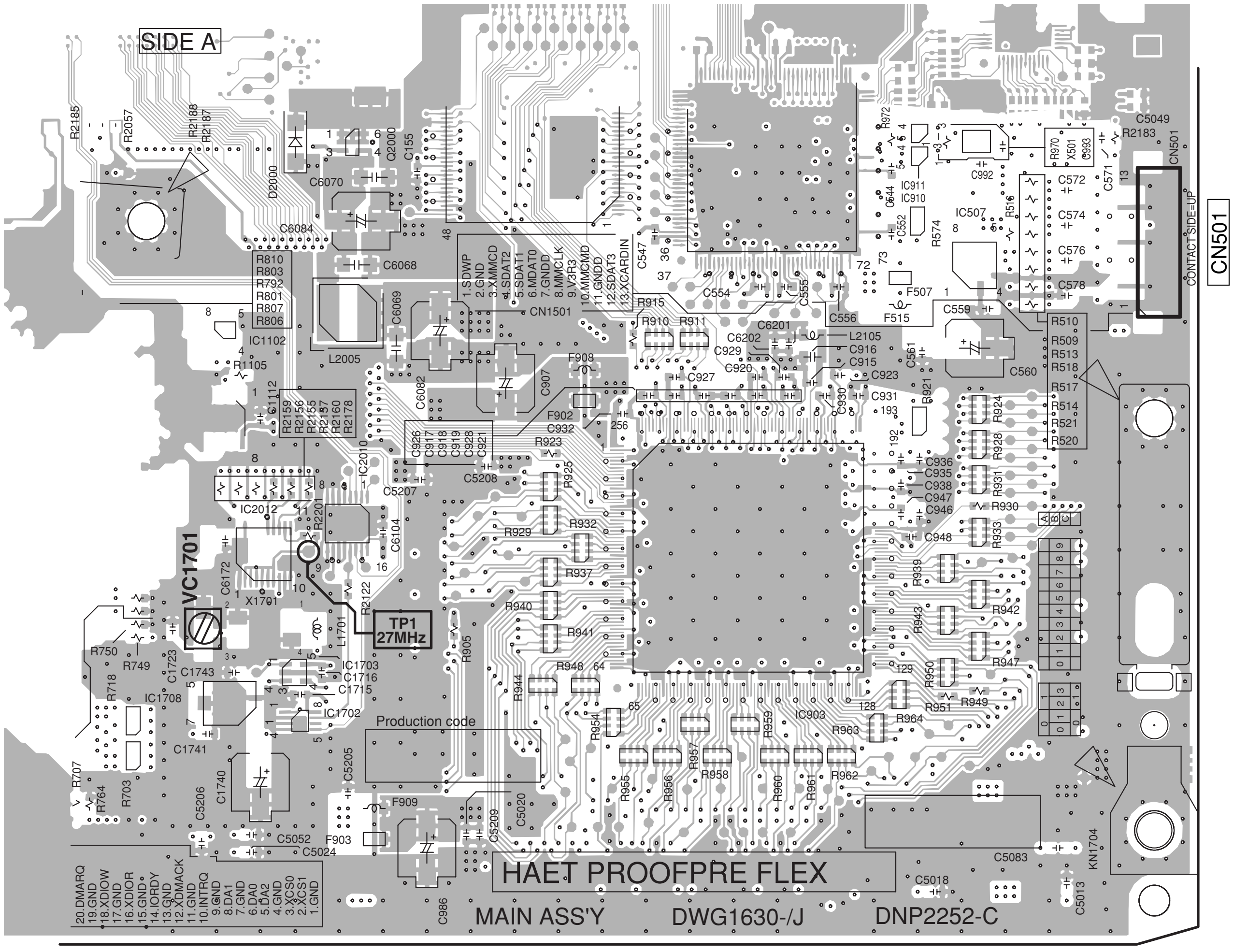
MAIN Assy Adjustment

■Master Clock Free-running Adjustment

Adjustment Condition	SYNC signal is no input. No disc
Adjustment point	VC1701
Measurement point	TP1
Measuring Instrument	Frequency counter
Adjustment Value	27 MHz ± 135 Hz
Procedures	Adjust VC1701 so that the frequency of TP1 becomes 27 MHz ± 135 Hz.
Symptom of an adjus tment error	Displays are disrupted, not correct, or stop.

Check Points

MAIN ASSY


SIDE A
VC1701
TP1
27MHz
X1701
IC2012
IC1702
IC1703
CN1501
CN501
CONTACT SIDE=UP
IC903
IC507
Production code
HAET PROOFPRE FLEX
MAIN ASS'Y
DWG1630-/J
DNP2252-C

(DNP2252-C)

7. GENERAL INFORMATION

7.1 DIAGNOSIS

7.1.1 SERVICE MODE

1.The outline in service mode

This machine is controlled by three microcomputers, a display microcomputer and a player microcomputer and a system microcomputer,and test mode is prepared for each.
A display microcomputer performs processing to a button input, display of FL, and lighting of LED. A player microcomputer drives a player.

[1]Version check mode / Error display mode
It is the mode which can check the version of the software of each microcomputer.
The history of the contents of an error can be checked to 16 pieces.

[2]Check mode of a button and a display function of a display microcomputer.
It is the mode which checks a button input and a display function.

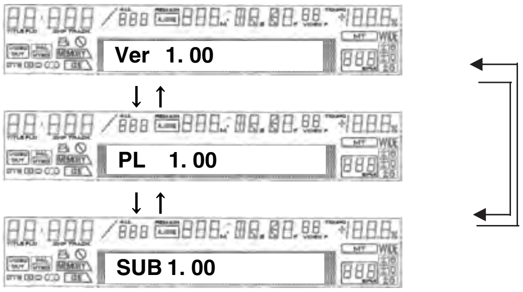
[3]Check mode of the load of JOG dial
It is the mode which measures the load when rotating JOG dial.

[4]Check mode of operation of a player microcomputer
It is the mode which performs the check of the mechanism and serbo of a player microcomputer of operation.
This mode consists of "player operation mode" and "test operation mode."

[5]How to input the ID number

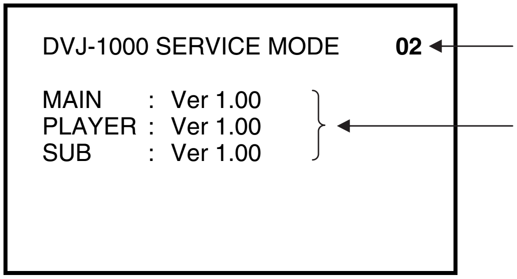
2. Version check mode / Error display mode

It is the mode which checks the software version of each microcomputer.
It will become this mode if it continues pushing RELOOP button for about 10 seconds in the state of usual use.
I change version indication with a Cross button for left/right, and it is indication better.



Cross button for left/right

Service mode screen



DVJ-1000 SERVICE MODE **02**

MAIN : Ver 1.00
PLAYER : Ver 1.00
SUB : Ver 1.00

region number

I display a version of each software

In addition, I am replaced by an error history indication mode when I push a JOGMODE button with a version confirmation indication mode.

Error history indication displays it to 16, and the first increases with the latest error.
I change indication with a Cross button for left/right, and it is indication better.

```
ERROR CHECK MODE
1  E-7202   DVD   9  E-7001   ERR
2  E-7201   CD   10  E-7002   ERR
3  E-7201   CD   11
4  E-8302   CD   12
5  E-8302   CD   13
6  E-8302   CD   14
7  E-8302   CD   15
8  E-9101   ERR  16
                        PAGE:01/03
```

↓ ↑

```
Version Information

MAIN      :  1.00
FPGA      :  0018
TI DSP    :  0133
MOT DSP   :  1000



                        PAGE:02/03
```

↓ ↑

```
DEVICE ERROR
FPGA-LOAD :     TI           :
MOTROLA   :     TE4300       :
VDEC      :     VDEC-SDRAM   :
FPGA      :     FPGA-SDRAM   :
AV1       :     GSL-SDRAM    :
GSL       :
ATAPI     :
CPRM      :

                        PAGE:03/03
```

■ I finish a mode and return to an original state when I push a RELOOP button again.
In addition, in an error history indication mode, I push a JOG MODE button and push a RELOOP button after having done it with a version indication mode.

■ List of error codes

Error code	Description	
E-7001	ATAPI system (drive) error	Unrecoverable: Turn the power off. The error indication will be cleared.
E-7002	FPGA device error	
E-7004	FPGA memory error	
E-7007	AV1 device error	
E-7008	GSL device error	
E-7009	GSL memory error	
E-7010	TI DSP device error	
E-7011	MOT DSP device error	
E-7012	TE4300 device error	
E-7015	FPGA download error	
E-7016	"No ID number" input error	
E-7101	An exception interrupt outbreak error	
E-7201	TOC not readable	The error indication is cleared when the EJECT process is finished.
E-7202	Incompatible region number	
E-8301	Error during startup	The error indication is cleared when the EJECT process is finished or startup is completed successfully.
E-8302	Error during playback	
E-8303	Other error of the player section	
E-8305	Data format error	
E-8304	MP3 decode error	I display it in search for two seconds. Reproduction is continued by possible data.
E-8709	Communication error	Communication of the microcomputer of a display part and the microcomputer of a system part cannot be performed.
E-9101	Error in mechanical operations	Unrecoverable: Turn the power off. The error indication will be cleared.

* Notes: E-7016,E-8304,E-8305 and E-8709 are not memorized by the internal memory.

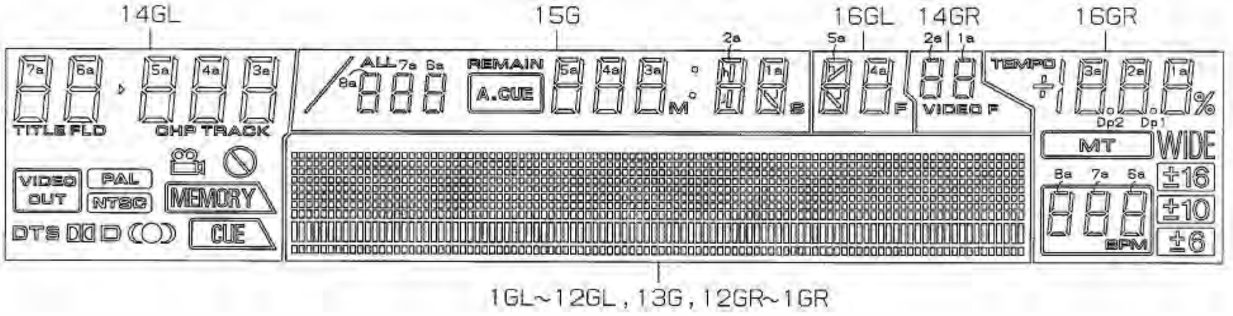
3.Check mode of a button and a display function of a display microcomputer

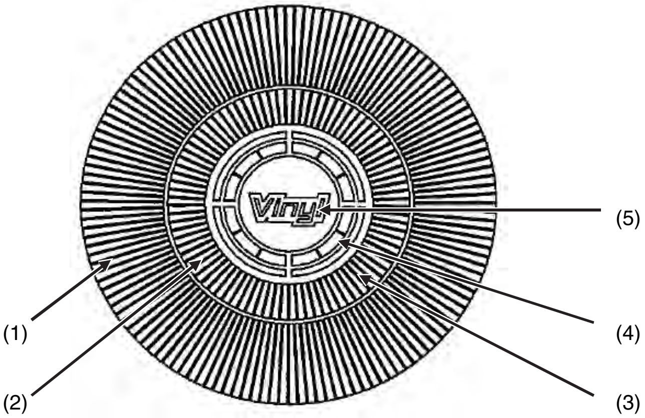
It will become this mode if a power supply is switched on pushing the TEMPO button and the MEMORY button together.
If it goes into this mode, it will be displayed on FL dot-matrix part as "SERVICE. MODE"
In this mode, it can check whether a display microcomputer can be made to turn on partially as follows, the input of each button and volume is normal, and a display is normal. In addition, a display is turned on only while pushing the button.

If a TOUCH/BRAKE knob is made into states other than the maximum (right-hand side), it will become the check mode of button and display function, and if each button is pushed, FL display and LED which correspond light up. Moreover, if a knob is made into the maximum (right-hand side) and arbitrary buttons are pushed once, it will become JOG load check mode and top speed and time will be displayed on a FL dot-matrix part.(After-mentioned)

Button, Switch	FL Dot-matrix display	FL Other display	Light up LED
PLAY/PAUSE	PLAY		PLAY/PAUSE
CUE	CUE		CUE
IN/REALTIME CUE	IN		IN/REALTIME CUE
OUT/OUT ADJAST	OUT		OUT/OUT ADJAST
RELOOP/EXIT	RELOOP		RELOOP
TITLE/FOLDER <-	TITLE/FOLDER <-	15G lights up	
TITLE/FOLDER ->	TITLE/FOLDER ->	16G lights up	
PREVIOUS (I<<)	PREVIOUS I<<	14G lights up	
NEXT (>>I)	NEXT >>I	(1)	
REV (<<)	REV <<	(2)	
FWD (>>)	FWD >>	(3)	
HOT CUE (A)	HOT CUE A		HOT CUE(A), (B), (C) red
HOT CUE (B)	HOT CUE B		HOT CUE(A), (B), (C) green
HOT CUE (C)	HOT CUE C		HOT CUE(A), (B), (C) orange
REC MODE	REC MODE		
4-BEAT LOOP	4-BEAT LOOP		
LOOP MODE	LOOP MODE		LOOP MODE
JOG MODE	JOG MODE	(5)	VINYL
TEMPO	TEMPO		TEMPO ±6%, ±10%, ±16%, WIDE
MASTER TEMPO	MASTER TEMPO		MASTER TEMPO
TEMPO RESET	TEMPO RESET		TEMPO RESET
HYPERJOG MODE	HYPER JOG		HYPER JOG
TIME MODE/AUTO CUE	TIME/ACUE		
TEXT/WAVE	TEXT/WAVE		
DELETE	All FL lights up	All FL lights up	All LED lights up * Notes
MEMORY	MEMORY		CARD
EJECT	EJECT		
STOP	STOP		
CUE/LOOP CALL	CALL		
ENTER	ENTER		
Cross button for left (←)	LEFT	Direction is also displayed in Area (1)	
Cross button for up (↑)	UP	Direction is also displayed in Area (1)	
Cross button for right (→)	RIGHT	Direction is also displayed in Area (1)	
Cross button for down (↓)	DOWN	Direction is also displayed in Area (1)	
AUDIO	AUDIO		
SUB.TITLE	SUB.TITLE		
ANGLE	ANGLE		
DISPLAY	DISPLAY		
MENU	MENU		
T.MENU	T.MENU		
SETUP	SETUP		
RETUEN	RET.		
TOUCH SENSOR	TOUCH SW	(4)	
JOG rotating to FWD	JOG >		
JOG rotating to REV	< JOG		
DIRECTION REVERSE			REV
EJECT LOCK	lock		

* Notes: As for HOT CUE button LED, as for HOT CUE(A), red, HOT CUE(B) are green, and LED of an orange turns on HOT CUE(C)


14GL
15G
16GL
14GR
16GR
7a 6a 5a 4a 3a
TITLE FLD
CHP TRACK
ALL 7a 6a
8a
REMAIN
A.CUE
5a 4a 3a
M
2a
1a
S
5a 4a
F
2a 1a
VIDEO F
TEMPO
3a 2a 1a
%
Dp2 Dp1
MT
WIDE
±16
±10
±6
8a 7a 6a
BPM
VIDEO OUT
PAL
NTSC
MEMORY
CUE
DTS
1GL~12GL , 13G , 12GR~1GR


Vinyl
(5)
(1)
(4)
(2)
(3)

A display of TOUCH/BRAKE , RELEASE/START and TEMPO slider reading value

In the following area of FL indication department, a bar displays TOUCH/BRAKE, RELEASE/START, a TEMPO slider reading value.

MEMORY Area : If a TOUCH/BRAKE knob is turned to the right, the number of display dots will increase.
CUE Area : If a RELEASE/START knob is turned to the right, the number of display dots will increase.
Playing Address: If TEMPO slider knob is moved to + side, the number of display dots will increase.

4.Check mode of operation with a player microcomputer simple substance

If a power supply is switched on pushing the TIME MODE button and the HOT CUE[A] button together, it will become "player operation mode."
This mode consists of "player operation mode" and "test operation mode."

<Player oparation mode>
Basic operation of servo, such as setup, play, pause, and track search, is carried out.
Moreover, measurement of an error rate can also be performed.

<Test opalation mode>
Servo operation is finely controllable gradually.

* It becomes player operation mode and shifts to test operation mode by the key input in the beginning.
* The command treated here is for mainly testing a mechanism and a servo system, and is not for DJ functions, such as scan and tempo.

Function	The button of the main body
<Player oparation mode>	
Play(trace) / Pause	PLAY/PAUSE
Track Search F/R	TRACK SEARCH F/R
Error Rate Count	CUE
Eject	EJECT
Mode Change	MASTER TEMPO
<Test opalation mode>	
Servo All Off	TIME
CD Select	MENU
DVD Select	T.MENU
Focus Jump Up	RETUEN
Focus Jump Down	SETUP
Slider Move Fwd	SEARCH FWD
Slider Move Rev	SEARCH REV
Step Command	TITLE/FOLDER ←/→
Mode Change	MASTER TEMPO

○ **Player operation mode command**

Play(trace) / Pause
If it is in a stop state, it will set up and play.
Moreover, if it is in a play state, whenever it will push a button, a pause and a play are carried out by turns.
I display time and the truck number that now I am tracing in FL in the case of a CD.
I display stratum number and the physical address that now I am tracing in FL in the case of DVD.

* In this mode, even if it inserts a disc, an automatic setup is not carried out.
Moreover, a play is not carrying out audio reproduction, but is tracing the signal side of a disc.
At a play, a disc is traced by 4000rpm/CAV rotation. A sound is not outputted.
I trace it in 4000rpmCAV in the case of a CD in 5000rpmCAV in the case of DVD by a play.

Track Search F/R
I search a track displayed by a FWD / REV course and, in the case of a CD, do pause.
A track search is not possible in CD-ROM (MP3).

* Whenever a FWD / REV direction is pushed, I search it with the following turn and, in the case of DVD, do a pause.

When a FWD direction was pushed with DUAL LAYER DISC.
→L0 inside→L0 middle→L0 outside→L1 inside→L1 middle→L1 outside→

When a REV direction was pushed with DUAL LAYER DISC.
←L0 inside←L0 middle←L0 outside←L1 inside←L1 middle←L1 outside←

When a FWD direction was pushed with SINGLE LAYER DISC.
→L0 inside→L0 middle→L0 outside→

When a REV direction was pushed with SINGLE LAYER DISC.
←L0 inside←L0 middle←L0 outside←

* A search address is different in inside/middle/outside at total capacity of a Disc in a relative address.
* Outside searches about 20000 sector I than a Disc most circumference address.

Error Rate Count

I measure an error rate of 10000 blocks in the case of DVD in the case of a CD from a present position doing a play / pause for about 20 seconds and display a measurement result in FL.
Usually, a track to measure is made to search and this button is inputted from a pause state.
For example, it is displayed as "3.56E-4 O.K." etc.
If an error rate is less than 3.00E-3, it will be displayed as O.K. If an error rate is larger than 3.00E-3, it will be displayed as NG.
Measurement with the managed disc at the time of factory shipments is a premise.
The product does not judge whether they are inferior goods at the time of service.

Eject

A disc is ejected.

Mode Change

If the MASTER TEMPO button is pushed into player operation mode, MASTER TEMPO LED will light up, and it will shift to the below-mentioned "test operation mode."
Indication of FL display increases with "All Servo Off" indication.

○ **Test operation mode command**

Serbo operation is finely controllable gradually.
Keep in mind a test operation mode command that it may give a damage to a player as mistaking the usage.
Please operate this mode after making a disc a set completion state.

Servo All Off

When serbo is ON, all serbo will be turned off if the TIME button is pushed.
I display it with "All Servo Off" in FL display for time that I push indication, and shine or one second (long one).

CD Select

When I start a CD, I push a MENU button and choose a CD.
I display it with "CD Select" in FL for time that I push indication, and shine or one second (long one).

DVD Select

When I start DVD, I push a T.MENU button and choose DVD.
I display it with "DVD Select" in FL for time that I push indication, and shine or one second (long one).

Focus Jump Up

In DVD choice, a focus jumps in L1.
I display it with "Focus Jump Up" in FL for time that I push indication, and shine or one second (long one).

Focus Jump Down

In DVD choice, a focus jumps in L0.
I display it with "Focus Jump Down" in FL for time that I push indication, and shine or one second (long one).

Slider Move Fwd

I send about 1.8mm sliders to a circumference direction whenever I push a SEARCH FWD button.
While I shine or I push a button display it with "Slider Move Fwd" in FL indication for one second.

Slider Move Rev

I send about 1.8mm sliders to an internal circumference direction whenever I push a SEARCH REV button.
While I shine or I push a button display it with "Slider Move Rev" in FL indication for one second.

Step Command

I let I stand and carry out serial movement of advance by a step.
I step when I push step up, a FOLDER SEARCH REV button when I push a FOLDER SEARCH FWD button and am downed.

step		FL display
STEP0:	Servo all off	All Servo Off
STEP1:	Laser diode on	STEP1 LD
STEP2:	Disc presence judgment	STEP2 D.SENSE
STEP3:	Spindle on (2000rpm)	STEP3 SPDL
STEP4:	Disc search	STEP4 D.SRCH
STEP5:	Focus serve on	STEP5 FCS
STEP6:	Focus position fault adjustment	STEP6 F.POS
STEP7:	Tracking balance adjust	STEP7 T.BAL
STEP8:	Tracking servo on	STEP8 TRK
STEP9:	Focus position adjust	STEP9 F.POS
STEP10:	Focus gain adjust	STEP10 F.GAIN
STEP11:	Tracking gain adjust	STEP11 T.GAIN
STEP12:	Address lead start	STEP12 READ

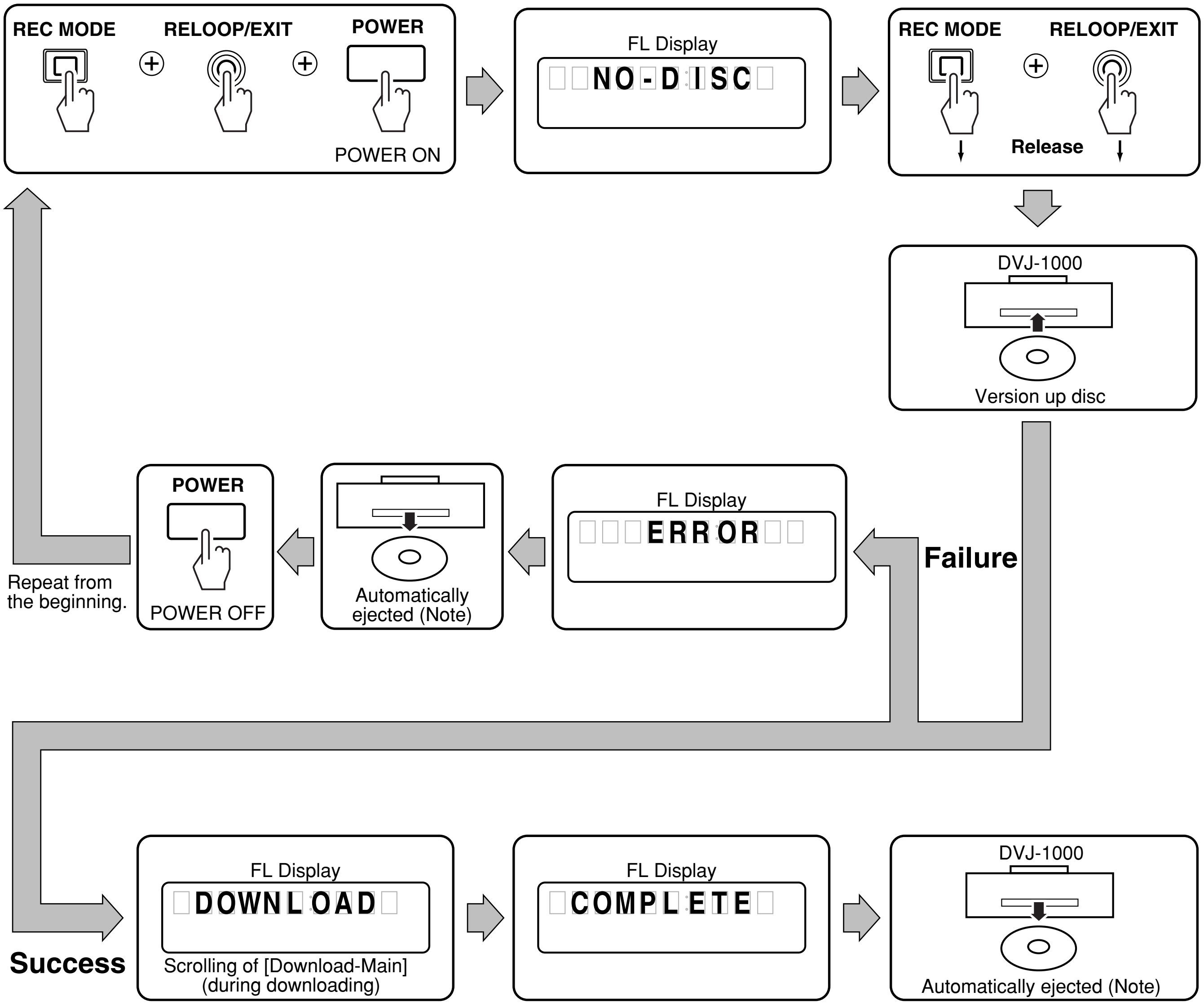
FOLDER SEARCH FWD button: step up
FOLDER SEARCH REV button : step down

* All Servo Off indication displays indication, indication of STEP1 to 12 for one second till I am replaced by the next mode.
* It turns in 2000rpmCAV.I do not come in turn UP more than it.

Mode Change

If the MASTER TEMPO button is pushed into "player operation mode", MASTER TEMPO LED will light up, and it will shift to the below-mentioned "test operation mode."

7.1.2 HOW TO UPGRADE THE SOFTWARE OF THE MICROCOMPUTER


REC MODE
RELOOP/EXIT
POWER
POWER ON
FL Display
NO-DISC
REC MODE
RELOOP/EXIT
Release
DVJ-1000
Version up disc
Failure
FL Display
ERROR
Automatically ejected (Note)
POWER
POWER OFF
Repeat from the beginning.
Success
FL Display
DOWNLOAD
Scrolling of [Download-Main] (during downloading)
FL Display
COMPLETE
DVJ-1000
Automatically ejected (Note)

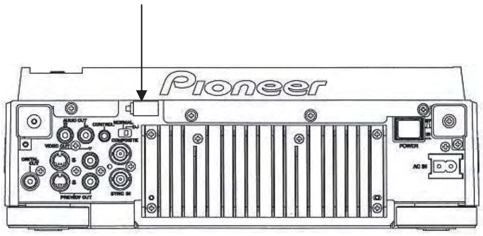
Note:

- Do NOT turn off the power after the upgrade disc is loaded till it is automatically ejected. If you do, the unit may not operate properly afterward.
- Eject a disc automatically even if updating fails.

7.1.3 HOW TO INPUT THE ID NUMBER

• Serial control terminals

- The serial control terminals for checking the ID number and the player section are provided inside the rear panel (hidden inside the slit of the rear panel) and inside the main unit (6-pin connector).
- To use the terminals, a PC with an RS-232C serial communications terminal, and either of the special device for level conversion dedicated for the hidden connector inside the slit of the rear panel or a special device for downloading, such as the CDJ-1000, are required.
- Do NOT use both terminals simultaneously, because these terminals are internally connected.



The terminal is hidden inside the slit.

5. How to input the ID number

- If error E-7016 is displayed, the ID number must be input.
- For inputting the ID number, a PC with an RS-232C serial communications terminal, the special device for level conversion, and the ID data disc (a special DVD disc on which the ID-number data) are written are required.
  This product is a player, please use a disc for recorder as ID data disc.

Procedures

① Check the ID number. The 9-digit ID number is written on the label attached to the bottom of the main unit.

② Connect the RS-232C terminal of the PC and the DVJ-1000 using a special device. (See next page.)
  - When the special device for level conversion is used, connect the card cable to the hidden connector inside the slit of the rear panel.
  - When a special device for downloading, such as the CDJ-1000, is used, open the bottom plate of the main unit to reveal the 6-pin connector and connect the special device with the connector.
  - As the hidden connector inside the rear panel and the 6-pin connector inside the main unit are internally connected, they must not be connected simultaneously.

③ Start up the terminal software of the PC (communication speed: 4800 bps, parity: none, STOP bit: 1).

④ Turn on the power of the DVJ-1000.
If error E-7016 is displayed, press the ENTER key on the PC several times to confirm that "R" is returned.

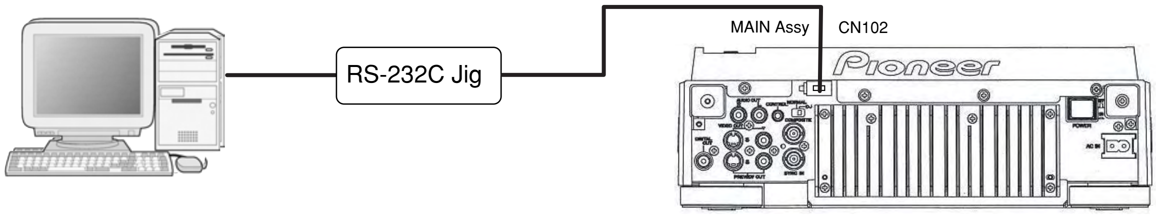
⑤ Enter "2MC" to confirm that "R" is returned. Use uppercase alphabetics for input.

⑥ Enter the 9-digit ID code then "ID" and confirm that "R" is returned.
If you type a wrong number for the ID code, type the correct numbers again.

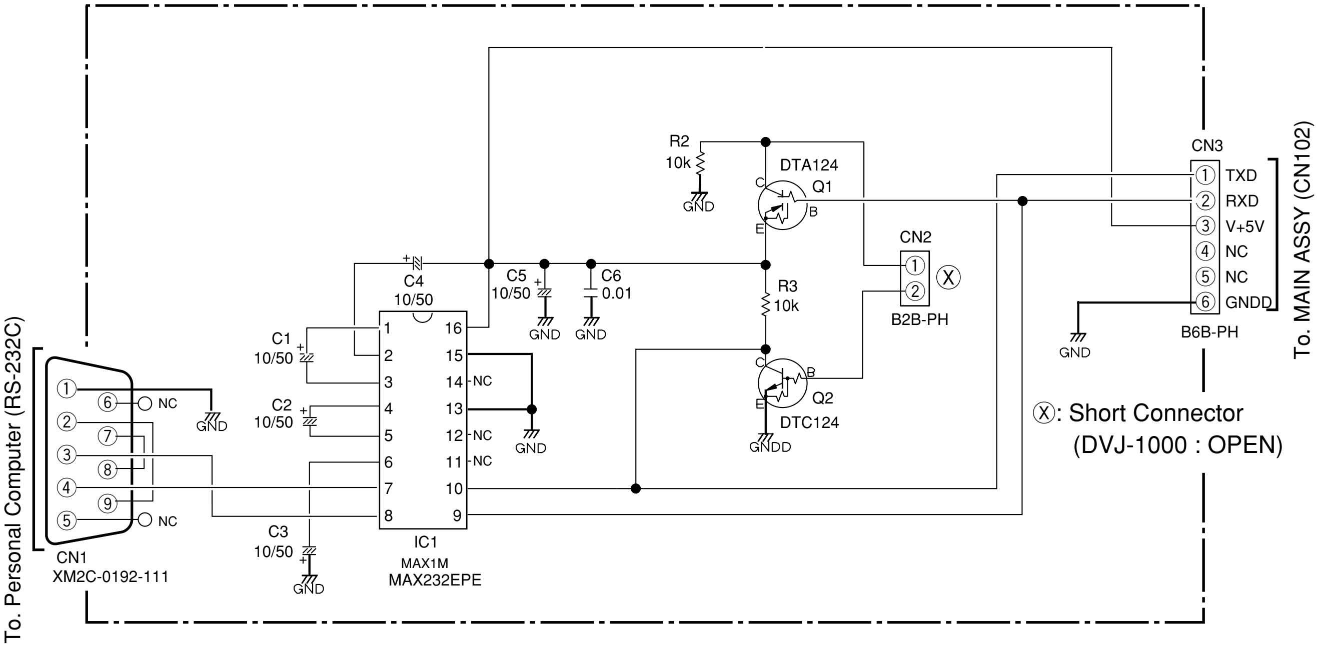
⑦ Load the special DVD disc into the player.
Wait for the unit to start up then enter "IW" to confirm that "R" is returned.
If error E11 is returned, wait a few moments then enter "IW" again.

⑧ Enter "EJ" to eject the disc, then turn off the power of the DVJ-1000 then turn it back on. Check that no error is displayed.

[Connection Diagram]


RS-232C Jig
MAIN Assy
CN102
Pioneer

■ RS-232C Jig Schematic Diagram


To. Personal Computer (RS-232C)
CN1
XM2C-0192-111
①
②
③
④
⑤
⑥
⑦
⑧
⑨
NC
NC
GND
C1
10/50
C2
10/50
C3
10/50
GND
C4
10/50
IC1
MAX1M
MAX232EPE
1
2
3
4
5
6
7
8
16
15
14
13
12
11
10
9
NC
NC
NC
GND
C5
10/50
C6
0.01
GND
GND
R2
10k
GND
DTA124
Q1
C
E
B
R3
10k
C
E
B
Q2
DTC124
GNDD
CN2
①
②
Ⓧ
B2B-PH
CN3
① TXD
② RXD
③ V+5V
④ NC
⑤ NC
⑥ GNDD
B6B-PH
GND
To. MAIN ASSY (CN102)

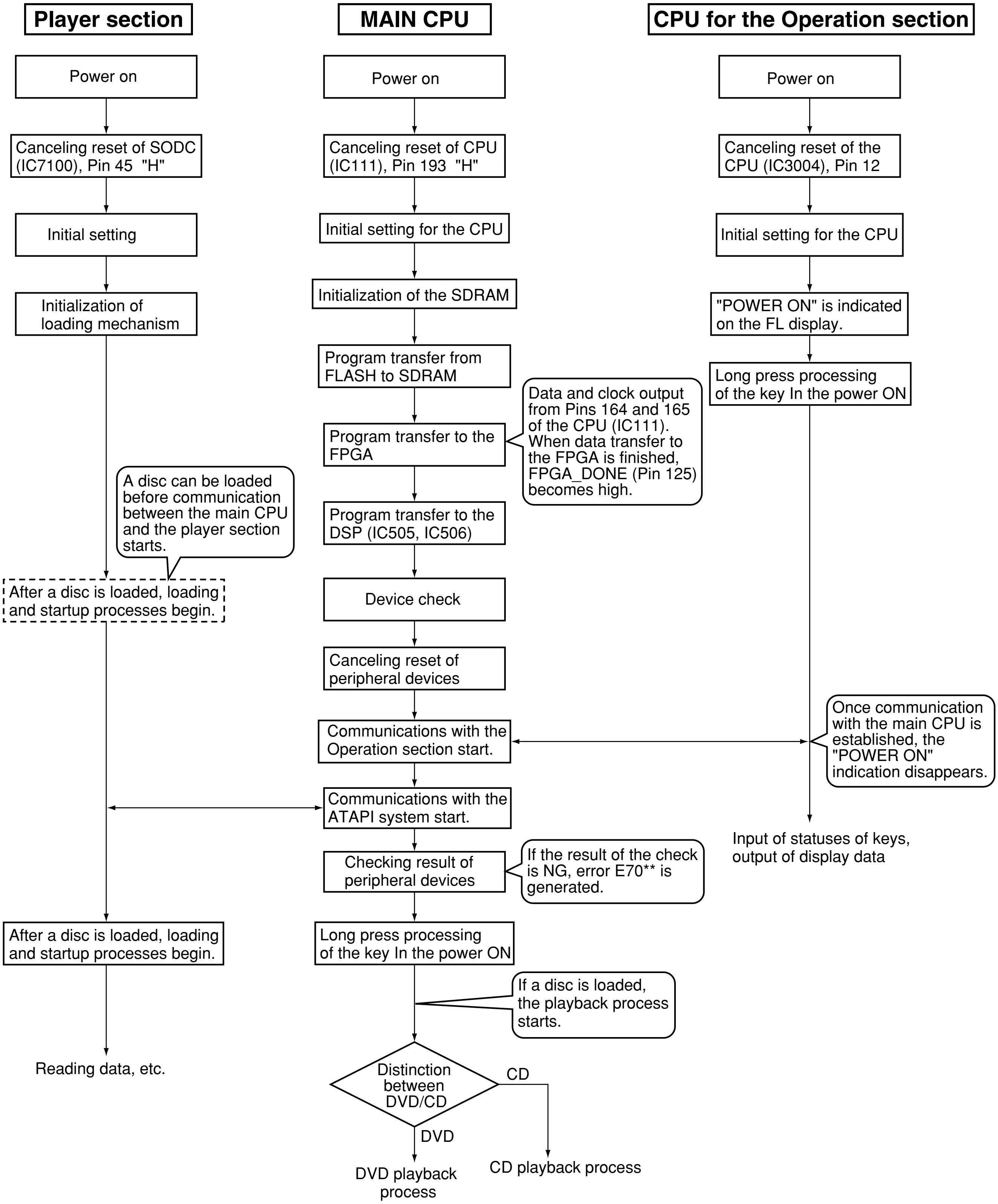
Ⓧ: Short Connector
(DVJ-1000 : OPEN)

Parts List

SEMICONDUCTORS	
IC1	: MAX232EPE
Q1	: DTA124
Q2	: DTC124
CAPACITORS	
C1, C2, C3, C4, C5	: CEAT100M50
C6	: CKSQYF103Z50
RESISTORS	
R2, R3	: RD1/2VM103J
OTHERS	
CN1 (RS-232C CONNECTOR)	: XM2C-0192-111
CN2 (2P CONNECTOR)	: B2B-PH
CN3 (6P CONNECTOR)	: B6B-PH

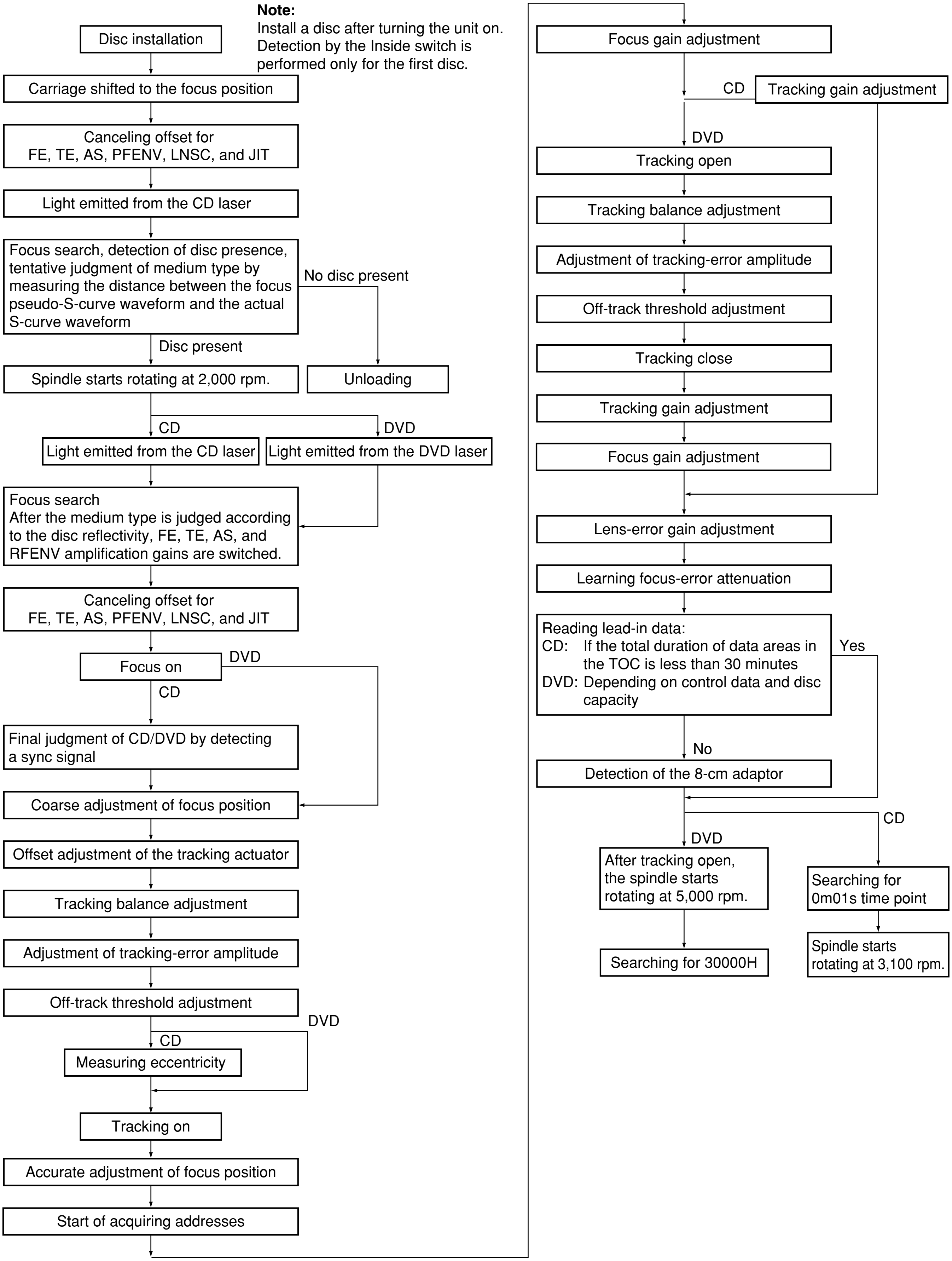
7.1.4 SEQUENCE

Power On Sequence


Player section
MAIN CPU
CPU for the Operation section
Power on
Canceling reset of SODC (IC7100), Pin 45 "H"
Initial setting
Initialization of loading mechanism
A disc can be loaded before communication between the main CPU and the player section starts.
After a disc is loaded, loading and startup processes begin.
After a disc is loaded, loading and startup processes begin.
Reading data, etc.
Power on
Canceling reset of CPU (IC111), Pin 193 "H"
Initial setting for the CPU
Initialization of the SDRAM
Program transfer from FLASH to SDRAM
Program transfer to the FPGA
Data and clock output from Pins 164 and 165 of the CPU (IC111). When data transfer to the FPGA is finished, FPGA_DONE (Pin 125) becomes high.
Program transfer to the DSP (IC505, IC506)
Device check
Canceling reset of peripheral devices
Communications with the Operation section start.
Communications with the ATAPI system start.
Checking result of peripheral devices
If the result of the check is NG, error E70** is generated.
Long press processing of the key In the power ON
If a disc is loaded, the playback process starts.
Distinction between DVD/CD
CD
DVD
DVD playback process
CD playback process
Power on
Canceling reset of the CPU (IC3004), Pin 12
Initial setting for the CPU
"POWER ON" is indicated on the FL display.
Long press processing of the key In the power ON
Once communication with the main CPU is established, the "POWER ON" indication disappears.
Input of statuses of keys, output of display data

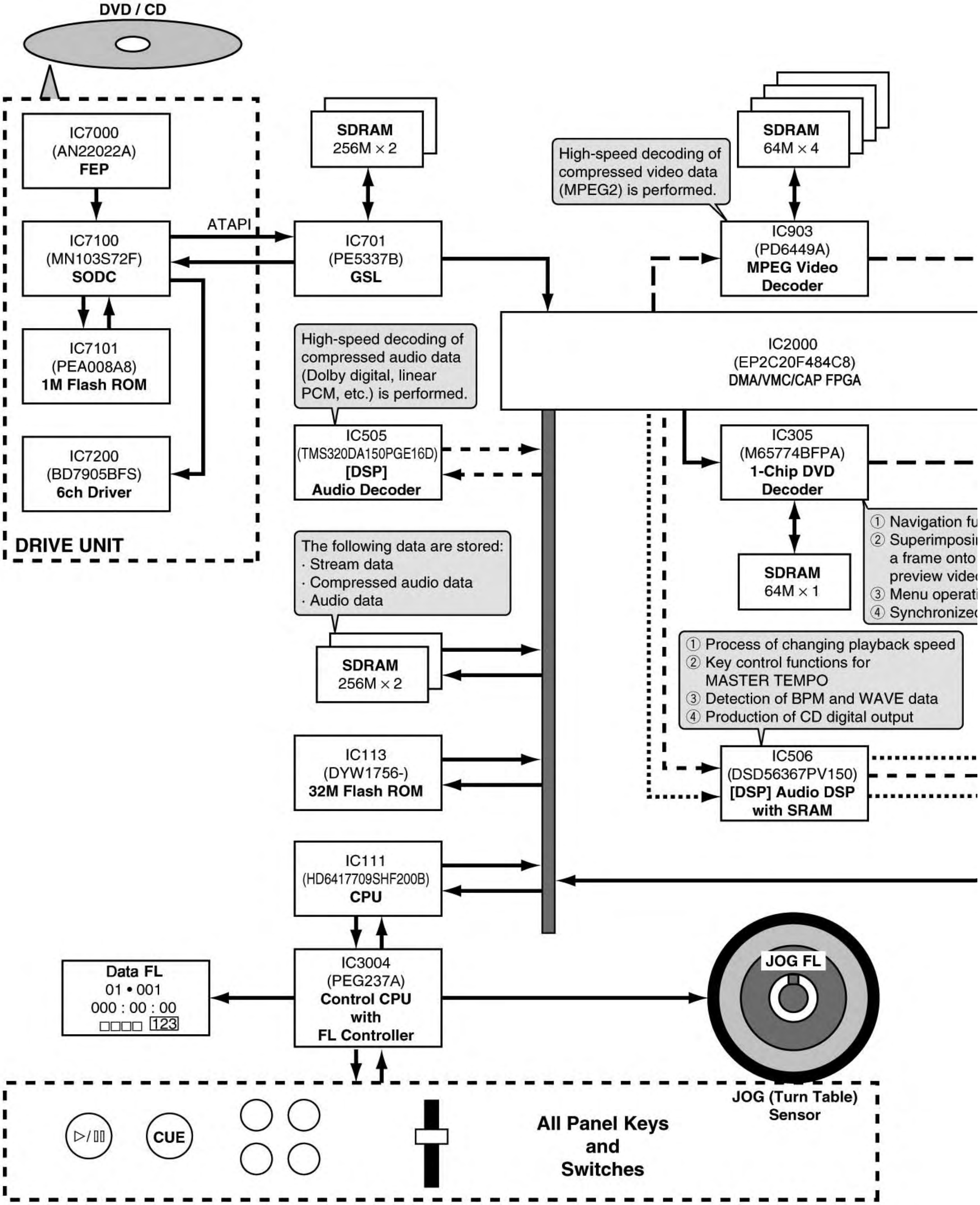
Sequence of Starting Up the Drive

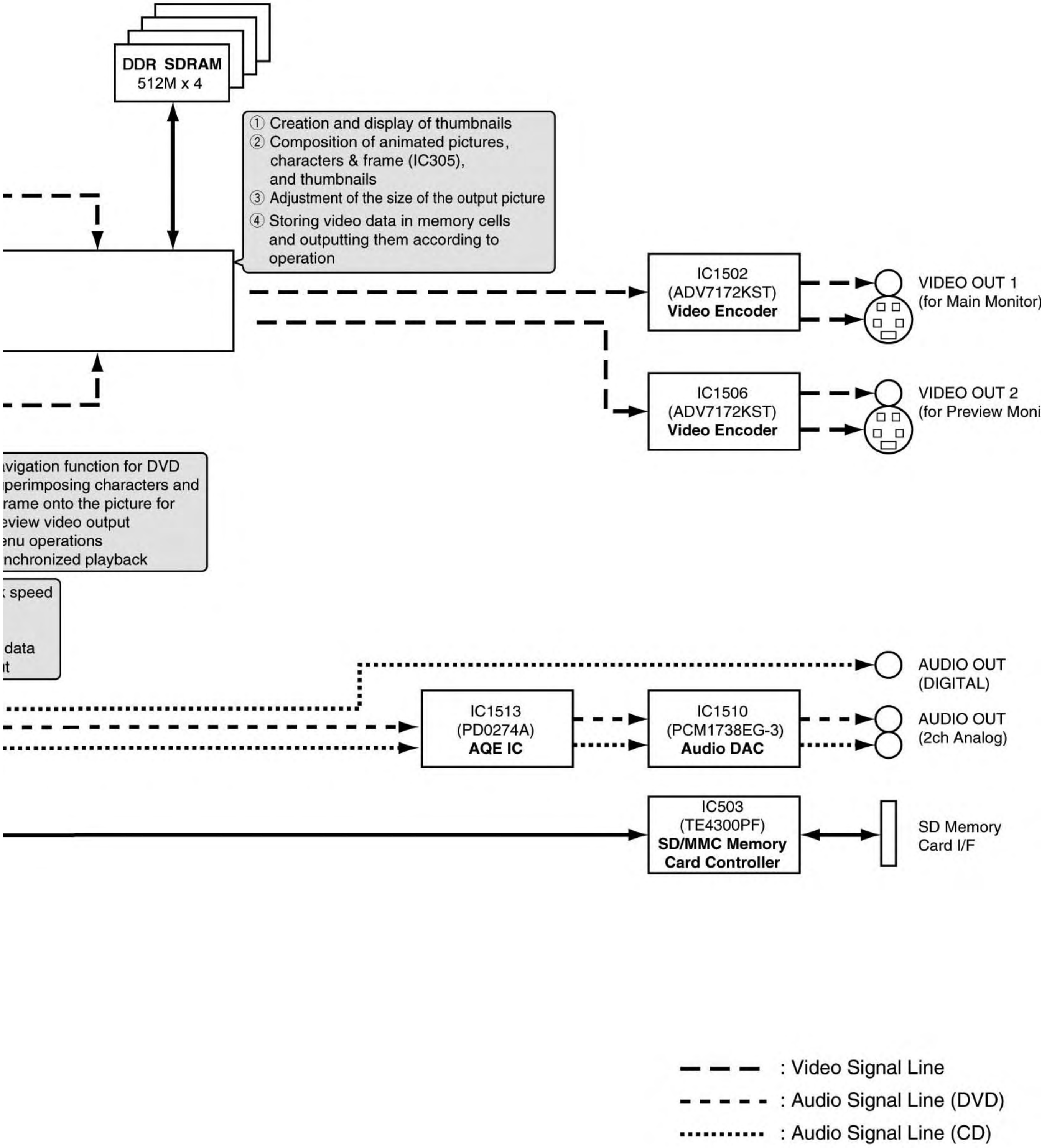
Note:
Install a disc after turning the unit on.
Detection by the Inside switch is performed only for the first disc.


Disc installation
Carriage shifted to the focus position
Canceling offset for FE, TE, AS, PFENV, LNSC, and JIT
Light emitted from the CD laser
Focus search, detection of disc presence, tentative judgment of medium type by measuring the distance between the focus pseudo-S-curve waveform and the actual S-curve waveform
No disc present
Disc present
Spindle starts rotating at 2,000 rpm.
Unloading
CD
DVD
Light emitted from the CD laser
Light emitted from the DVD laser
Focus search After the medium type is judged according to the disc reflectivity, FE, TE, AS, and RFENV amplification gains are switched.
Canceling offset for FE, TE, AS, PFENV, LNSC, and JIT
Focus on
DVD
CD
Final judgment of CD/DVD by detecting a sync signal
Coarse adjustment of focus position
Offset adjustment of the tracking actuator
Tracking balance adjustment
Adjustment of tracking-error amplitude
Off-track threshold adjustment
DVD
CD
Measuring eccentricity
Tracking on
Accurate adjustment of focus position
Start of acquiring addresses
Focus gain adjustment
CD
Tracking gain adjustment
DVD
Tracking open
Tracking balance adjustment
Adjustment of tracking-error amplitude
Off-track threshold adjustment
Tracking close
Tracking gain adjustment
Focus gain adjustment
Lens-error gain adjustment
Learning focus-error attenuation
Reading lead-in data:
CD: If the total duration of data areas in the TOC is less than 30 minutes
DVD: Depending on control data and disc capacity
Yes
No
Detection of the 8-cm adaptor
CD
DVD
After tracking open, the spindle starts rotating at 5,000 rpm.
Searching for 0m01s time point
Searching for 30000H
Spindle starts rotating at 3,100 rpm.

7.1.5 DVJ-1000 BLOCK CHART (DJ MODE)

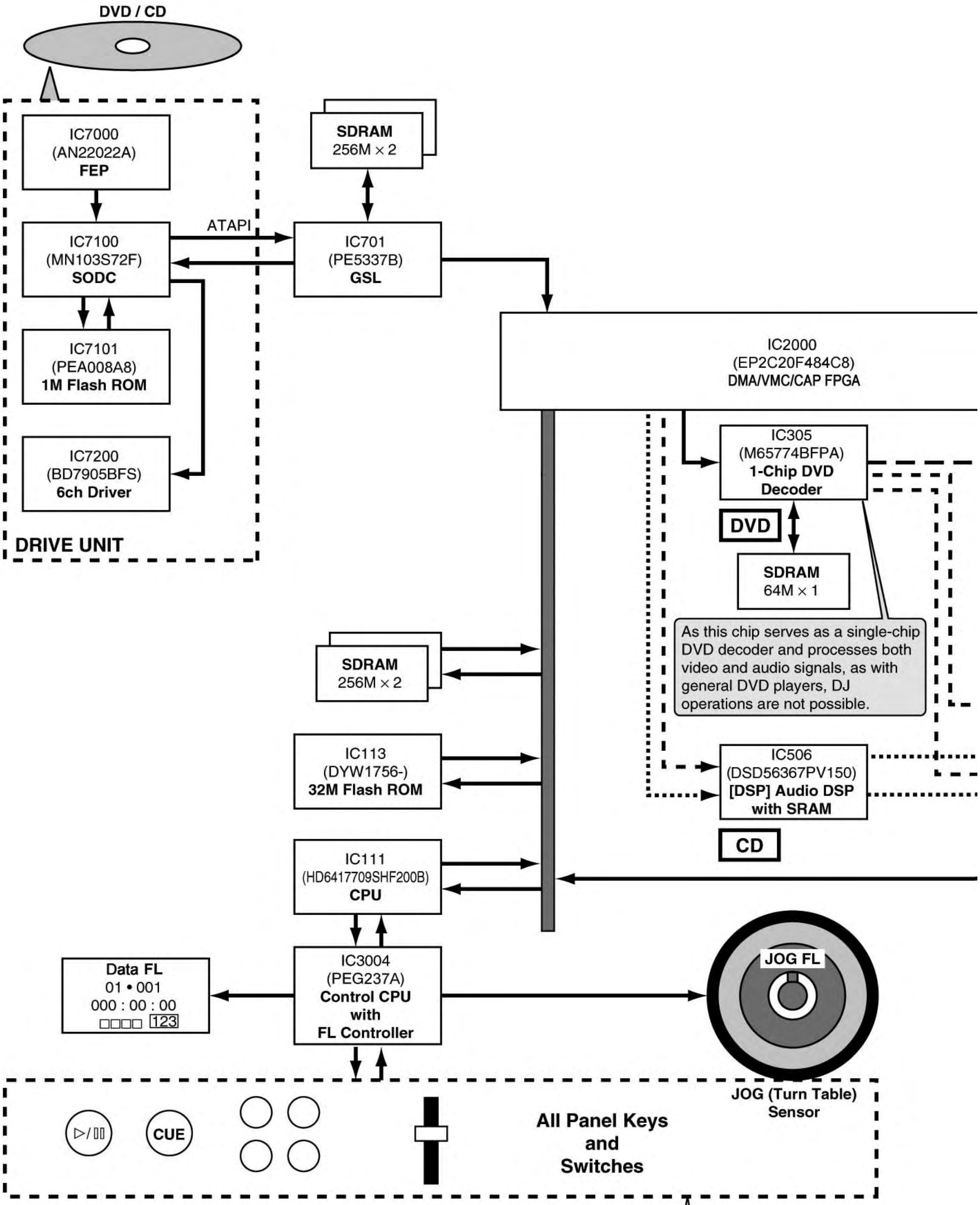
DVJ-1000 BLOCK CHART(DJ MODE)


DVD / CD
IC7000
(AN22022A)
FEP
IC7100
(MN103S72F)
SODC
ATAPI
IC7101
(PEA008A8)
1M Flash ROM
IC7200
(BD7905BFS)
6ch Driver
DRIVE UNIT
SDRAM
256M × 2
IC701
(PE5337B)
GSL
High-speed decoding of compressed audio data (Dolby digital, linear PCM, etc.) is performed.
IC505
(TMS320DA150PGE16D)
[DSP]
Audio Decoder
The following data are stored:
· Stream data
· Compressed audio data
· Audio data
SDRAM
256M × 2
IC113
(DYW1756-)
32M Flash ROM
IC111
(HD6417709SHF200B)
CPU
High-speed decoding of compressed video data (MPEG2) is performed.
SDRAM
64M × 4
IC903
(PD6449A)
MPEG Video
Decoder
IC2000
(EP2C20F484C8)
DMA/VMC/CAP FPGA
IC305
(M65774BFPA)
1-Chip DVD
Decoder
SDRAM
64M × 1
① Navigation fu
② Superimposi
a frame onto
preview vide
③ Menu operati
④ Synchronize
① Process of changing playback speed
② Key control functions for MASTER TEMPO
③ Detection of BPM and WAVE data
④ Production of CD digital output
IC506
(DSD56367PV150)
[DSP] Audio DSP
with SRAM
Data FL
01 • 001
000 : 00 : 00
□□□□ 123
IC3004
(PEG237A)
Control CPU
with
FL Controller
JOG FL
JOG (Turn Table)
Sensor
▷/▯▯
CUE
All Panel Keys
and
Switches


DDR SDRAM
512M x 4
① Creation and display of thumbnails
② Composition of animated pictures, characters & frame (IC305), and thumbnails
③ Adjustment of the size of the output picture
④ Storing video data in memory cells and outputting them according to operation
IC1502
(ADV7172KST)
Video Encoder
VIDEO OUT 1
(for Main Monitor)
IC1506
(ADV7172KST)
Video Encoder
VIDEO OUT 2
(for Preview Moni
ıvigation function for DVD
ıperimposing characters and
rame onto the picture for
eview video output
enu operations
nchronized playback
: speed
data
ıt
AUDIO OUT
(DIGITAL)
IC1513
(PD0274A)
AQE IC
IC1510
(PCM1738EG-3)
Audio DAC
AUDIO OUT
(2ch Analog)
IC503
(TE4300PF)
SD/MMC Memory
Card Controller
SD Memory
Card I/F
: Video Signal Line
: Audio Signal Line (DVD)
: Audio Signal Line (CD)

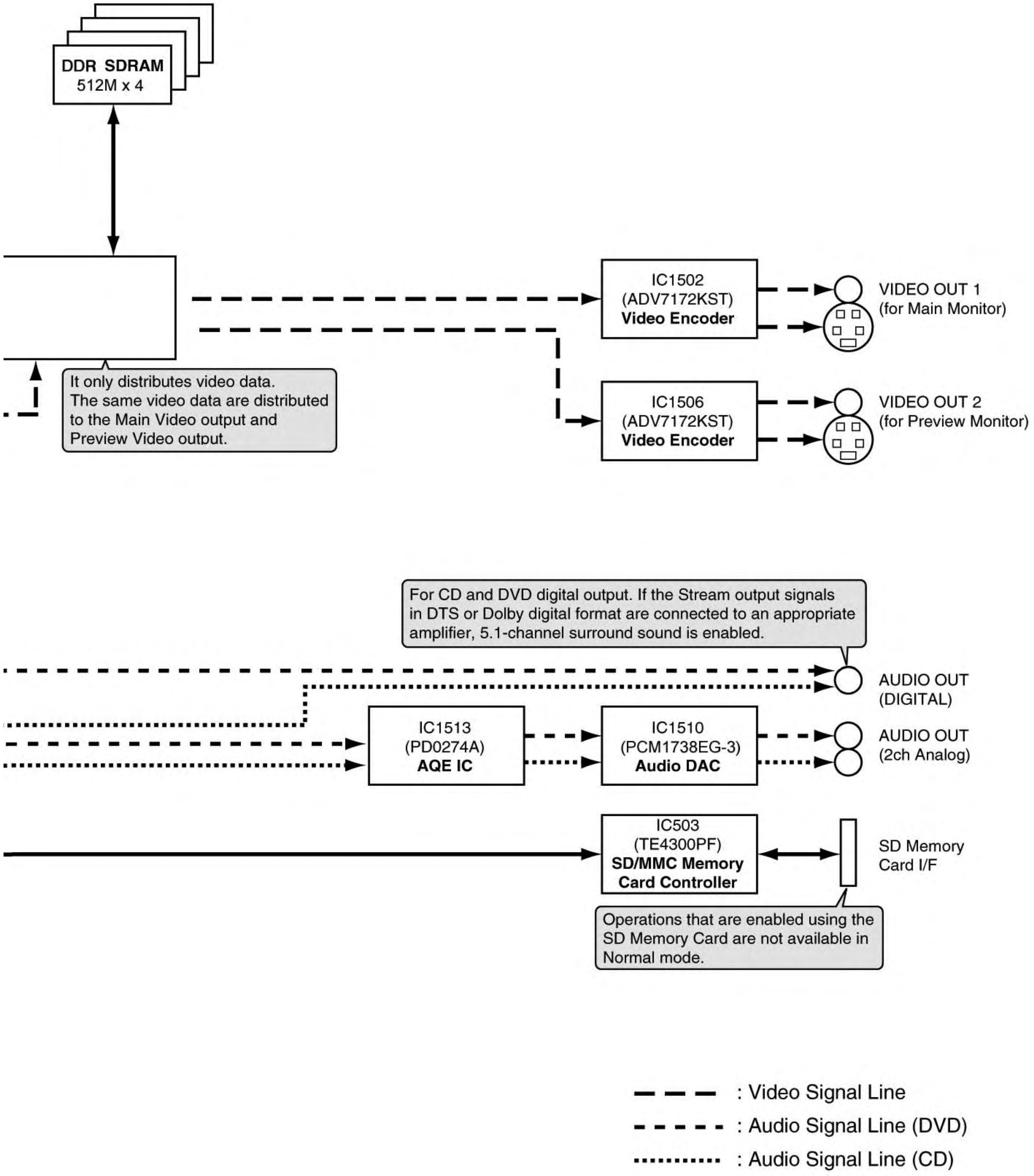
7.1.6 DVJ-1000 BLOCK CHART (NORMAL MODE)

DVJ-1000 BLOCK CHART(NORMAL MODE)


DVD / CD
IC7000
(AN22022A)
FEP
IC7100
(MN103S72F)
SODC
ATAPI
IC7101
(PEA008A8)
1M Flash ROM
IC7200
(BD7905BFS)
6ch Driver
DRIVE UNIT
SDRAM
256M × 2
IC701
(PE5337B)
GSL
IC2000
(EP2C20F484C8)
DMA/VMC/CAP FPGA
IC305
(M65774BFPA)
1-Chip DVD
Decoder
DVD
SDRAM
64M × 1
SDRAM
256M × 2
IC113
(DYW1756-)
32M Flash ROM
IC506
(DSD56367PV150)
[DSP] Audio DSP
with SRAM
CD
IC111
(HD6417709SHF200B)
CPU
IC3004
(PEG237A)
Control CPU
with
FL Controller
Data FL
01 • 001
000 : 00 : 00
123
JOG FL
JOG (Turn Table)
Sensor
CUE
All Panel Keys
and
Switches

As this chip serves as a single-chip DVD decoder and processes both video and audio signals, as with general DVD players, DJ operations are not possible.

The switches and jog dial that are operable during DJ operations are disabled.


DDR SDRAM
512M x 4
IC1502
(ADV7172KST)
Video Encoder
VIDEO OUT 1
(for Main Monitor)
IC1506
(ADV7172KST)
Video Encoder
VIDEO OUT 2
(for Preview Monitor)
AUDIO OUT
(DIGITAL)
IC1513
(PD0274A)
AQE IC
IC1510
(PCM1738EG-3)
Audio DAC
AUDIO OUT
(2ch Analog)
IC503
(TE4300PF)
SD/MMC Memory
Card Controller
SD Memory
Card I/F
: Video Signal Line
: Audio Signal Line (DVD)
: Audio Signal Line (CD)

It only distributes video data. The same video data are distributed to the Main Video output and Preview Video output.

For CD and DVD digital output. If the Stream output signals in DTS or Dolby digital format are connected to an appropriate amplifier, 5.1-channel surround sound is enabled.

Operations that are enabled using the SD Memory Card are not available in Normal mode.

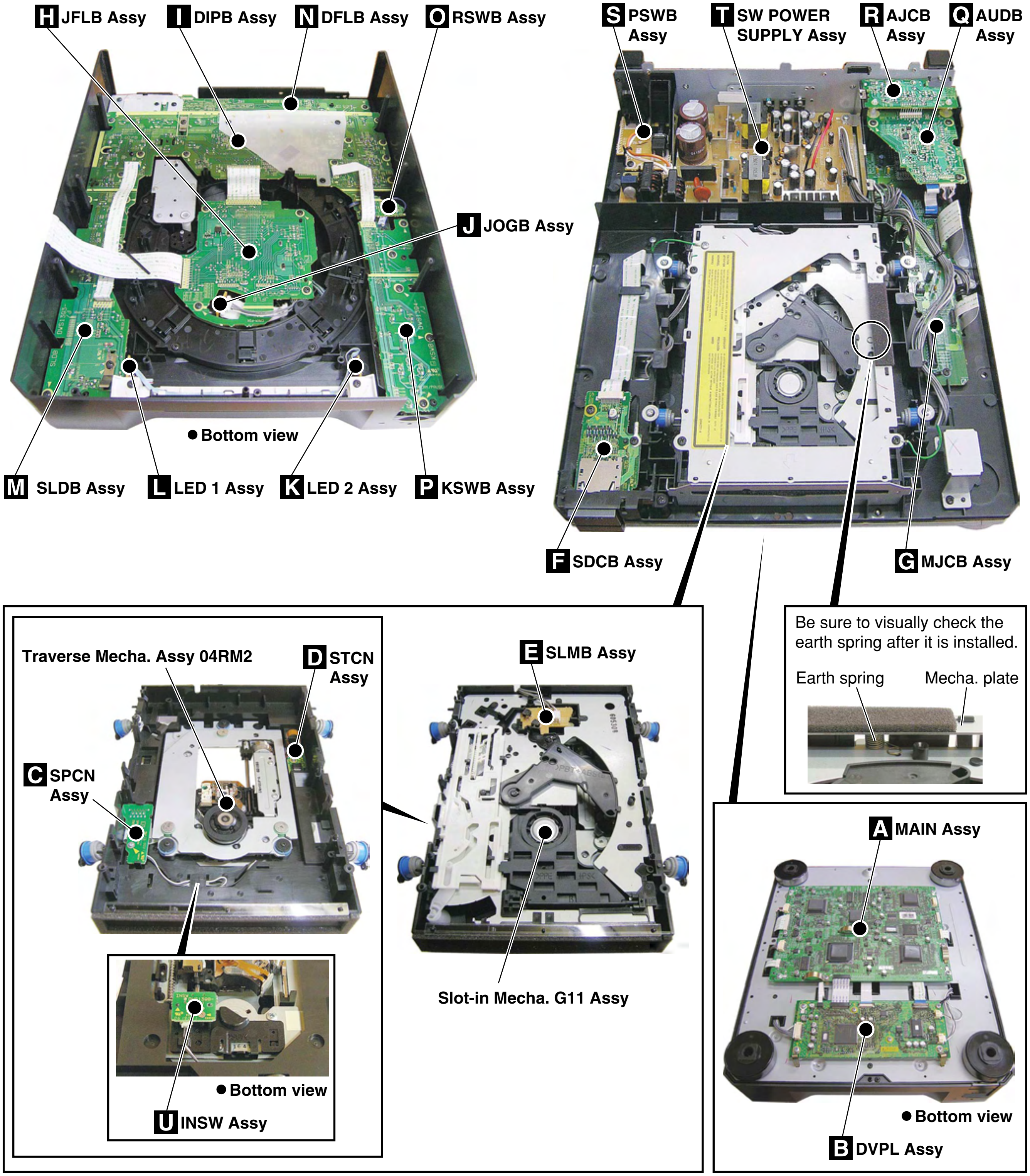
7.1.7 DISASSEMBLY

Note 1: Do NOT look directly into the pickup lens. The laser beam may cause eye injury.

Note 2: Even if the unit shown in the photos and illustrations in this manual may differ from your product, the procedures described here are common.

Note 3: The DEX1021 forced-eject pin supplied with this model is dedicated to this model only. The DEX1007 or DEX1013 forced-eject pin used in other models must not be used with this model, because it is very dangerous for the following reason: If the disc being rotated is forcibly ejected with a DEX1007 or DEX1013 forced-eject pin, the rapidly rotating disc will pop out.

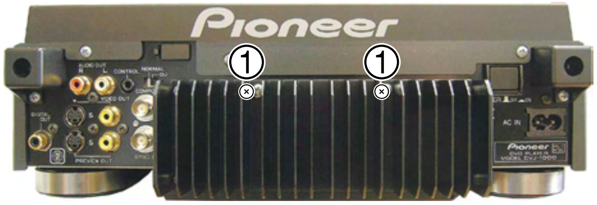
PCB Location


H JFLB Assy
I DIPB Assy
N DFLB Assy
O RSWB Assy
J JOGB Assy
● Bottom view
M SLDB Assy
L LED 1 Assy
K LED 2 Assy
P KSWB Assy
S PSWB Assy
T SW POWER SUPPLY Assy
R AJCB Assy
Q AUDB Assy
F SDCB Assy
G MJCB Assy
Traverse Mecha. Assy 04RM2
D STCN Assy
C SPCN Assy
● Bottom view
U INSW Assy
E SLMB Assy
Slot-in Mecha. G11 Assy
Earth spring
Mecha. plate
A MAIN Assy
● Bottom view
B DVPL Assy

Be sure to visually check the earth spring after it is installed.

Diagnosis of MAIN Assy

① Remove the two screws.

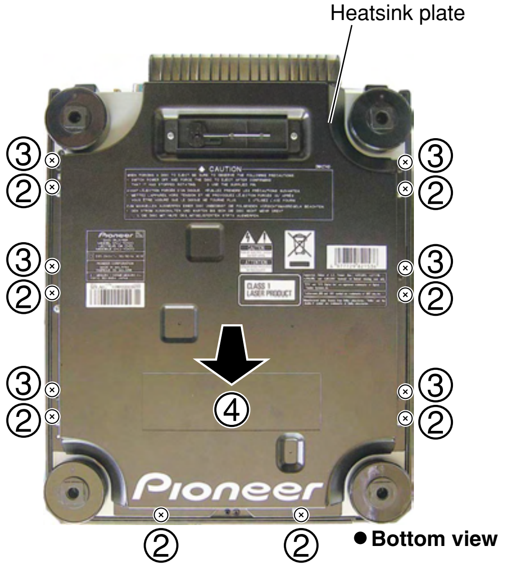


● **Rear view**

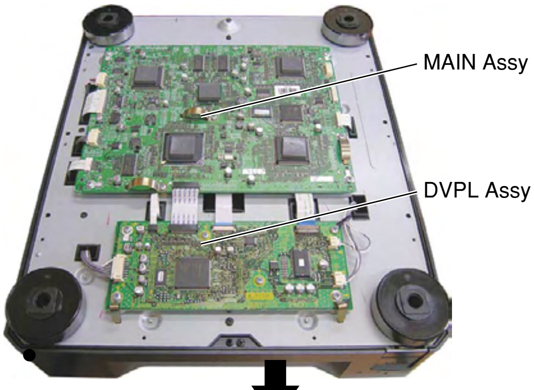
② Remove the eight screws.

③ Remove the six screws.

④ Remove the heatsink plate.


Heatsink plate
③
②
③
②
③
②
③
②
③
②
③
②
④
②
②
Pioneer

● **Bottom view**


MAIN Assy
DVPL Assy

Bottom view

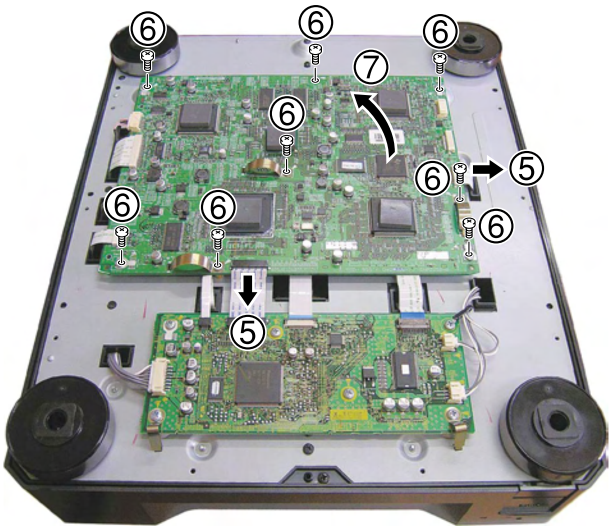
Diagnosis

- **More when measure the regulator output of IC2005 and IC902**

⑤ Disconnect the two flexible cables.

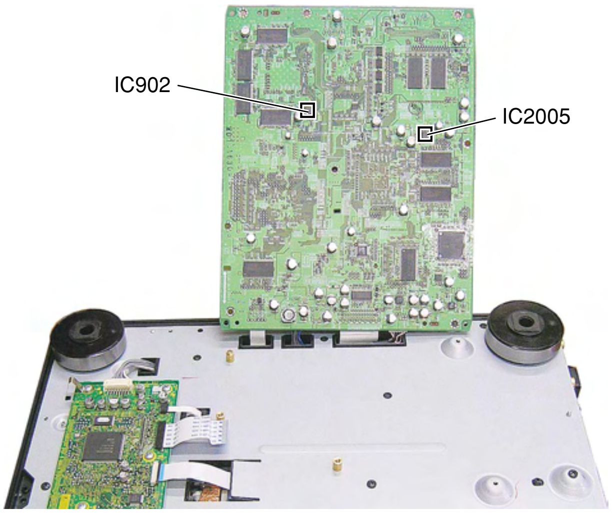
⑥ Remove the eight screws.

⑦ Stand the MAIN Assy.


⑥
⑥
⑥
⑦
⑥
⑥
⑤
⑥
⑥
⑥
⑤

● **Bottom view**

MAIN Assy (Side B)


IC902
IC2005

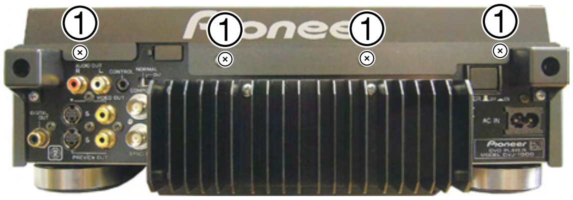
● **Bottom view**

Diagnosis

Disassembly

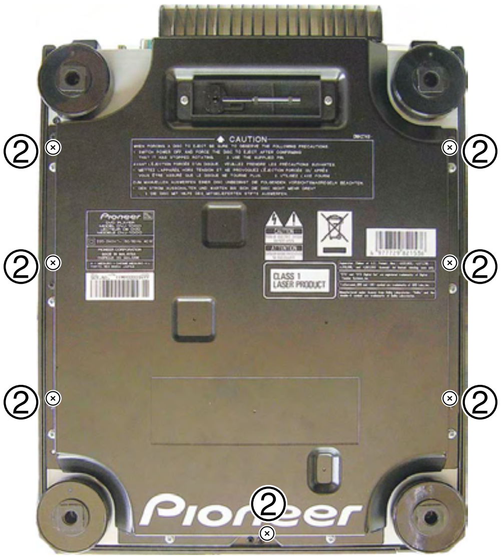
1 Control Panel Section

① Remove the four screws.



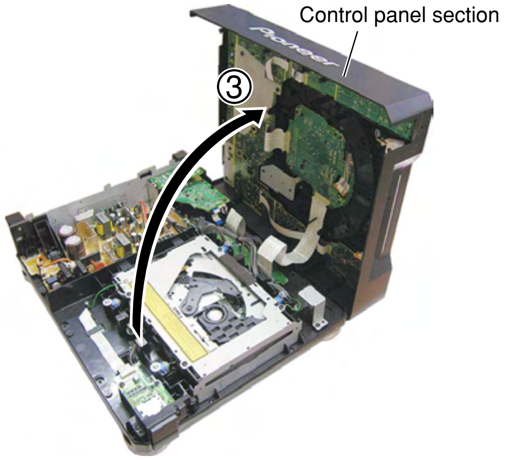
● **Rear view**

② Remove the seven screws.



● **Bottom view**

③ Remove the control panel section.


Control panel section
③

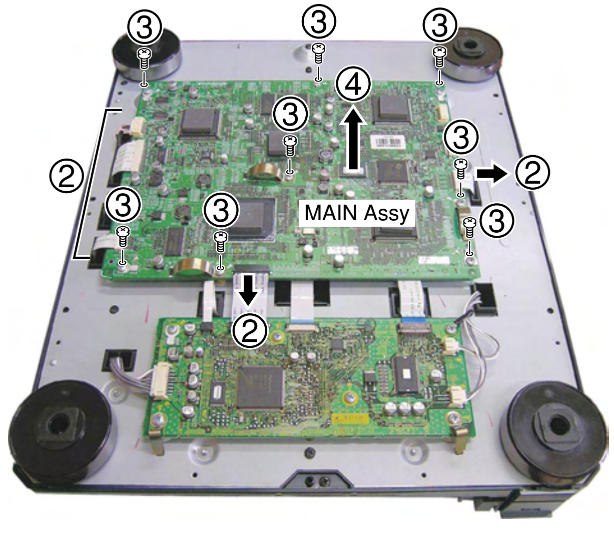
2 Slot-in Mecha. Section

❶ Remove the heatsink plate.
(See "Diagnosis of MAIN Assy".)

② Disconnect the all jumper wires from the MAIN Assy.

③ Remove the eight screws.

④ Remove the MAIN Assy.

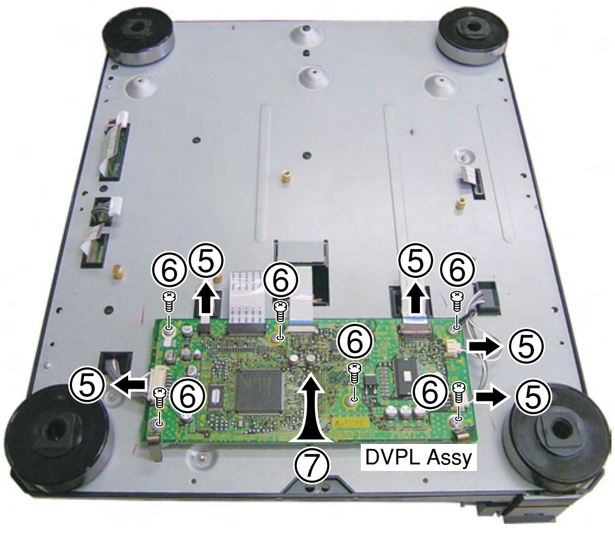

③
③
③
④
③
③
②
②
③
③
MAIN Assy
③
②

● **Bottom view**

⑤ Disconnect the two flexible cables and the three connectors.

⑥ Remove the six screws.

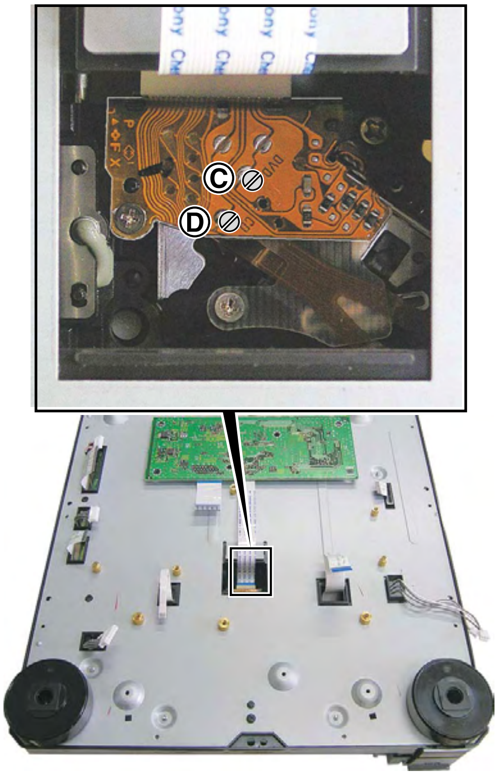
⑦ Reverse the DVPL Assy.


⑥
⑤
⑥
⑤
⑥
⑥
⑤
⑤
⑥
⑥
⑤
⑦
DVPL Assy

● **Bottom view**

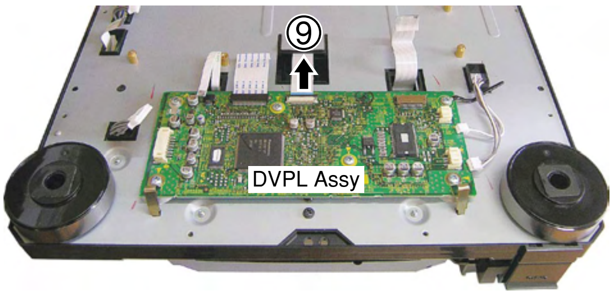
⑧ Short-circuit two points of C and D by soldering.

Note : After replacement, connect the flexible cable, then remove the soldered joint. (open)



● **Bottom view**

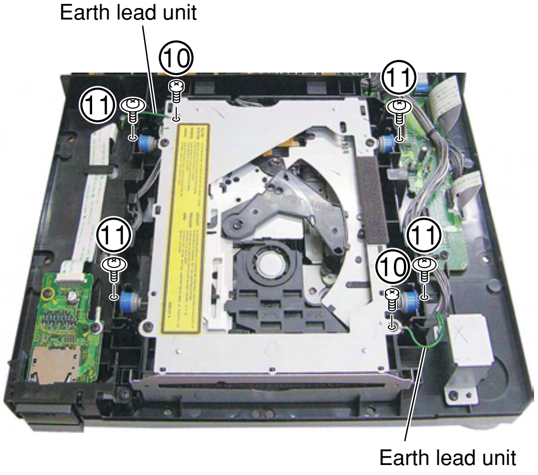
⑨ Disconnect the one flexible cable.



● **Bottom view**

⑩ Remove the two earth lead units by removing the two screws.

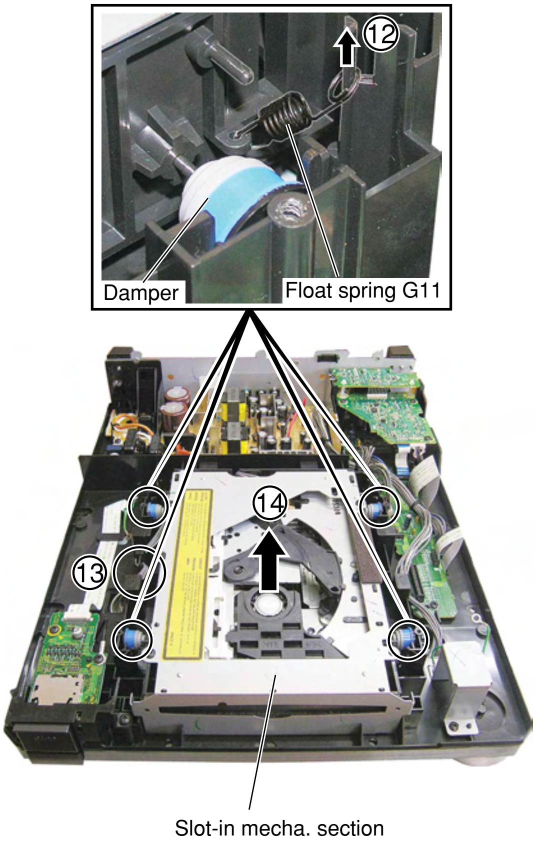
⑪ Remove the four DM screws.


Earth lead unit
⑩
⑪
⑪
⑪
⑪
⑩
Earth lead unit

⑫ Remove the four float springs G11.

⑬ Loosen the jumper wire.

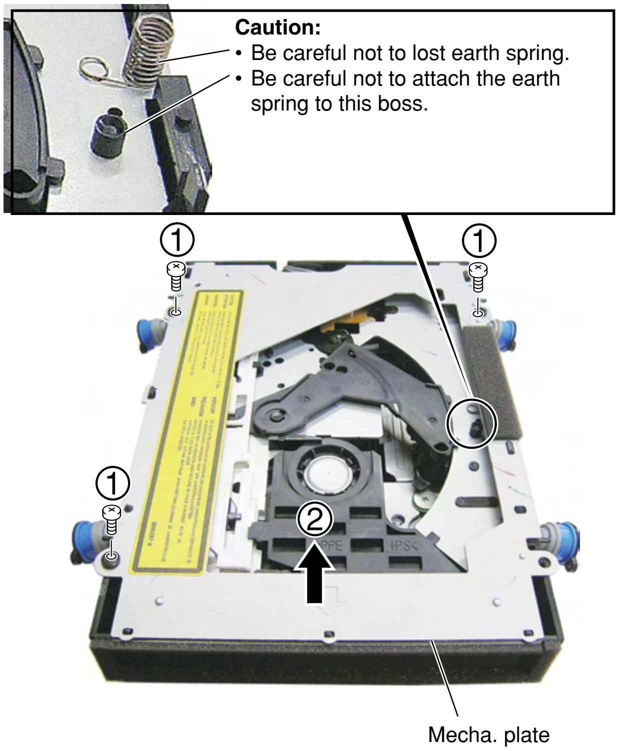
⑭ Remove the slot-in mecha. section.


⑫
Damper
Float spring G11
⑭
⑬
Slot-in mecha. section

3 Traverse Mecha. Assy 04RM2

① Remove the three screws.

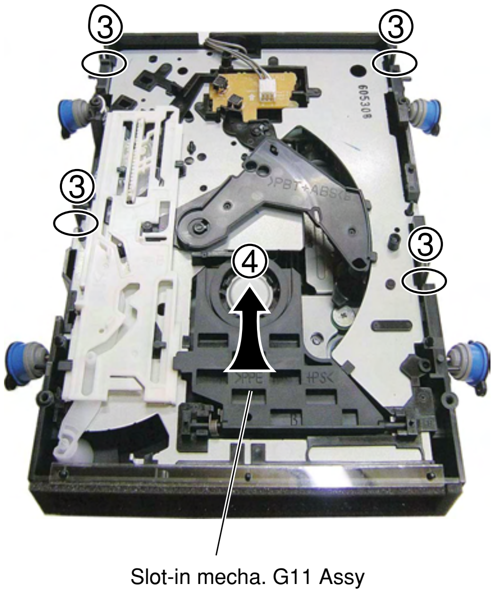
② Remove the mecha. plate.


①
①
①
②
Mecha. plate

Caution:
- Be careful not to lost earth spring.
- Be careful not to attach the earth spring to this boss.

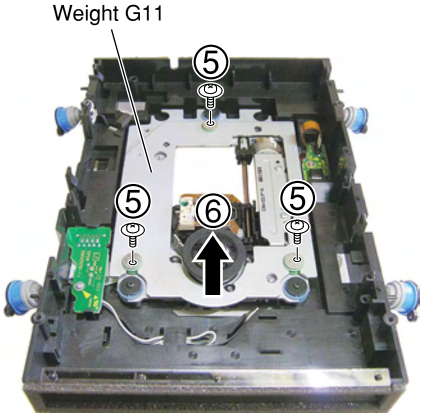
③ Unhook the four hooks.

④ Remove the slot-in mecha. G11 Assy.


③
③
③
③
④
Slot-in mecha. G11 Assy

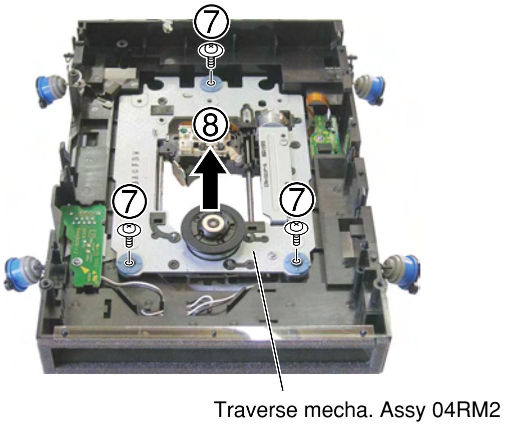
⑤ Remove the three DRA screws.

⑥ Remove the weight G11.


Weight G11
⑤
⑤
⑤
⑥

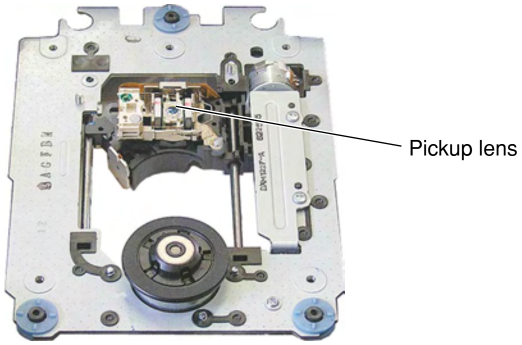
⑦ Remove the three float screws.

⑧ Remove the traverse mecha. Assy 04RM2.


⑦
⑦
⑦
⑧
Traverse mecha. Assy 04RM2

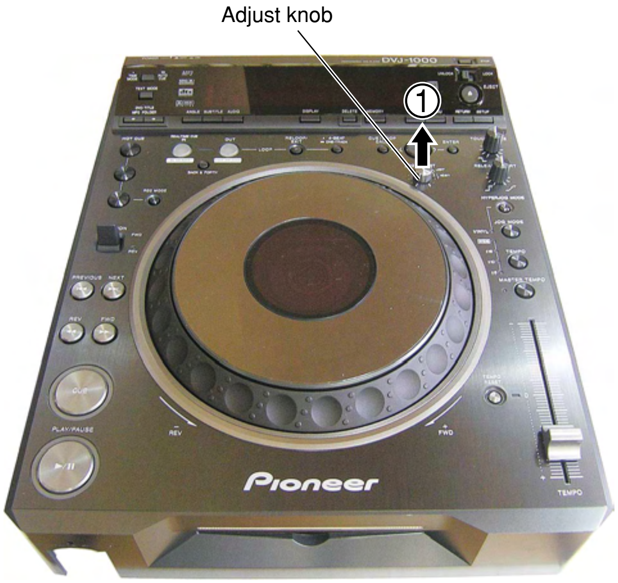
Before shipment, be sure to clean the pickup lens, using the following cleaning materials:

Cleaning liquid : GEM1004
Cleaning paper : GED-008


Pickup lens

4 JOG Section

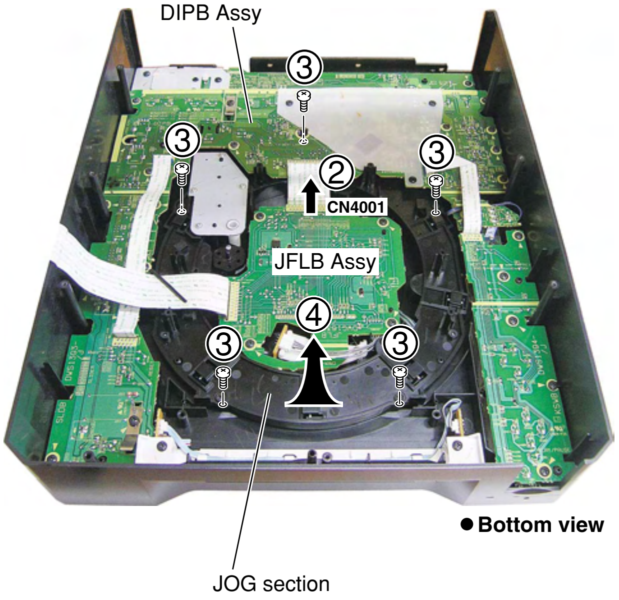
① Remove the adjust knob.


Adjust knob
①

② Disconnect the one flexible cable.

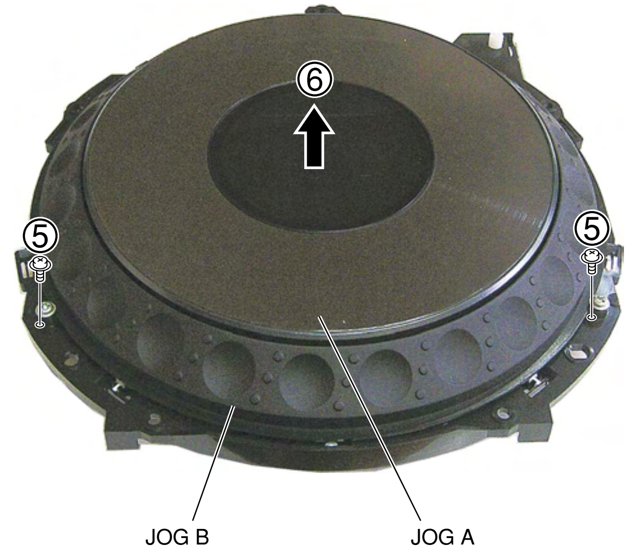
③ Remove the five screws.

④ Remove the JOG section.


DIPB Assy
③
③
③
②
CN4001
JFLB Assy
④
③
③
● Bottom view
JOG section

⑤ Remove the two screws.

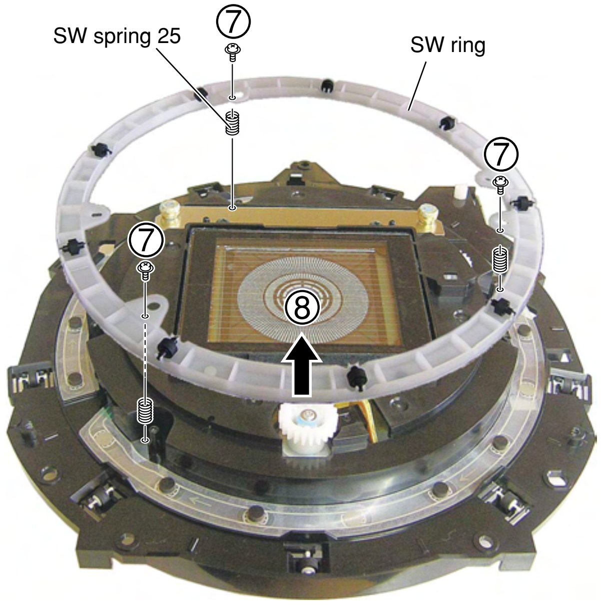
⑥ Remove the JOG A and JOG Bs.


⑥
⑤
⑤
JOG B
JOG A

⑦ Remove the three screws.

⑧ Remove the SW ring.

Caution:
Be careful not to lost SW spring 25.


SW spring 25
⑦
SW ring
⑦
⑦
⑧

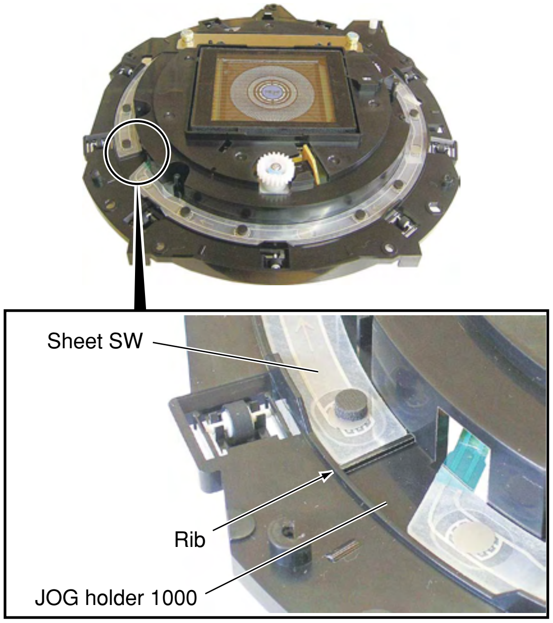
Notes on Replacement

Notes on replacing the Sheet SW

● Place to adhere the Sheet SW

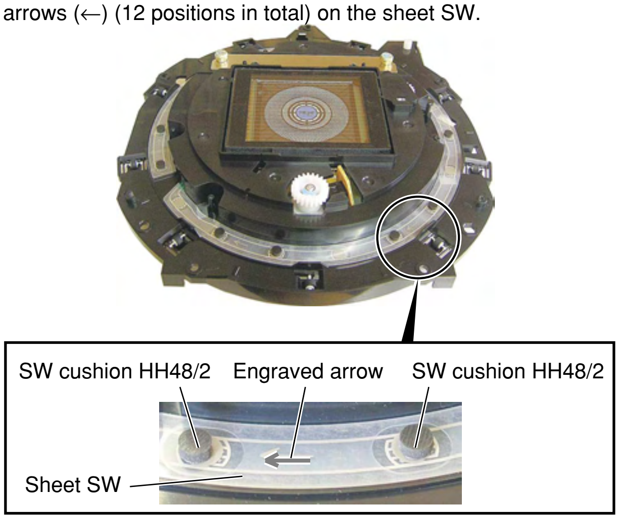
Notes:

1. Be careful not to warp the sheet SW.
2. Remove any dirt on the JOG holder to which the sheet SW is to be adhered. If some adhesive for the old sheet SW remains on the JOG holder, completely remove it with a cloth moistened with alcohol.
3. Do NOT place the sheet SW so that it is mounted on the rib of JOG holder 1000.
4. When adhering the sheet SW, be careful not to trap air bubbles in it. If air bubbles are formed, remove the sheet SW and adhere a new sheet SW.
   Do NOT reuse the removed sheet SW.
5. When making a connection, be sure to first release the lock of the connector then securely relock the connector after making the connection.


Sheet SW
Rib
JOG holder 1000

● Place to adhere the SW cushions HH48/2

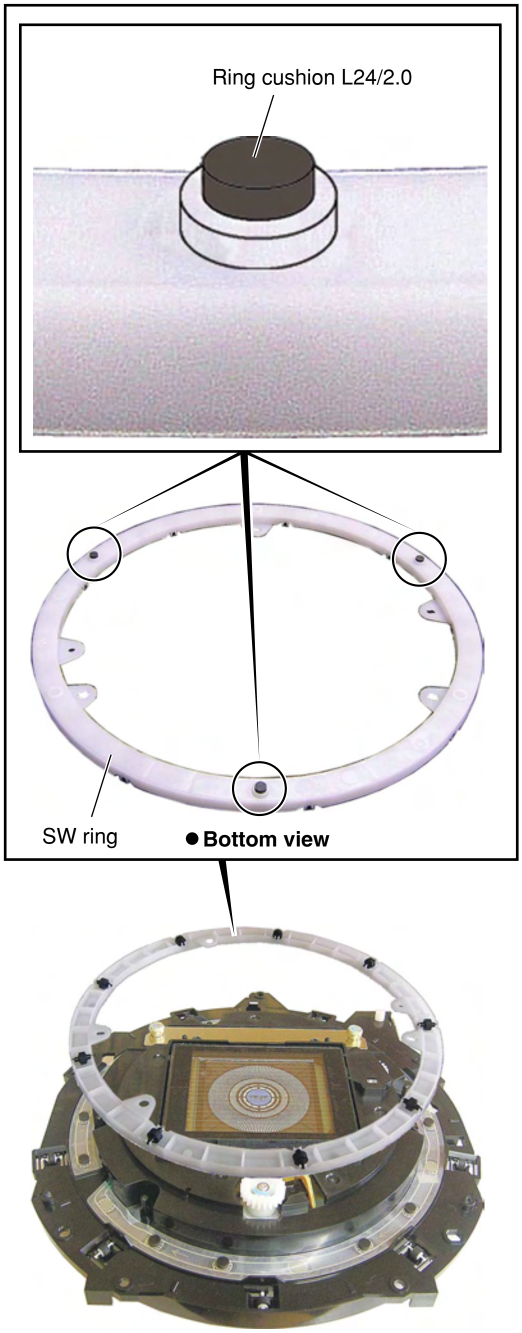
Adhere the cushions to the right and left of the engraved arrows (←) (12 positions in total) on the sheet SW.


SW cushion HH48/2
Engraved arrow
SW cushion HH48/2
Sheet SW

Notes on replacing the SW Ring

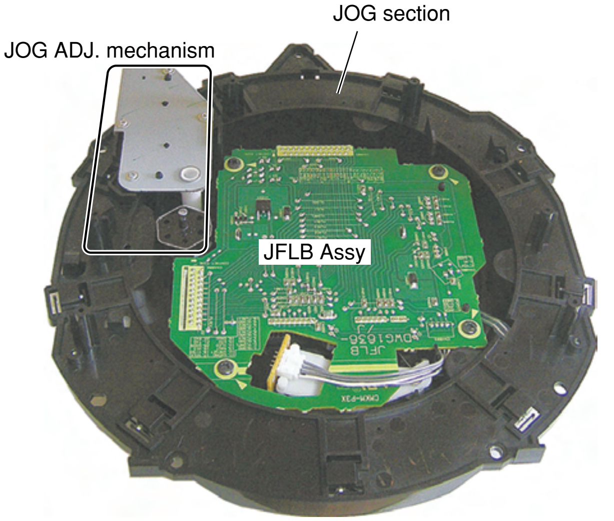
● Place to adhere the Ring cushions L24/2.0

Note:
Adhere a ring cushion on and inside the pedestal.
Do NOT place the ring cushion outside the pedestal.


Ring cushion L24/2.0
SW ring
● Bottom view

Positioning of the JOG ADJ. mechanism

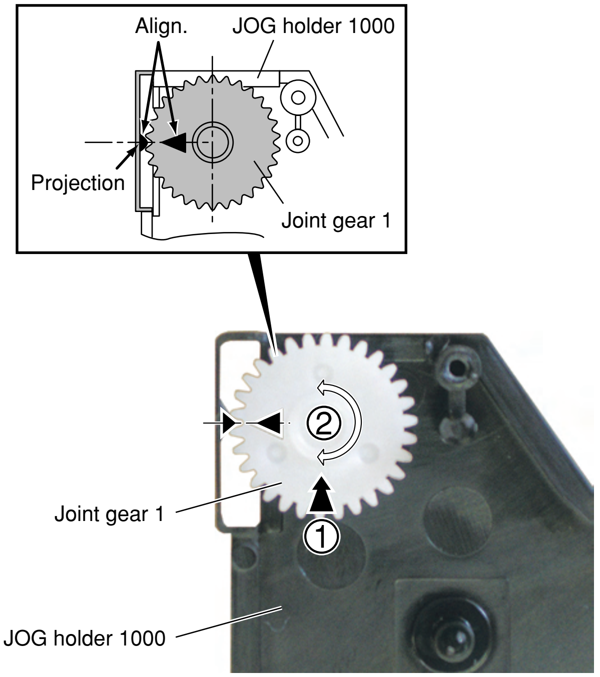
Note: When reassembling the JOG ADJ. mechanism, be sure to perform positioning, as shown below:


JOG section
JOG ADJ. mechanism
JFLB Assy

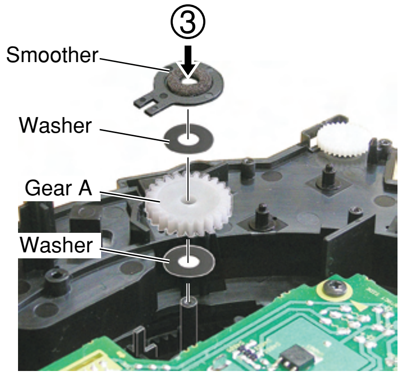
● **Bottom view**

① Mount the joint gear 1.

② Align the projection of JOG holder 1000 (▶) with the arrow head (◀) on joint gear 1.

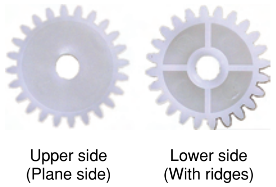

Align.
JOG holder 1000
Projection
Joint gear 1
②
①
Joint gear 1
JOG holder 1000

③ Install the washer, gear A, washer, then smoother, in that order.


③
Smoother
Washer
Gear A
Washer

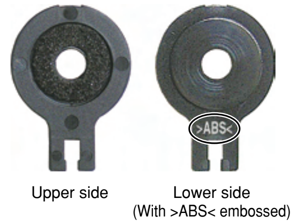
● Gear A

Place Gear A plane side up.

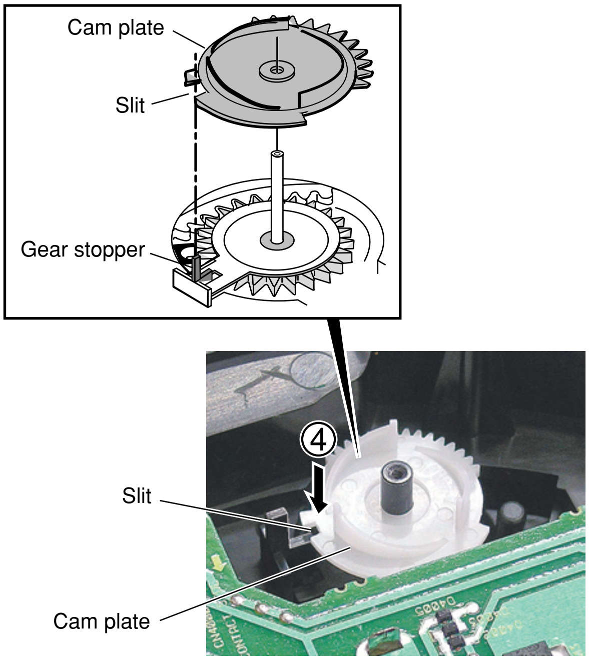

Upper side
(Plane side)
Lower side
(With ridges)

● Smoother

Place it with the side with the indication ">ABS<" facing downward.

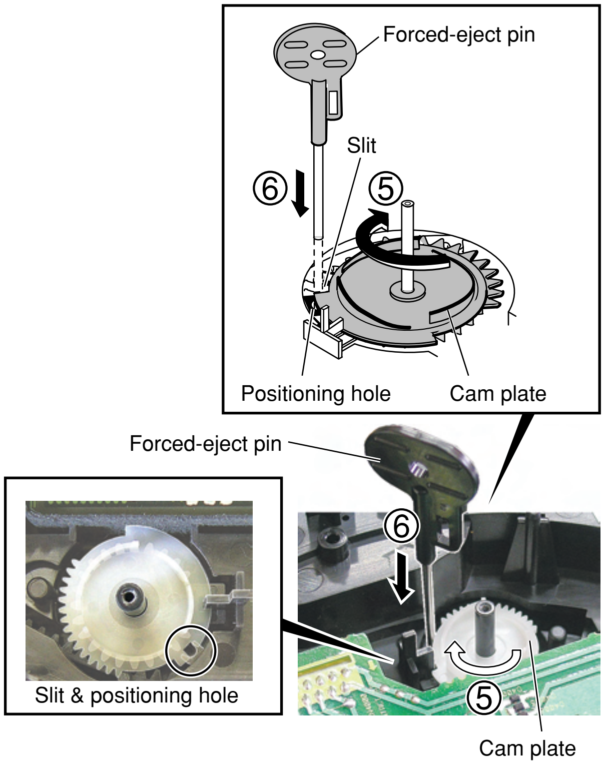

>ABS<
Upper side
Lower side
(With >ABS< embossed)

④ Mount the cam plate on place it so that the slit of the cam plate aligns with the gear stopper.


Cam plate
Slit
Gear stopper
④
Slit
Cam plate

⑤ Turn the cam plate clockwise until the slit aligns with the positioning hole.

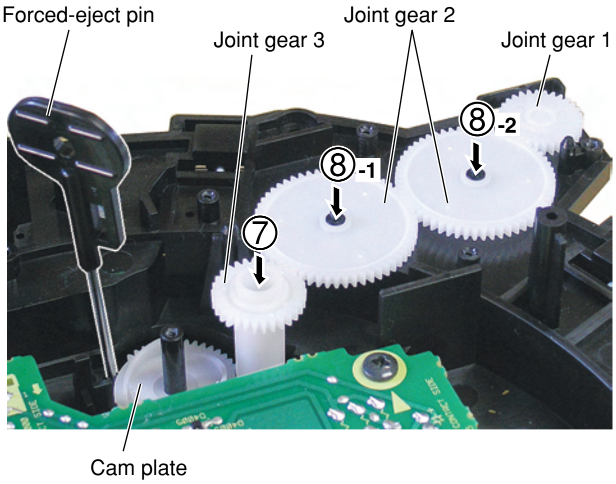
⑥ Insert the forced-eject pin through the positioning hole to immobilize the cam plate.


Forced-eject pin
Slit
⑥
⑤
Positioning hole
Cam plate
Forced-eject pin
⑥
Slit & positioning hole
⑤
Cam plate

⑦ Mount the joint gear 3.

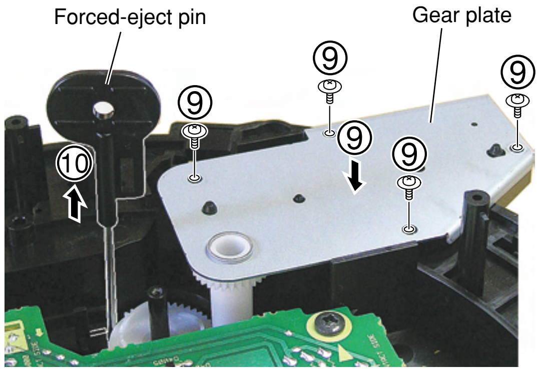
⑧ Mount the two joint gears 2. (There is "> POM <" marked beneath)

Note: When mounting joint gears 2, engage the gears, being careful not to move joint gear 1.


Forced-eject pin
Joint gear 3
Joint gear 2
Joint gear 1
⑧-2
⑧-1
⑦
Cam plate

⑨ Secure the gear plate, using the four screws.

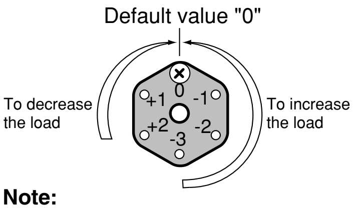
⑩ Remove the forced-eject pin.


Forced-eject pin
Gear plate
⑨
⑨
⑨
⑨
⑨
⑩

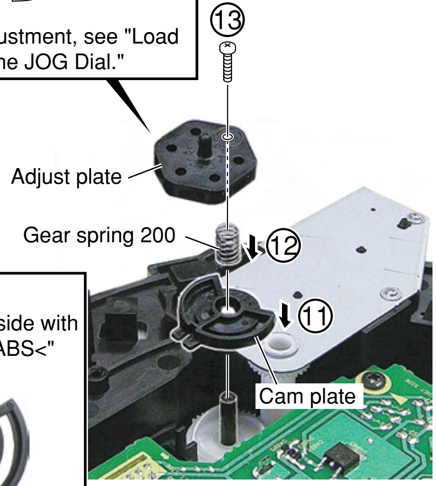
⑪ Mount the cam plate.

⑫ Install the gear spring 200.

⑬ Secure the adjust plate, using one screw.

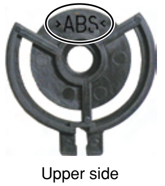

Default value "0"
To decrease the load
To increase the load
0
+1
-1
+2
-2
-3

Note:
For details on adjustment, see "Load Check mode for the JOG Dial."


⑬
Adjust plate
Gear spring 200
⑫
⑪
Cam plate

- **Cam plate**

Place it with the side with the indication ">ABS<" facing upward.

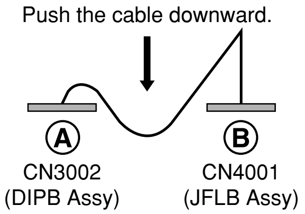


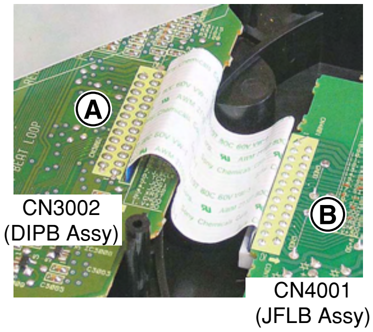
Upper side
(With >ABS< embossed)

Notes on Lead Dressing

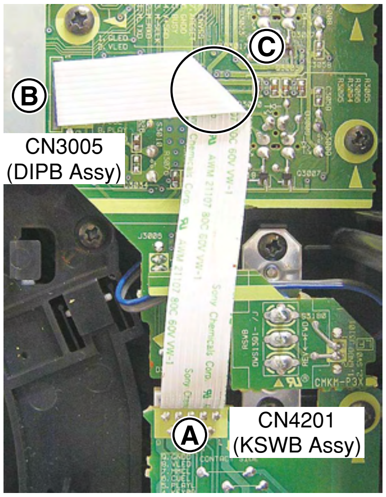
• Control Panel Section

Notes:
- Fold one end of the flat cable along a line 13 mm from the edge.
- The folded end of the cable must be inserted to connector Ⓑ.
- Connect the end not folded first to connector Ⓐ then the folded end to Ⓑ.
- NEVER fold the cable at the Ⓐ end.

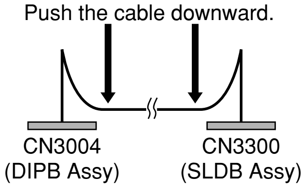

Push the cable downward.
Ⓐ
CN3002
(DIPB Assy)
Ⓑ
CN4001
(JFLB Assy)


Ⓐ
CN3002
(DIPB Assy)
Ⓑ
CN4001
(JFLB Assy)

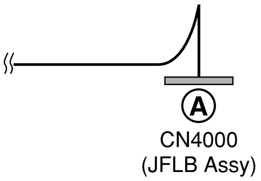
Notes:
- Fold one end of the flat cable at the end of the reinforcing plate.
- The folded end of the cable must be inserted to connector Ⓑ.
- Connect the end not folded first to connector Ⓐ then the folded end to Ⓑ.
- Fold the flat cable at point Ⓒ so that the fold forms a right angle.
- Never fold the cable at the Ⓐ end.

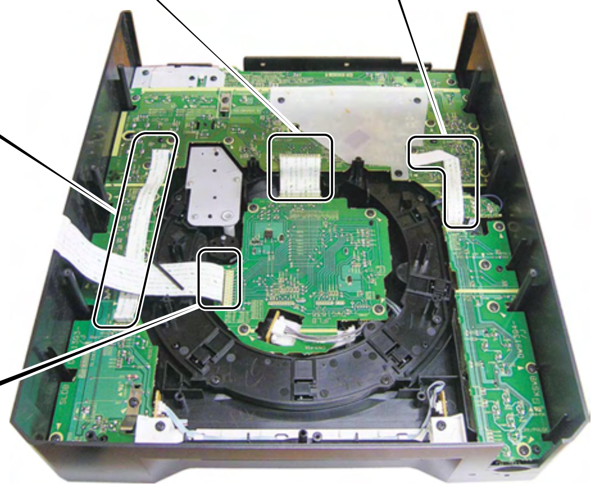

Ⓒ
Ⓑ
CN3005
(DIPB Assy)
Ⓐ
CN4201
(KSWB Assy)

Note:
- Fold both ends of the flat cable, each along a line 15 mm from the edge.


Push the cable downward.
CN3004
(DIPB Assy)
CN3300
(SLDB Assy)

Notes:
- Fold one end of the flat cable along a line 13 mm from the edge.
- The folded end of the cable must be inserted to connector Ⓐ.

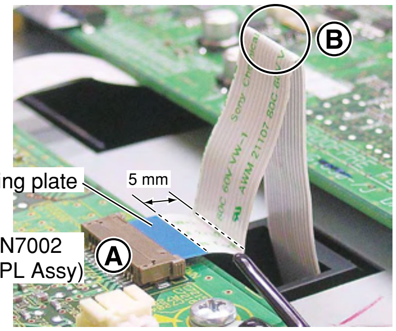

Ⓐ
CN4000
(JFLB Assy)

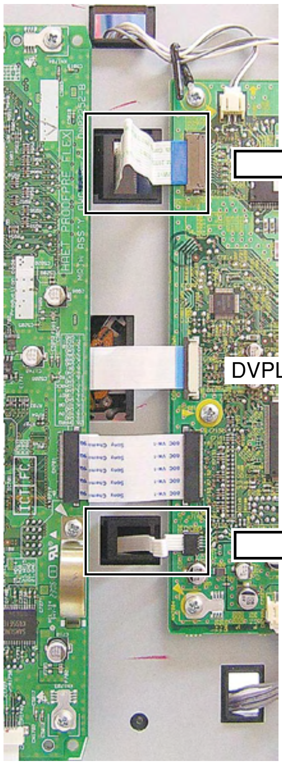


● **Bottom view**

• Bottom Section of Chassis

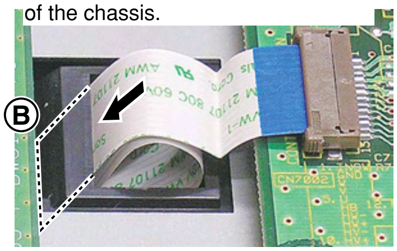
Notes:
- Fold one end of the flat cable along a line 5 mm from the end of the reinforcing plate.
  Then connect it to connector Ⓐ.
- Fold the flat cable at point Ⓑ to fold it back.


B
Reinforcing plate
5 mm
CN7002
(DVPL Assy)
A

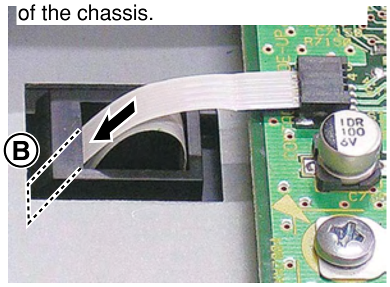


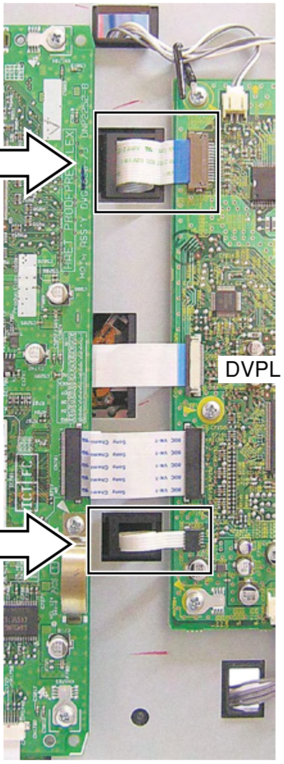
• Bottom view

Insert the folded part Ⓑ into the slot of the chassis.



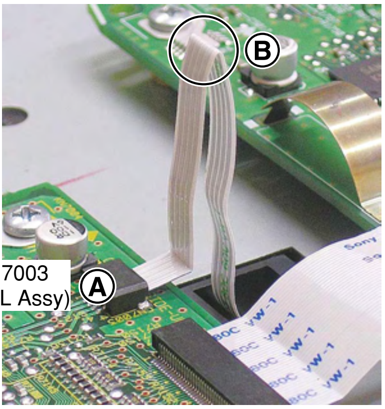
Insert the folded part Ⓑ into the slot of the chassis.





• Bottom view

Notes:
- Fold one end of the flat cable at the end of the reinforcing plate.
  Then connect it to connector Ⓐ.
- Fold the flat cable at point Ⓑ to fold it back.


B
CN7003
(DVPL Assy)
A

7.2 PASS / FALL JUDGMENT OF PICK UP

• Pass/fail judgment of pick up

■ Pick up pass/fail judgment

CHECK POINT	CD : PU LD Power for CD (Wavelength=790ns) is checked within 0.18 ± 0.017 mW
	DVD : PU LD Power for DVD (Wavelength=650ns) is checked within 0.193 ± 0.018 mW

CHECK POINT	• LD current: CD LD Typ 55mA Max 65mA / DVD LD Typ 60mA Max 73mA
METHOD	CD LD : DVPB ASSY:The voltages (DC value) of both sides of R7011 is measured and divided by 24 DVD LD : DVPB ASSY:The voltages (DC value) of both sides of R7010 is measured and divided by 12
NOTE	

CHECK POINT	• Focus coil resistance value: 3.7 ± 0.55Ω
METHOD	FFC is removed from CN7001 of DVPB ASSY after LD short land of PU ASSY, the resistance value between the terminal board conductor sides of 1 Pin and 2 Pin of FFC is measured.

CHECK POINT	• Tracking coil resistance value: 4.3 ± 0.65Ω
METHOD	FFC is removed from CN7001 of DVPB ASSY after LD short land of PU ASSY, the resistance value between the terminal board conductor sides of 3 Pin and 4 Pin of FFC is measured.

7.3 PARTS

7.3.1 IC

- The information shown in the list is basic information and may not correspond exactly to that shown in the schematic diagrams.

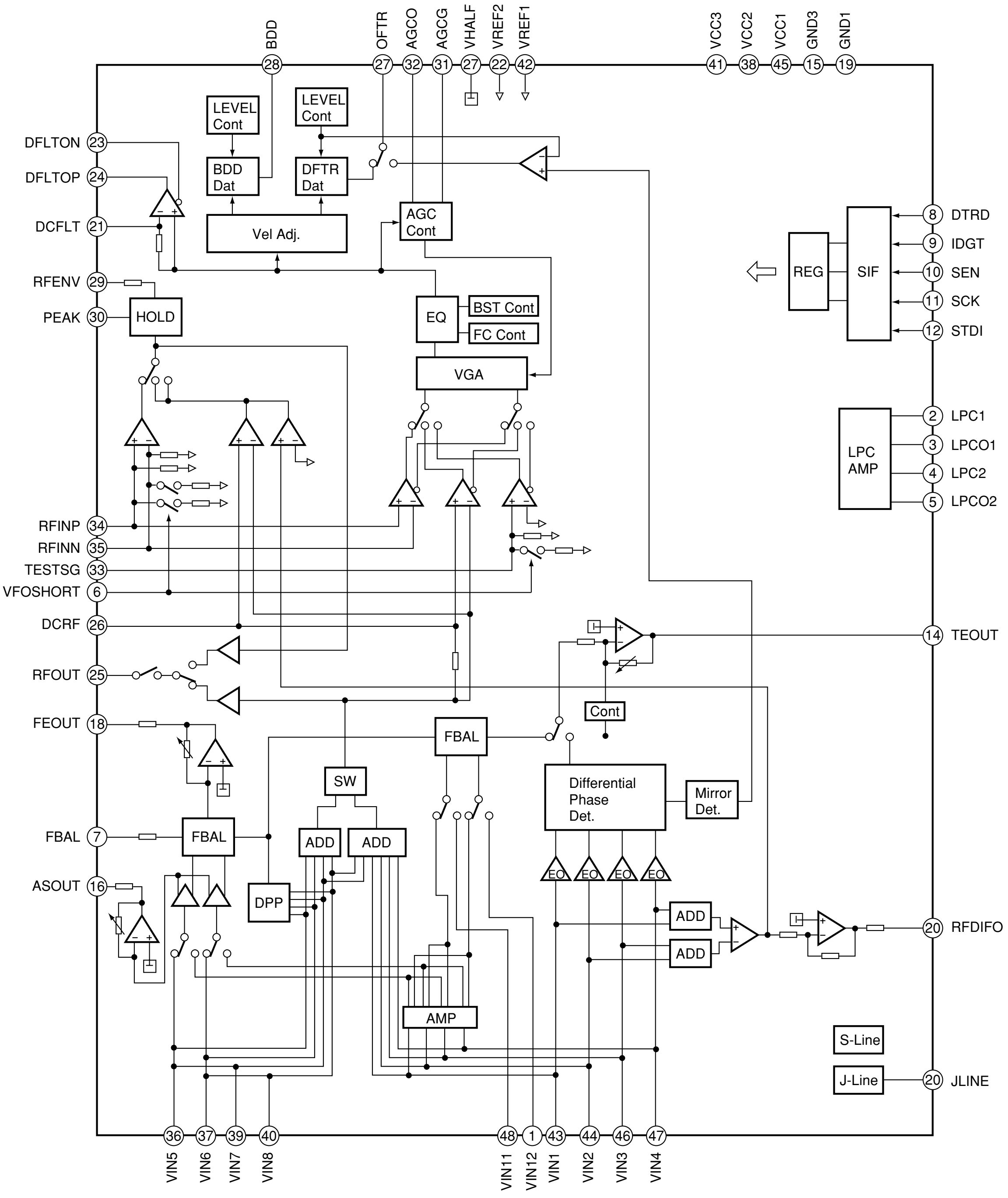
● List of IC

AN22022A, MN103S71F, EP2C20F484C8, HD6417709SHF200B

■ AN22022A (DVPL ASSY : IC7000)

- FEP

● Block Diagram


BDD
OFTR
AGCO
AGCG
VHALF
VREF2
VREF1
VCC3
VCC2
VCC1
GND3
GND1
28
27
32
31
27
22
42
41
38
45
15
19
LEVEL Cont
LEVEL Cont
BDD Dat
DFTR Dat
Vel Adj.
AGC Cont
DFLTON 23
DFLTOP 24
DCFLT 21
RFENV 29
PEAK 30
HOLD
EQ
BST Cont
FC Cont
VGA
REG
SIF
8 DTRD
9 IDGT
10 SEN
11 SCK
12 STDI
LPC AMP
2 LPC1
3 LPCO1
4 LPC2
5 LPCO2
RFINP 34
RFINN 35
TESTSG 33
VFOSHORT 6
DCRF 26
RFOUT 25
FEOUT 18
FBAL 7
ASOUT 16
14 TEOUT
Cont
FBAL
SW
ADD
ADD
FBAL
DPP
Differential Phase Det.
Mirror Det.
EQ
EQ
EQ
EQ
ADD
ADD
20 RFDIFO
AMP
S-Line
J-Line
20 JLINE
36
37
39
40
48
1
43
44
46
47
VIN5
VIN6
VIN7
VIN8
VIN11
VIN12
VIN1
VIN2
VIN3
VIN4

Pin Function

No.	Pin Name	Pin Function
1	VIN12	3-beam sub (CD) input 2
2	LPC1	Laser pin input (DVD head)
3	LPCO1	Laser drive output (DVD head)
4	LPC2	Laser pin input (CD head)
5	LPCO2	Laser drive output (CD head)
6	VFOSHORT	VFOSHORT control
7	FBAL	Focus balance control
8	DTRD	Data read signal input of data slice (for RAM)
9	IDGT	Gate signal input of data slice-address block (for RAM)
10	SEN	Serial enable input
11	SCK	Serial clock input
12	STDI	Serial data input/output
13	JLINE	JLINE current setting
14	TEOUT	Tracking error signal output
15	GND3	Ground 3
16	ASOUT	All addition signal output
17	VHALF	VHALF voltage output
18	FEOUT	Focus error signal output
19	GND1	Ground 1
20	RFDIFO	Radial differential output
21	DCFLT	Capacitor connection pin for filter output
22	VREF2	VREF 2 voltage output
23	DFLTON	Filter amplifier inverting output
24	DFLTOP	Filter amplifier non-inverting output
25	RFOUT	RF all addition amplifier output
26	DCRF	Filter pin for DC cut of RF addition amplifier
27	OFTR	OFTR output
28	BDO	BDO output
29	RFENV	RF envelope output
30	PEAK	Peak envelope detecting filter
31	AGCG	AGC amplifier gain control
32	AGCO	AGC amplifier level control
33	TESTSG	TEST signal input
34	RFINP	RF signal non-inverting input
35	RFINN	RF signal inverting input
36	VIN5	RF input pin 1 of inside division into 4 (CD)
37	VIN6	RF input pin 2 of inside division into 4 (CD)
38	VCC2	Power supply pin 2 (5V)
39	VIN7	RF input pin 3 of inside division into 4 (CD)
40	VIN8	RF input pin 4 of inside division into 4 (CD)
41	VCC3	Power supply pin 3 (3.3V)
42	VREF1	VREF1 voltage output
43	VIN1	RF input pin 1 of inside division into 4 (DVD)
44	VIN2	RF input pin 2 of inside division into 4 (DVD)
45	VCC1	Power supply pin 1 (5V)
46	VIN3	RF input pin 3 of inside division into 4 (DVD)
47	VIN4	RF input pin 4 of inside division into 4 (DVD)
48	VIN1	3-beam sub (CD) input 1

■ MN103S71F (DVPL ASSY : IC7100) (1/3) [SODC]

● Pin Function • SSCDC-IC

No.	Mark	Pin name	I/O	Function
1	P2	XDMUTE1	O	Driver IC mute control
2	P3	XDMUTE2	O	Driver IC mute control
3	P4/EXCNT0	ILMUSK	I	Connect to VSS
4	VDD3	VDD3	-	Power supply (VD3V)
5	VSS	VSS	-	GNDD
6	P5/EXCNT1	INSIDE	I	Servo mechanism inside SW input (L:ON)
7	P6/NSPCCS	GSW	O	PU LD lighting selection (L:DVD H:CD)
8	P7/FADR17	FADR17/NSPCCS	O	Address output to FLASH ROM
9	P8/FADR18	FADR18	O	NC
10	FADR11	FADR11	O	Address output to FLASH ROM
11	FADR9	FADR9	O	Address output to FLASH ROM
12	VDD15	VDD15	-	Power supply (VD1R5)
13	FADR8	FADR8	O	Address output to FLASH ROM
14	FADR13	FADR13	O	Address output to FLASH ROM
15	FADR14	FADR14	O	Address output to FLASH ROM
16	NWE	NWE	O	Write enable output to FLASH ROM
17	FADR16	FADR16	O	Address output to FLASH ROM
18	FADR15	FADR15	O	Address output to FLASH ROM
19	DRAMVDD15	DRAMVDD15	-	DRAM power supply (DRAMD1R5)
20	DRAMVSS	DRAMVSS	-	GND for DRAM
21	VSS	VSS	-	GNDD
22	FADR12	FADR12	O	Address output to FLASH ROM
23	FADR7	FADR7	O	Address output to FLASH ROM
24	FADR6	FADR6	O	Address output to FLASH ROM
25	FADR5	FADR5	O	Address output to FLASH ROM
26	FADR4	FADR4	O	Address output to FLASH ROM
27	FADR3	FADR3	O	Address output to FLASH ROM
28	FADR2	FADR2	O	Address output to FLASH ROM
29	FADR1	FADR1	O	Address output to FLASH ROM
30	FADR0	FADR0	O	Address output to FLASH ROM
31	VSS	VSS	-	GNDD
32	VDD3	VDD3	-	Power supply (VD3V)
33	FDT0	FDT0	I/O	Data input/output to FLASH ROM
34	FDT1	FDT1	I/O	Data input/output to FLASH ROM
35	FDT2	FDT2	I/O	Data input/output to FLASH ROM
36	FDT3	FDT3	I/O	Data input/output to FLASH ROM
37	FDT4	FDT4	I/O	Data input/output to FLASH ROM
38	FDT5	FDT5	I/O	Data input/output to FLASH ROM
39	FDT6	FDT6	I/O	Data input/output to FLASH ROM
40	FDT7	FDT7	I/O	Data input/output to FLASH ROM
41	NCE	NCE	O	Chip enable output to FLASH ROM
42	FADR10	FADR10	O	Address output to FLASH ROM
43	NOE	NOE	O	Output enable output to FLASH ROM
44	MMOD	MMOD	I	Connect to VSS
45	NRST	NRST	I	Hardware reset input
46	VSS	VSS	-	GNDD
47	SCLOCK	SCLOCK	I/O	NC
48	SDATA	SDATA	I/O	NC
49	TxD/EXTRG0/MDATA	TXD	I/O	NC
50	RxD/EXTRG1/MCLOCK	RXD	I/O	NC
51	VDD3	VDD3	-	Power supply (VD3V)
52	OSCI	OSCI	I	Oscillation input
53	OSCO	OSCO	O	Oscillation output (16.93MHz)
54	VSS	VSS	-	GNDD
55	DRV0	DRV0(VRCAP)	O	NC
56	DRV1	DRV1(SPDRV)	O	Spindle motor control
57	DRV2	DRV2(FBAL)	O	Focus balance control
58	DRV3	DRV3(TBAL)	O	NC
59	DRV4	DRV4(STPM1P)	O	Stepping motor control
60	DRV5	DRV5(STPM1N)	O	Stepping motor control
61	DRV6	DRV6(6VRSW)	O	NC
62	DRV7	DRV7(7VRSW)	O	NC
63	VSS	VSS	-	GNDD
64	DRV8	DRV8(FCSG)	O	NC
65	DRV9	DRV9(NFEPRST)	O	PU monitor current DVD selection (H:ON L:OFF)
66	DRV10	DRV10	O	PU monitor current CD selection (H:ON L:OFF)
67	DRV11	DRV11(LOAD)	O	Loading motor control
68	DRV12	DRV12(FEPCK)	O	Clock output to FEP
69	DRV13	DRV13(FEPDT)	I/O	Data input/output to FEP
70	DRV14	DRV14(FEPEN)	O	Enable output to FEP
71	DRAMVSS	DRAMVSS	-	GND for DRAM
72	DRAMVDD15	DRAMVDD15	-	DRAM power supply (DRAMD1R5)
73	DRAMVDD33	DRAMVDD33	-	Power supply (VD3V)
74	VDD3	VDD3	-	Power supply (VD3V)

■ MN103S71F (DVPL ASSY : IC7100) (2/3) [SODC]

● Pin Function

No.	Mark	Pin name	I/O	Function
75	FG	FG	I	Spindle motor FG input
76	TX	TX	O	NC
77	VDD15	VDD15	-	Power supply (VD1R5)
78	VSS	VSS	-	GNDD
79	TSTSG	TSTSG	O	EQ calibration signal to FEP
80	VFOSHORT	VFOSHORT	O	VFO short control
81	JLINE	JLINE	O	NC
82	BDO	BDO	I	BDO (black dot out) input
83	OFTR	OFTR	I	OFTR (off track) input
84	AVSSD	AVSSD	-	GNDA
85	ROUT	ROUT	O	NC
86	LOUT	LOUT	O	NC
87	AVDDD	AVDDD	-	Power supply (VA3V)
88	VCOF	VCOF	I	VCO control voltage
89	TRCRS	TRCRS	I	Input for TRCRS (track cross) formation
90	AVDDC	AVDDC	-	Power supply (VA3V)
91	WBLIN	WBLIN	I	Connect to VSS
92	CSLFLT	CSLFLT	I	NC
93	RFDIF	RFDIF	I	NC
94	AVSSC	AVSSC	-	GNDA
95	PLFLT2	PLFLT2	I	Capacitor connection for PLL
96	PLFLT1	PLFLT1	I	Capacitor connection for PLL
97	AVSSB	AVSSB	-	GNDA
98	ARF	ARF	I	RF+input
99	NARF	NARF	I	RF-input
100	VHALF	VHALF	I	Reference voltage (1.65V) input
101	RVI	RVI	I	VREFH reference current source control
102	VREFH	VREFH	I	Reference voltage (2.2V) input
103	DSLF2	DSLF2	I	Capacitor connection for DSL (data slicer)
104	DSLF1	DSLF1	I	Capacitor connection for DSL (data slicer)
105	AVDDB	AVDDB	-	Power supply (VA3V)
106	JITOUT	JITOUT	O	Jitter monitor output
107	AVDDA	AVDDA	-	Power supply (VA3V)
108	TECAPA	TECAPA	I	NC
109	AD0	AD0(FE)	I	Focus error input
110	AD2	AD2(AS)	I	RFAS input
111	AD1	AD(TE)	I	Tracking error input
112	AD3	AD3(RFENV)	I	RF envelope input
113	AD4	AD4(RFDIF)	I	Radial differential input
114	AD5	AD5(FEDRV)	I	Focus drive input
115	AD6	AD6(TEDRV)	I	Tracking drive input
116	AD7	AD7(T+)	I	Connect to VSS
117	AD8	AD8(T-)	I	Connect to VSS
118	AVSSA	AVSSA	-	GNDA
119	PWM0	PWM0	O	Focus drive control
120	PWM1	PWM1	O	Tracking drive control
121	VSS	VSS	-	GNDD
122	VDD3	VDD3	-	Power supply (VD3V)
123	IDGT	IDGT	O	NC
124	DTR	DTR	O	NC
125	MONI0/P9	SPGAIN/MONI0	I/O	SPDL motor current switching control (at reverse brake: H)
126	MONI1/P10	DL_CHK/MONI1	I/O	Program start check (at program start: H)
127	MONI2/P11	SETUP_CHK/MONI2	I/O	SETUP check (at disc start process complete: H)
128	MONI3/P12	NDMUTE2/MONI3	I/O	L output
129	MSTPOL/MONI4	MSTPOL/MONI4	I	
130	DASPST/MONI5	DASPST/MONI5	I	Connect to VSS
131	NEJECT/MONI6	NEJECT/MONI6	O	NC
132	NTRYCL/MONI7	NTRYCL/MONI7	O	NC
133	DMARQ	DMARQ	O	DMA request output to host
134	NIOWR	NIOWR	I	Host write input
135	VDD3	VDD3	-	Power supply (VD3V)
136	VSS	VSS	-	GNDD
137	NIORD	NIORD	I	Host read input
138	IODRY	IODRY	O	Ready output to host
139	NDMACK	NDMACK	I	Host DMA acknowledge input
140	INTRQ	INTRQ	O	Interrupt output to host
141	NIOCS16	NIOCS16	O	Data bus width selection
142	DA1	DA1	I	Address input
143	NPDIAG	NPDIAG	I/O	
144	DA0	DA0	I	Address input
145	VSS	VSS	-	GNDD
146	VDD3	VDD3	-	Power supply (VD3V)
147	DA2	DA2	I	Address input
148	NCS1FX	NCS1FX	I	Chip select input

■ MN103S71F (DVPL ASSY : IC7100) (3/3) [SODC]

● Pin Function

No.	Mark	Pin name	I/O	Function
149	NCS3FX	NCS3FX	I	Chip select input
150	NDASP	NDASP	I/O	
151	HDD15	HDD15	I/O	Data input/output
152	HDD0	HDD0	I/O	Data input/output
153	HDD14	HDD14	I/O	Data input/output
154	HDD1	HDD1	I/O	Data input/output
155	HDD13	HDD13	I/O	Data input/output
156	VDD3	VDD3	-	Power supply (VD3V)
157	VDD15	VDD15	-	Power supply (VD1R5)
158	VSS	VSS	-	GNDD
159	HDD2	HDD2	I/O	Data input/output
160	HDD12	HDD12	I/O	Data input/output
161	HDD3	HDD3	I/O	Data input/output
162	HDD11	HDD11	I/O	Data input/output
163	HDD4	HDD4	I/O	Data input/output
164	HDD10	HDD10	I/O	Data input/output
165	HDD5	HDD5	I/O	Data input/output
166	VSS	VSS	-	GNDD
167	VDD3	VDD3	-	Power supply (VD3V)
168	HDD9	HDD9	I/O	Data input/output
169	HDD6	HDD6	I/O	Data input/output
170	HDD8	HDD8	I/O	Data input/output
171	HDD7	HDD7	I/O	Data input/output
172	VDDH	VDDH	-	Power supply (VD5V)
173	NRESET	NRESET	I	Reset input from host
174	MASTER	MASTER	I	Connect to VSS
175	P0/SERIAL	LPS1	I	Loading mechanism SW input
176	P1/SERIAL	LPS2	I	Loading mechanism SW input

■EP2C20F484C8 (MAIN ASSY : IC2000) (1/8)

• DMA / VMC / CAPT FPGA

● Pin Function

No.	Pin name	Signal name	I/O	Function
A1	GND	GNDD	-	GND
A2	VCCIO3	VLTTL	-	Input/output power supply (3.3V)
A3	IO	VTAG0	I	VDEC status
A4	IO	VC6	I	Video data
A5	IO	VC4	I	Video data
A6	IO	VC2	I	Video data
A7	IO	VC0	I	Video data
A8	IO	VY6	I	Video data
A9	IO	VY4	I	Video data
A10	IO	VY2	I	Video data
A11	IO	VY0	I	Video data
A12	IN	VCLK54M	I	Clock for video data
A13	IO	GNDD	O	GND (unused pin)
A14	IO	VDEC_BSCLK	O	Bus strobe for VDEC
A15	IO	XRD_FPGA	I	Host read
A16	IO	INT_HSYNC	O	HSYNC for external sync
A17	IO	CONT1	I	Mixer control
A18	IO	CSYNC	I	CSYNC for external sync
A19	IO	33M_GSL	O	GSL 33MHz clock
A20	IO	GSL_XCS04	O	GSL chip select
A21	VCCIO4	VLTTL	-	Input/output power supply (3.3V)
A22	GND	GNDD	-	GND
AA1	VCCIO1	VLTTL	-	Input/output power supply (3.3V)
AA2	GND	GNDD	-	GND
AA3	IO	DCKE1	O	DDR SDRAM clock enable
AA4	IO	GNDD	O	GND (unused pin)
AA5	IO	GNDD	O	GND (unused pin)
AA6	IO	DQ4	IO	DDR SDRAM data
AA7	IO	DQ6	IO	DDR SDRAM data
AA8	IO	DQ11	IO	DDR SDRAM data
AA9	IO	DQ9	IO	DDR SDRAM data
AA10	IO	DQ13	IO	DDR SDRAM data
AA11	IO	GNDD	O	GND (unused pin)
AA12	IO	DCKN0	O	DDR SDRAM clock
AA13	IO	DCKN1	O	DDR SDRAM clock
AA14	IO	DA1	O	DDR SDRAM address
AA15	IO	DA3	O	DDR SDRAM address
AA16	IO	DA10	O	DDR SDRAM address
AA17	IO	DA11	O	DDR SDRAM address
AA18	IO	DBA0	O	DDR SDRAM address
AA19	IO	DDWE	O	DDR SDRAM write
AA20	IO	DRAS	O	DDR SDRAM RAS
AA21	GND	GNDD	-	GND
AA22	VCCIO6	VLTTL	-	Input/output power supply (3.3V)
AB1	GND	GNDD	-	GND
AB2	VCCIO8	V+2R5D	-	Input/output power supply (2.5V)
AB3	IO	DCKE0	O	DDR SDRAM clock enable
AB4	IO	DCS0	O	DDR SDRAM chip select
AB5	IO	DCS1	O	DDR SDRAM chip select
AB6	IO	DQ5	IO	DDR SDRAM data
AB7	IO	DQ7	IO	DDR SDRAM data
AB8	IO	DQ10	IO	DDR SDRAM data
AB9	IO	DQ8	IO	DDR SDRAM data
AB10	IO	DQ12	IO	DDR SDRAM data
AB11	IO	DQS1	IO	DDR SDRAM DQS
AB12	IO	DCK0	O	DDR SDRAM clock
AB13	IO	DCK1	O	DDR SDRAM clock
AB14	IO	DA0	O	DDR SDRAM address
AB15	IO	DA2	O	DDR SDRAM address
AB16	IO	DA8	O	DDR SDRAM address
AB17	IO	DA9	O	DDR SDRAM address
AB18	IO	DBA1	O	DDR SDRAM address

■ EP2C20F484C8 (MAIN ASSY : IC2000) (2/8)

● Pin Function

No.	Pin name	Signal name	I/O	Function
AB19	IO	DA12	O	DDR SDRAM address
AB20	IO	DCAS	O	DDR SDRAM CAS
AB21	VCCIO7	V+2R5D	-	Input/output power supply (2.5V)
AB22	GND	GNDD	-	GND
B1	VCCIO2	VLTTL	-	Input/output power supply (3.3V)
B2	GND	GNDD	-	GND
B3	IO	GNDD	O	GND (unused pin)
B4	IO	VC7	I	Video data
B5	IO	VC5	I	Video data
B6	IO	VC3	I	Video data
B7	IO	VC1	I	Video data
B8	IO	VY7	I	Video data
B9	IO	VY5	I	Video data
B10	IO	VY3	I	Video data
B11	IO	VY1	I	Video data
B12	GND+	GNDD	-	GND
B13	IO	VDEC_VALID	O	Video data valid
B14	IO	VDEC_XSTOP	I	Video data output stop
B15	IO	SYNC_ON	O	External sync ON/OFF
B16	IO	EXT_HSYNC	O	HSYNC for external sync
B17	IO	CONT2	I	Mixer control
B18	IO	M_CONT1_ON	O	Mixer control
B19	IO	GSL_XCS01	O	GSL chip select
B20	IO	GSL_RDWR	O	GSL read write
B21	GND	GNDD	-	GND
B22	VCCIO5	VLTTL	-	Input/output power supply (3.3V)
C1	IO	XWE0	I	Host/write
C2	IO	XWE1	I	Host/write
C3	IO	GNDD	O	GND (unused pin)
C4	IO	GNDD	O	GND (unused pin)
C5	GND	GNDD	-	GND
C6	VCCIO3	VLTTL	-	Input/output power supply (3.3V)
C7	IO	VDEC_D7	O	VDEC stream data
C8	GND	GNDD	-	GND
C9	IO	VDEC_D4	O	VDEC stream data
C10	IO	VDEC_D2	O	VDEC stream data
C11	VCCIO3	VLTTL	-	Input/output power supply (3.3V)
C12	VCCIO4	VLTTL	-	Input/output power supply (3.3V)
C13	IO	VDEC_D0	O	VDEC stream data
C14	IO	GNDD	O	GND (unused pin)
C15	GND	GNDD	-	GND
C16	IO	GSL_D0	I	GSL stream data
C17	IO	GSL_D6	I	GSL stream data
C18	IO	GSL_D2	I	GSL stream data
C19	IO	DIS_XCS	I	Panel microcomputer chip select
C20	IO	DIS_SCLK	I	Panel microcomputer serial clock
C21	IO	GSL_XWE0	O	GSL host write
C22	IO	GSL_XWAIT	I	GSL host wait
D1	IO	XCS0	I	Host chip select
D2	IO	XCS6	I	Host chip select
D3	IO	DMA_XINT	O	DMA interrupt
D4	IO	CAP_XINT	O	CAP interrupt
D5	IO	VDEC_XCS	O	VDEC host chip select
D6	IO	GNDD	O	GND (unused pin)
D7	IO	BSLATCH	O	VDEC bus cycle latch
D8	IO	VDEC_D6	O	VDEC stream data
D9	IO	VDEC_D3	O	VDEC stream data
D10	GND	GNDD	-	GND
D11	IO	VDEC_D1	O	VDEC stream data
D12	IN	PLL_CLK	I	PLL clock
D13	GND	GNDD	-	GND
D14	IO	GSL_DREQ	O	GSL data request
D15	IO	GSL_DACK	I	GSL data acknowledge
D16	IO	GSL_D1	I	GSL stream data
D17	VCCIO4	VLTTL	-	Input/output power supply (3.3V)

■ EP2C20F484C8 (MAIN ASSY : IC2000) (3/8)

● Pin Function

No.	Pin name	Signal name	I/O	Function
D18	GND	GNDD	-	GND
D19	IO	DIS_DOUT	I	Panel microcomputer data input
D20	IO	DIS_DIN	O	Panel microcomputer data output
D21	IO	GSL_XWE1	O	GSL host write
D22	IO	GSL_XRD	O	GSL host read
E1	IO	XCS5	I	Host chip select
E2	IO	RDWR	I	Read write
E3	IO	XDREQ1	O	Host data request
E4	IO	XDREQ0	O	Host data request
E5	VCCD_PLL3	V+1R2PLL	-	PLL power supply (1.2V)
E6	VCCA_PLL3	V+1R2PLL	-	PLL power supply (1.2V)
E7	IO	DVC_XRST	I	Reset
E8	IO	VDEC_XWAIT	I	VDEC host wait
E9	IO	VDEC_D5	O	VDEC stream data
E10	VCCIO3	VLTTL	-	Input/output power supply (3.3V)
E11	IO	GNDD	O	GND (unused pin)
E12	GND+	GNDD	-	GND
E13	VCCIO4	VLTTL	-	Input/output power supply (3.3V)
E14	IO	GSL_D5	I	GSL stream data
E15	IO	GSL_D4	I	GSL stream data
E16	GNDA_PLL2	GNDD	-	GND(PLL)
E17	GNDG_PLL2	GNDD	-	GND(PLL)
E18	IO	SPD5	O	VDEC sub video data
E19	IO	SPD7	O	VDEC sub video data
E20	IO	SPD6	O	VDEC sub video data
E21	IO	GSL_A24	O	GSL address
E22	IO	GSL_A25	O	GSL address
F1	IO	XCS4	I	Host chip select
F2	IO	XCS2	I	Host chip select
F3	IO	A24	I	Host address
F4	IO	A25	I	Host address
F5	GNDG_PLL3	GNDD	-	GND(PLL)
F6	GNDD_PLL3	GNDD	-	GND(PLL)
F7	GNDA_PLL3	GNDD	-	GND(PLL)
F8	IO	GNDD	O	GND (unused pin)
F9	IO	CARD_XCS	O	TE4300 host chip select
F10	IO	CARD_XWR	O	TE4300 host write
F11	IO	GNDD	O	GND (unused pin)
F12	IO	CARD_XRD	O	TE4300 host read
F13	IO	GNDD	O	GND (unused pin)
F14	IO	GSL_D7	I	GSL stream data
F15	IO	GSL_D3	I	GSL stream data
F16	VCCA_PLL2	V+1R2PLL	-	PLL power supply (1.2V)
F17	VCCD_PLL2	V+1R2PLL	-	PLL power supply (1.2V)
F18	GNDD_PLL2	GNDD	-	GND(PLL)
F19	GND	GNDD	-	GND
F20	IO	SPD4	O	VDEC sub video data
F21	IO	MPD0	O	VDEC main video data
F22	IO	MPD2	O	VDEC main video data
G1	NC	-		NC
G2	NC	-		NC
G3	IO	A8	I	Host address
G4	GND	GNDD	-	GND
G5	IO	A24	I	Host address
G6	IO	A5	I	Host address
G7	IO	GNDD	O	GND (unused pin)
G8	IO	TI_HRDY	I	TI DSP ready
G9	VCCIO3	VLTTL	-	Input/output power supply (3.3V)
G10	GND	GNDD	-	GND
G11	IO	GNDD	O	GND (unused pin)
G12	IO	TI_XHDS1	O	TI DSP data strobe
G13	GND	GNDD	-	GND
G14	VCCIO4	VLTTL	-	Input/output power supply (3.3V)
G15	IO	GNDD	O	GND (unused pin)
G16	IO	AVD7	I	AV1 video data

■ EP2C20F484C8 (MAIN ASSY : IC2000) (4/8)

● Pin Function

No.	Pin name	Signal name	I/O	Function
G17	IO	SPD3	O	VDEC sub video data
G18	IO	SPD1	O	VDEC sub video data
G19	VCCIO5	VLTTL	-	Input/output power supply (3.3V)
G20	IO	MPD3	O	VDEC main video data
G21	IO	MPD1	O	VDEC main video data
G22	IO	M_PRG_27M	O	VDEC main video data clock
H1	IO	XBS	I	Host bus cycle start
H2	IO	A22	I	Host address
H3	IO	XWAIT	O	Host wait
H4	IO	A8	I	Host address
H5	IO	A6	I	Host address
H6	IO	GNDD	O	GND (unused pin)
H7	IO	GNDD	O	GND (unused pin)
H8	IO	GNDD	O	GND (unused pin)
H9	IO	TI_XCS	O	TIDSP host chip select
H10	IO	GNDD	O	GND (unused pin)
H11	IO	GNDD	O	GND (unused pin)
H12	IO	M_XRD	O	DSP host read
H13	IO	GNDD	O	GND (unused pin)
H14	IO	GNDD	O	GND (unused pin)
H15	IO	GNDD	O	GND (unused pin)
H16	IO	AVD2	I	AV1 video data
H17	IO	AVD6	I	AV1 video data
H18	IO	AVD5	I	AV1 video data
H19	IO	SPD2	O	VDEC sub video data
H20	GND	GNDD	-	GND
H21	NC	-		NC
H22	NC	-		NC
J1	IO	A2	I	Host address
J2	IO	A3	I	Host address
J3	NC	-		NC
J4	IO	LATCH	O	Latch (for debug)
J5	NC	-		NC
J6	NC	-		NC
J7	VCCIO2	VLTTL	-	Input/output power supply (3.3V)
J8	NC	-		NC
J9	NC	-		NC
J10	VCCINT	V+1R2	-	Core power supply (1.2V)
J11	VCCINT	V+1R2	-	Core power supply (1.2V)
J12	VCCINT	V+1R2	-	Core power supply (1.2V)
J13	VCCINT	V+1R2	-	Core power supply (1.2V)
J14	IO	GNDD	O	GND (unused pin)
J15	IO	GNDD	O	GND (unused pin)
J16	VCCIO5	VLTTL	-	Input/output power supply (3.3V)
J17	IO	AVD1	I	AV1 video data
J18	IO	AVD4	I	AV1 video data
J19	IO	MPD4	O	VDEC main video data
J20	IO	MPD6	O	VDEC main video data
J21	IO	MPD5	O	VDEC main video data
J22	IO	S_PRG_27M	O	VDEC sub video data clock
K1	nCE	-	I	Chip enable
K2	TCK	FTCK	I	Pull down
K3	GND	GNDD	-	GND
K4	DATA0	COFDAT1	I	Configuration data
K5	TDI	FTDI	I	Pull up
K6	TMS	FTMS	I	Pull up
K7	GND	GNDD	-	GND
K8	NC	-		NC
K9	VCCINT	V+1R2	-	Core power supply (1.2V)
K10	GND	GNDD	-	GND
K11	GND	GNDD	-	GND
K12	GND	GNDD	-	GND
K13	GND	GNDD	-	GND
K14	VCCINT	V+1R2	-	Core power supply (1.2V)
K15	NC	-		NC

■ EP2C20F484C8 (MAIN ASSY : IC2000) (5/8)

● Pin Function

No.	Pin name	Signal name	I/O	Function
K16	GND	GNDD	-	GND
K17	NC	-		NC
K18	NC	-		NC
K19	GND	GNDD	-	GND
K20	IO	MPD7	O	VDEC main video data
K21	IO	GNDD	O	GND (unused pin)
K22	IO	GNDD	O	GND (unused pin)
L1	IN	SYS_66M	I	System clock
L2	GND+	GNDD	-	GND
L3	VCCIO2	VLTTL	-	Input/output power supply (3.3V)
L4	nCONFIG	FPGA_XPRG	I	FPGA configuration signal
L5	TDO	FTDO	O	NC
L6	DCLK	COFIG_CLK4		Configuration serial clock
L7	NC	-		NC
L8	IO	GNDD	O	GND (unused pin)
L9	VCCINT	V+1R2	-	Core power supply (1.2V)
L10	GND	GNDD	-	GND
L11	GND	GNDD	-	GND
L12	GND	GNDD	-	GND
L13	GND	GNDD	-	GND
L14	VCCINT	V+1R2	-	Core power supply (1.2V)
L15	NC	-		NC
L16	NC	-		NC
L17	NC	-		NC
L18	IO	AVD3	I	AV1 video data
L19	IO	SPD0	O	VDEC sub video data
L20	VCCIO5	VLTTL	-	Input/output power supply (3.3V)
L21	GND+	GNDD	-	GND
L22	GND+	GNDD	-	GND
M1	GND+	GNDD	-	GND
M2	GND+	GNDD	-	GND
M3	VCCIO1	VLTTL	-	Input/output power supply (3.3V)
M4	GND	GNDD	-	GND
M5	IO	A4	I	Host address
M6	IO	A1	I	Host address
M7	NC	-		NC
M8	NC	-		NC
M9	VCCINT	V+1R2	-	Core power supply (1.2V)
M10	GND	GNDD	-	GND
M11	GND	GNDD	-	GND
M12	GND	GNDD	-	GND
M13	GND	GNDD	-	GND
M14	VCCINT	V+1R2	-	Core power supply (1.2V)
M15	NC	-		NC
M16	NC	-		NC
M17	MSEL0	MSEL0	I	Configuration mode select
M18	IO	AVD0	I	AV1 video data
M19	IO	AVSD7	O	AV1 stream data
M20	VCCIO6	VLTTL	-	Input/output power supply (3.3V)
M21	GND+	GNDD	-	GND
M22	IN	27M_FPGA	I	Video system clock
N1	IO	D0	IO	Host data
N2	IO	D16	IO	Host data
N3	IO	D2	IO	Host data
N4	IO	D3	IO	Host data
N5	NC	-		NC
N6	IO	D19	IO	Host data
N7	GND	GNDD	-	GND
N8	NC	-		NC
N9	VCCINT	V+1R2	-	Core power supply (1.2V)
N10	GND	GNDD	-	GND
N11	GND	GNDD	-	GND
N12	GND	GNDD	-	GND
N13	GND	GNDD	-	GND
N14	VCCINT	V+1R2	-	Core power supply (1.2V)

■ EP2C20F484C8 (MAIN ASSY : IC2000) (6/8)

● Pin Function

No.	Pin name	Signal name	I/O	Function
N15	IO	GNDD	O	GND (unused pin)
N16	GND	GNDD	-	GND
N17	MSEL1	MSEL1	I	Configuration mode select
N18	CONF_DONE	FPGA_DONE	IO	FPGA configuration signal
N19	GND	GNDD	-	GND
N20	nSTATUS	FPGA_XINIT	IO	FPGA configuration signal
N21	IO	AVSD5	O	AV1 stream data
N22	IO	AVSD6	O	AV1 stream data
P1	VCCINT	D17	IO	Host data
P2	NC	D18	IO	Host data
P3	NC	D4	IO	Host data
P4	NC	-		NC
P5	IO	D1	IO	Host data
P6	VCCIO1	D20	IO	Host data
P7	VCCIO1	VLTTL	-	Input/output power supply (3.3V)
P8	IO	GNDD	O	GND (unused pin)
P9	IO	GNDD	O	GND (unused pin)
P10	VCCINT	V+1R2	-	Core power supply (1.2V)
P11	VCCINT	V+1R2	-	Core power supply (1.2V)
P12	VCCINT	V+1R2	-	Core power supply (1.2V)
P13	VCCINT	V+1R2	-	Core power supply (1.2V)
P14	NC	-		NC
P15	IO	GNDD	O	GND (unused pin)
P16	VCCIO6	VLTTL	-	Input/output power supply (3.3V)
P17	IO	AVSD0	O	AV1 stream data
P18	NC	AVSD4	O	AV1 stream data
P19	NC	-		NC
P20	NC	-		NC
P21	NC	-		NC
P22	NC	-		NC
R1	IO	D21	IO	Host data
R2	IO	D22	IO	Host data
R3	GND	GNDD	-	GND
R4	NC	-		NC
R5	IO	XRD	I	Host read
R6	IO	D6	IO	Host data
R7	IO	GNDD	O	GND (unused pin)
R8	IO	GNDD	O	GND (unused pin)
R9	IO	GNDD	O	GND (unused pin)
R10	IO	GNDD	O	GND (unused pin)
R11	IO	GNDD	O	GND (unused pin)
R12	IO	GNDD	O	GND (unused pin)
R13	IO	GNDD	O	GND (unused pin)
R14	IO	GNDD	O	GND (unused pin)
R15	IO	GNDD	O	GND (unused pin)
R16	IO	GNDD	O	GND (unused pin)
R17	IO	AVBDACK	O	AV1 video data acknowledge
R18	IO	AVSD1	O	AV1 stream data
R19	IO	AVSD2	O	AV1 stream data
R20	IO	AVSD3	O	AV1 stream data
R21	IO	M_XCS	O	DSP host chip select
R22	GND	M_VALID	O	DSP audio data valid
T1	GND	D23	IO	Host data
T2	GND	D24	IO	Host data
T3	VCCIO1	D7	IO	Host data
T4	VCCIO1	VLTTL	-	Input/output power supply (3.3V)
T5	IO	D8	IO	Host data
T6	IO	D10	IO	Host data
T7	IO	GNDD	O	GND (unused pin)
T8	IO	GNDD	O	GND (unused pin)
T9	VCCIO8	V+2R5D	-	Input/output power supply (2.5V)
T10	GND	GNDD	-	GND
T11	IO	GNDD	O	GND (unused pin)
T12	IO	GNDD	O	GND (unused pin)
T13	GND	GNDD	-	GND

■ EP2C20F484C8 (MAIN ASSY : IC2000) (7/8)

● Pin Function

No.	Pin name	Signal name	I/O	Function
T14	VCCIO7	V+2R5D	-	Input/output power supply (2.5V)
T15	IO	GNDD	O	GND (unused pin)
T16	IO	GNDD	O	GND (unused pin)
T17	GND_PLL4	GNDD	-	GND(PLL)
T18	IO	GNDD	O	GND (unused pin)
T19	VCCIO6	VLTTL	-	Input/output power supply (3.3V)
T20	GND	GNDD	-	GND
T21	IO	GNDD	O	GND (unused pin)
T22	IO	M_XWE	O	DSP host /write
U1	IO	D25	IO	Host data
U2	IO	D5	IO	Host data
U3	IO	D9	IO	Host data
U4	GNDD_PLL1	PLL_CLK	O	PLL clock
U5	VCCD_PLL1	GNDD	-	GND(PLL)
U6	VCCA_PLL1	V+1R2PLL	-	PLL power supply (1.2V)
U7	VCCA_PLL1	V+1R2PLL	-	PLL power supply (1.2V)
U8	IO	DQ0	IO	DDR SDRAM data
U9	IO	DQM1	O	DDR SDRAM mask
U10	IN	GNDD	I	GND (unused pin)
U11	GND+	GNDD	-	GND
U12	GND+	GNDD	-	GND
U13	IO	GNDD	O	GND (unused pin)
U14	IO	GNDD	O	GND (unused pin)
U15	IO	GNDD	O	GND (unused pin)
U16	VCCA_PLL4	V+1R2PLL	-	PLL power supply (1.2V)
U17	VCCD_PLL4	V+1R2PLL	-	PLL power supply (1.2V)
U18	IO	AVXWE1	O	AV1 host/write
U19	IO	AVXCS	I	AV1 host chip select
U20	IO	AVBDREQ	O	AV1 video data request
U21	IO	M_BCLK	O	DSP audio data clock
U22	IO	M_REQ	I	DSP audio data request
V1	VCCIO8	D26	IO	Host data
V2	IO	D27	IO	Host data
V3	GND	GNDD	-	GND
V4	IO	GNDD	O	GND (unused pin)
V5	GND_PLL1	GNDD	-	GND(PLL)
V6	GNDA_PLL1	GNDD	-	GND(PLL)
V7	GNDA_PLL1	GNDD	-	GND(PLL)
V8	IO	DQ1	IO	DDR SDRAM data
V9	IO	GNDD	O	GND (unused pin)
V10	VCCIO8	V+2R5D	-	Input/output power supply (2.5V)
V11	IN	DQ15	IO	DDR SDRAM data
V12	GND+	GNDD	-	GND
V13	VCCIO7	V+2R5D	-	Input/output power supply (2.5V)
V14	IO	GNDD	O	GND (unused pin)
V15	IO	GNDD	O	GND (unused pin)
V16	GNDA_PLL4	GNDD	-	GND(PLL)
V17	GNDG_PLL4	GNDD	-	GND(PLL)
V18	GND_PLL4	GNDD	-	GND(PLL)
V19	IO	F_INITDONE	O	Pull up
V20	IO	AVXWE0	O	AV1 host/write
V21	IO	M_DATL1	O	DSP audio data
V22	GND	M_DBCLK	O	DSP audio data switching
W1	GND	D28	IO	Host data
W2	IO	D29	IO	Host data
W3	IO	D12	IO	Host data
W4	IO	D13	IO	Host data
W5	VCCIO8	D11	IO	Host data
W6	VCCIO8	V+2R5D	-	Input/output power supply (2.5V)
W7	IO	DQ2	IO	DDR SDRAM data
W8	IO	DQ3	IO	DDR SDRAM data
W9	IO	GNDD	O	GND (unused pin)
W10	GND	GNDD	-	GND
W11	IN	DQ14	IO	DDR SDRAM data
W12	GND+	GNDD	-	GND

■ EP2C20F484C8 (MAIN ASSY : IC2000) (8/8)

● Pin Function

No.	Pin name	Signal name	I/O	Function
W13	GND	GNDD	-	GND
W14	IO	GNDD	O	GND (unused pin)
W15	IO	DA6	O	Host address
W16	VCCIO7	DA5	O	Host address
W17	VCCIO7	V+2R5D	-	Input/output power supply (2.5V)
W18	NC	-		NC
W19	GND	GNDD	-	GND
W20	IO	GNDD	O	GND (unused pin)
W21	IO	M_DATR1	O	DSP audio data
W22	IO	M_DATL2	O	DSP audio data
Y1	IO	D30	IO	Host data
Y2	IO	D31	IO	Host data
Y3	IO	D14	IO	Host data
Y4	IO	D15	IO	Host data
Y5	IO	GNDD	O	GND (unused pin)
Y6	IO	DQM0	O	DDR SDRAM mask
Y7	VREFB8N1	VREF	-	DDR reference (1.25V)
Y8	GND	GNDD	-	GND
Y9	IO	DQS0	IO	DDR SDRAM DQS
Y10	VREFB8N0	VREF	-	DDR reference (1.25V)
Y11	VCCIO7	V+2R5D	-	Input/output power supply (2.5V)
Y12	VCCIO7	V+2R5D	-	Input/output power supply (2.5V)
Y13	IO	GNDD	O	GND (unused pin)
Y14	GND	DA7	O	Host address
Y15	GND	GNDD	-	GND
Y16	IO	GNDD	O	GND (unused pin)
Y17	IO	DA4	O	Host address
Y18	IO	AVDACK	O	AV1 host acknowledge
Y19	IO	AVDREQ	I	AV1 host request
Y20	IO	AVXWAIT	I	AV1 host wait
Y21	IO	SUB_CODE	O	Sub code
Y22	IO	M_DATR2	O	DSP audio data

■ HD6417709SHF200B (MAIN ASSY : IC111) (1/2) [CPU]

● Pin Function • MPU-IC

No.	Pin name	Signal name	I/O	Function
1	MD1	MD1	I	pull up
2	MD2	MD2	I	pull up
3	Vcc-RTC	V+2SH	-	Power supply for RTC (2.0V)
4	XTAL2	XTAL2	O	-
5	EXTAL2	EXTAL2	I	pull down
6	Vss-RTC	GNDD	-	Power supply for RTC (0V)
7	NMI	NMI	I	pull down
8	IRQ0	XBS_REQ	I	TI DSP Input data request
9	IRQ1	XPCM_REQ	I	TI DSP Output data request
10	IRQ2	CAP_XINT	I	Capture process interrupt
11	IRQ3	-	I	pull up
12	IRQ4	DIS_REQ	I	Operation section data receive complete interrupt
13	D31	D31	IO	Data bus
14	D30	D30	IO	Data bus
15	D29	D29	IO	Data bus
16	D28	D28	IO	Data bus
17	D27	D27	IO	Data bus
18	D26	D26	IO	Data bus
19	VssQ	GNDD	-	Power supply for input/output (0V)
20	D25	D25	IO	Data bus
21	VccQ	V+3SH	-	Power supply for input/output (3.3V)
22	D24	D24	IO	Data bus
23	D23	D23	IO	Data bus
24	D22	D22	IO	Data bus
25	D21	D21	IO	Data bus
26	D20	D20	IO	Data bus
27	Vss	GNDD	-	Power supply (0V)
28	D19	D19	IO	Data bus
29	Vcc	V+2SH	-	Power supply (2.0V)
30	D18	D18	IO	Data bus
31	D17	D17	IO	Data bus
32	D16	D16	IO	Data bus
33	VssQ	GNDD	-	Power supply for input/output (0V)
34	D15	D15	IO	Data bus
35	VccQ	V+3SH	-	Power supply for input/output (3.3V)
36	D14	D14	IO	Data bus
37	D13	D13	IO	Data bus
38	D12	D12	IO	Data bus
39	D11	D11	IO	Data bus
40	D10	D10	IO	Data bus
41	D9	D9	IO	Data bus
42	D8	D8	IO	Data bus
43	D7	D7	IO	Data bus
44	D6	D6	IO	Data bus
45	VssQ	V+3SH	-	Power supply for input/output (0V)
46	D5	D5	IO	Data bus
47	VccQ	V+3SH	-	Power supply for input/output (3.3V)
48	D4	D4	IO	Data bus
49	D3	D3	IO	Data bus
50	D2	D2	IO	Data bus
51	D1	D1	IO	Data bus
52	D0	D0	IO	Data bus
53	A0	A0	O	Address bus
54	A1	A1	O	Address bus
55	A2	A2	O	Address bus
56	A3	A3	O	Address bus
57	VssQ	GNDD	-	Power supply for input/output (0V)
58	A4	A4	O	Address bus
59	VccQ	V+3SH	-	Power supply for input/output (3.3V)
60	A5	A5	O	Address bus
61	A6	A6	O	Address bus
62	A7	A7	O	Address bus
63	A8	A8	O	Address bus
64	A9	A9	O	Address bus
65	A10	A10	O	Address bus
66	A11	A11	O	Address bus
67	A12	A12	O	Address bus
68	A13	A13	O	Address bus
69	VssQ	GNDD	-	Power supply for input/output (0V)
70	A14	A14	O	Address bus
71	VccQ	V+3SH	-	Power supply for input/output (3.3V)
72	A15	A15	O	Address bus
73	A16	A16	O	Address bus

■ HD6417709SHF200B (MAIN ASSY : IC111) (2/3) [CPU]

● Pin Function

No.	Pin name	Signal name	I/O	Function
74	A17	A17	O	Address bus
75	A18	A18	O	Address bus
76	A19	A19	O	Address bus
77	A20	A20	O	Address bus
78	A21	A21	O	Address bus
79	Vss	GNDD	-	Power supply (0V)
80	A22	A22	O	Address bus
81	Vcc	V+2SH	-	Power supply (2.0V)
82	A23	A23	O	Address bus
83	VssQ	GNDD	-	Power supply for input/output (0V)
84	A24	A24	O	Address bus
85	VccQ	V+3SH	-	Power supply for input/output (3.3V)
86	A25	A25	O	Address bus
87	XBS	XBS	O/IO	Bus cycle start signal
88	XRD	RD	O	Read strobe
89	XWE0	XWE0	O	Write strobe
90	XWE1	XWE1	O	Write strobe
91	XWE2	XWE2	O/IO	Write strobe
92	XWE3	XWE3	O/IO	Write strobe
93	RDXWR	RDWR	O	Read/write
94	XAUDSYNC	AUDSYNC	-	-
95	VssQ	GNDD	-	Power supply for input/output (0V)
96	XCS0	XCS0	O	CS0
97	VccQ	V+3SH	-	Power supply for input/output (3.3V)
98	XCS2	XCS2	O/IO	CS2
99	XCS3	XCS3	O/IO	CS3 (for SDRAM)
100	XCS4	XCS4	O/IO	CS4
101	XCS5	XCS5	O/IO	CS5
102	XCS6	XCS6	O	CS6
103	PTE[4]	P_XI	O/IO	Progressive/interlace switching
104	PTE[5]	PRO2_XCS	O/IO	Chip select output for PM0033A
105	CKE	CKE	O/IO	CKE(SDRAM)
106	XRAS3L	RAS3L	O/IO	RAS3L(SDRAM)
107	PTJ[1]	AV1_RST	O	AV-1 reset
108	XCASL	CASL	O/IO	CASL(SDRAM)
109	VssQ	GNDD	-	Power supply for input/output (0V)
110	PTJ[3]	MOT_RST	O	Reset for Motorola DSP
111	VccQ	V+3SH	-	Power supply for input/output (3.3V)
112	PTJ[4]	TI_RST	O	Reset for TI DSP
113	PTJ[5]	VDEC_RST	O	Reset for MPEG decoder
114	XDACK0	XDACK0	O	DMA acknowledge 0
115	XDACK1	XDACK1	O	DMA acknowledge 1
116	PTE[6]	AQE_RST	O	PD0274A reset
117	PTE[3]	-	-	-
118	XRAS3U	44X48	O/IO	44.1/48K selection
119	PTE[1]	MUTE	O/IO	Audio output stage mute (mute ON at H)
120	TDO	TDO	-	-
121	XBACK	BACK	O	-
122	XBREQ	BREQ	I	pull up
123	XWAIT	XWAIT	I	Connect to FPGA
124	XRESETM	RESETM	I	pull up
125	XADTRG	FPGA_DONE	I	DONE for FPGA configuration
126	XIOIS16	RY/XBY	I	Connection to FLASH ROM
127	XASEMD0	ASEMD0	-	-
128	XASEBRKAK	ASEBRKAK	-	-
129	PTG[4]	FPGA_XINIT	I	INIT for FPGA configuration
130	AUDATA[3]	AUDATA[3]	-	-
131	AUDATA[2]	AUDATA[2]	-	-
132	Vss	GNDD	-	Power supply (0V)
133	AUDATA[1]	AUDATA[1]	-	-
134	Vcc	V+2SH	-	Power supply (2.0V)
135	AUDATA[0]	AUDATA[0]	-	-
136	XTRST	TRST	-	-
137	TMS	TMS	-	-
138	TDI	TDI	-	-
139	TCK	TCK	-	-
140	PINT[11]	VDEC_XINT	I	MPEG2 decoder interrupt
141	PINT[10]	AV1_XINTR	I	PINT[10] AV-1 INTR interrupt
142	PINT[9]	GSL_XIRQS	I	PIN[9] GSL XIRQS interrupt
143	PINT[8]	GSL_XIRQ	I	PIN[8] GSL XIRQ interrupt
144	MD0	MD0	I	pull up
145	Vcc-PLL1	V+2SH	-	Power supply for PLL1 (2.0V)
146	CAP1	CAP1	-	C

■ HD6417709SHF200B (MAIN ASSY : IC111) (3/3) [CPU]

● Pin Function

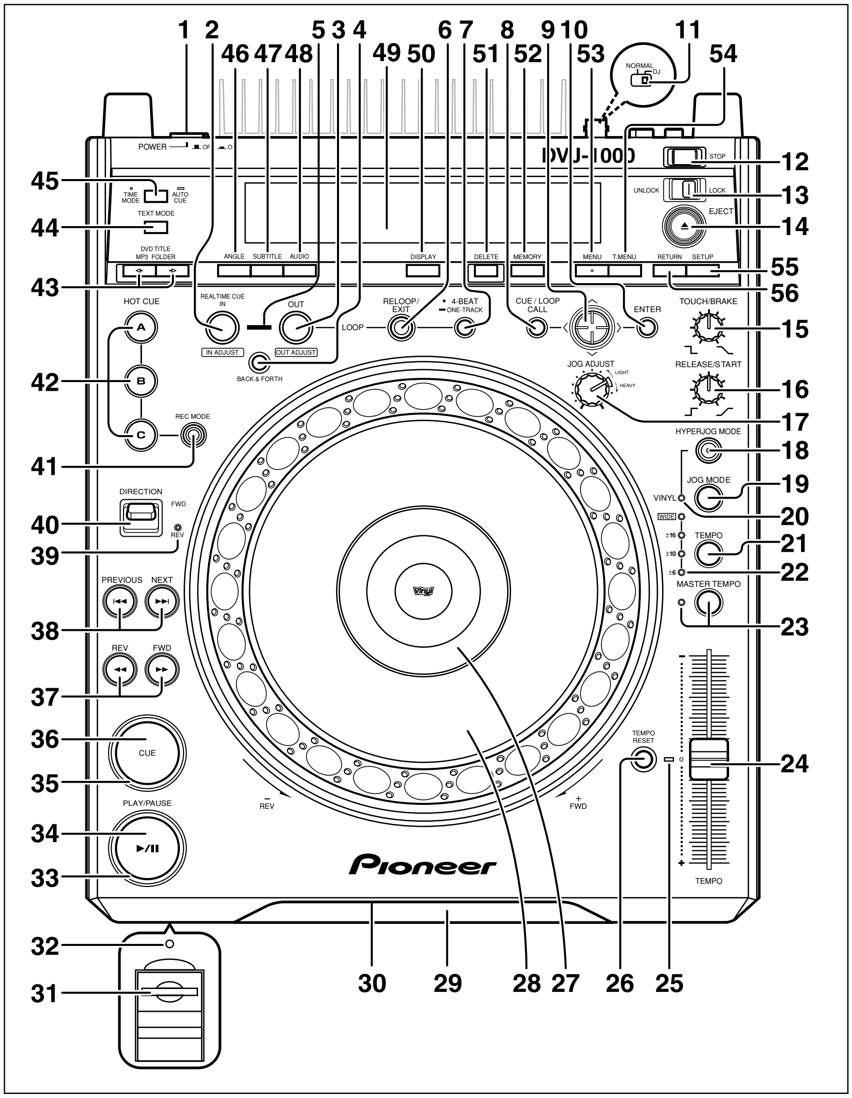
No.	Pin name	Signal name	I/O	Function
147	Vss-PLL1	GNDD	-	Power supply for PLL1 (0V)
148	Vss-PLL2	GNDD	-	Power supply for PLL2 (0V)
149	CAP2	CAP2	-	C
150	Vcc-PLL2	V+2SH	-	Power supply for PLL2 (2.0V)
151	AUDCK	AUDCK	I	Use with JTAG terminal
152	Vss	GNDD	-	Power supply (0V)
153	Vss	GNDD	-	Power supply (0V)
154	Vcc	V+2SH	-	Power supply (2.0V)
155	XTAL	XTAL	O	-
156	EXTAL	EXTAL	I	GNDD
157	PTJ[6]	XPCM_ACK	O	TI DSP Output data acknowledge
158	PTJ[7]	XBS_ACK	O	TI DSP Input data acknowledge
159	PTH[7]	FPGA_XPRG	IO	PRG for FPGA configuration
160	XIRQOUT	-	O	-
161	VssQ	GNDD	-	Power supply for input/output (0V)
162	CKIO	CKIO	I	Clock input (66MHz)
163	VccQ	V+3SH	-	Power supply for input/output (3.3V)
164	TxD0	MD	O	FPGA configuration serial data output
165	SCK0	MC	IO	FPGA configuration serial clock output
166	TxD1	OSD_MD	-	-
167	SCK1	OSD_MC	-	-
168	TxD2	TXD2	O	RS-232-C communication output
169	SCPT[5]	OSD_RESET	-	-
170	SCPT[6]	OSD_XCS	-	-
171	SCPT[0]	DJMODE_SW	I	Normal mode/DJ mode switching SW
172	RxD1	-	-	-
173	Vss	GNDD	-	Power supply (0V)
174	RxD2/SCPT[4]	RxD2	I	RS-232-C communication input
175	Vcc	V+2SH	-	Power supply (2.0V)
176	IRQ5	AV1_XINT2	I	AV-1 INT2 interrupt
177	PTC[7]	VQE_RST	O	ADV7172 reset
178	PTC[6]	DMA_RST	O	Reset for DMA FPGA
179	PTC[5]	CAP_RST	O	Reset for capture FPGA
180	PTC[4]	VMC_RST	O	Reset for VMC FPGA
181	VssQ	GNDD	-	Power supply for input/output (0V)
182	PTD[3]	TE4300_RST	O	TE4300 reset
183	VccQ	V+3SH	-	Power supply for input/output (3.3V)
184	PTD[2]	-	-	-
185	PINT[3]	CAP_XINT	O/IO/I	Capture FPGA interrupt
186	PINT[2]	TE4300_XINT	O/IO/I	TE4300 interrupt
187	PINT[1]	DMA_XINT	O/IO/I	DMA FPGA interrupt
188	PINT[0]	-	O/IO/I	-
189	PTD[1]	GSL_RST	O	GSL reset
190	PTD[0]	-	-	-
191	XDREQ0	DREQ0	I	DMA request 0
192	XDREQ1	DREQ1	I	DMA request 1
193	XRESETP	XRESET	I	Reset input
194	CA	CA	I	pull up
195	MD3	MD3	I	pull down
196	MD4	MD4	I	pull up
197	MD5	MD5	I	pull down
198	AVss	GNDD	-	Power supply for analog (0V)
199	PTL[0]	MOT_EMPTY	I	Motorola FIFO empty signal input (High at EMPTY)
200	AN[1]	-	I	-
201	AN[2]	-	I	-
202	AN[3]	-	I	-
203	AN[4]	-	I	-
204	AN[5]	-	I	-
205	AVcc(3.3V)	V+3SH	-	Power supply for analog (3.3V)
206	AN[6]	-	I	-
207	AN[7]	-	I	-
208	AVss	GNDD	-	Power supply for analog (0V)

8. PANEL FACILITIES

8.1 PANEL FACILITES

Operation panel

Buttons and controls with the DJ mark are disabled when the NORMAL/DJ switch is set to NORMAL.
Buttons and controls with the NORMAL mark are disabled when the NORMAL/DJ switch is set to DJ.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54
NORMAL
DJ
POWER
OFF
ON
DVJ-1000
STOP
UNLOCK
LOCK
EJECT
12
13
14
45
TIME MODE
AUTO CUE
TEXT MODE
44
DVD TITLE
MP3 FOLDER
ANGLE
SUBTITLE
AUDIO
DISPLAY
DELETE
MEMORY
MENU
T.MENU
RETURN
SETUP
55
56
43
HOT CUE
REALTIME CUE IN
OUT
RELOOP/EXIT
4-BEAT
ONE-TRACK
CUE / LOOP CALL
ENTER
TOUCH/BRAKE
15
LOOP
IN ADJUST
OUT ADJUST
BACK & FORTH
JOG ADJUST
LIGHT
HEAVY
RELEASE/START
A
B
C
42
16
17
REC MODE
HYPERJOG MODE
18
41
JOG MODE
19
VINYL
20
DIRECTION
FWD
40
REV
39
WIDE
±16
±10
±6
TEMPO
21
22
PREVIOUS
NEXT
MASTER TEMPO
38
23
REV
FWD
37
36
CUE
24
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TEMPO RESET
REV
FWD
PLAY/PAUSE
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Pioneer
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TEMPO
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1. **POWER OFF (■)/ON (▬) switch**
   Located on the rear panel.
2. **Loop in/realtime cue/loop in point adjust button/indicator (IN/REALTIME CUE/IN ADJUST) DJ**
   Real time cue
   Loop-in point input
   Loop-in point adjust
3. **Loop out/loop out point adjust button/indicator (OUT/OUT ADJUST) DJ**
   Loop-out point input
   Loop-out point adjust
4. **BACK & FORTH button DJ**
   Use to switch between normal loop mode and back & forth loop mode.
5. **Loop mode indicator DJ**
   Lights during back & forth loop mode.
6. **RELOOP/EXIT button/indicator DJ**
7. **4-BEAT/ONE-TRACK button DJ**
   - **4-Beat Loop**
     When this button is pressed, loop play will begin with that point as the loop-in point, and the loop-out automatically point set 4 beats later.
   - **One-Track Loop**
     When this button is held depressed for 1 second or more, the chapter loop or track loop play will begin.
8. **CUE/LOOP CALL button DJ**
   Press to turn cue point navigation mode ON/OFF.
9. **Cursor button (</>/∧/∨)**
   This button is used for cue point navigation; during MP3 playback, functions for performing selections with MP3 navigator, and for making various DVD settings.
10. **ENTER button**
    This button is used for cue point navigation; during MP3 playback, functions for performing selections with MP3 navigator, and for making various DVD settings.
11. **NORMAL/DJ switch**
    Located on the rear panel.
12. **STOP button**
    Stops disc playback. When the eject/stop mode select switch is set to **LOCK**, playback will not stop unless the PAUSE mode is set first.
13. **Eject/stop mode select switch (UNLOCK/LOCK)**
    UNLOCK: If the **EJECT** (⏏) button is pressed during playback, the disc stops and is ejected. If the **STOP** button is pressed during playback, the disc playback stops.
    LOCK: If the **EJECT** (⏏) button is pressed during playback, the disc will not be ejected. To eject the disc, set the unit to pause, then press the **EJECT** (⏏) button. Likewise, playback will not stop if the **STOP** button is pressed during playback; to stop disc playback, set the unit to pause, then press the **STOP** button.
14. **EJECT(⏏) button**
    When this button is pressed, disc rotation stops and the disc is ejected from the loading slot. If the eject/stop mode select switch is set to the **LOCK** position, the disc will not be ejected unless the unit is set to pause before pressing the **EJECT** (⏏) button .
15. **TOUCH/BRAKE response dial DJ**
    Adjusts the disc deceleration speed (time to playback stop) when the jog dial’s top surface is touched with jog mode set to VINYL ON. Rotate the dial counterclockwise to stop playback quickly, and rotate dial clockwise to cause slower deceleration.
16. **RELEASE/START response dial DJ**
    Adjusts the disc acceleration speed (playback startup time) when the jog dial’s top surface is released with jog mode set to VINYL ON. Rotate the dial counterclockwise to restart playback quickly, and rotate dial clockwise to cause slower acceleration.
17. **JOG ADJUST dial**
    Use to adjust the felt resistance (light/heavy) of the jog dial when it is rotated.
18. **HYPERJOG MODE button/indicator DJ**
    When hyper jog mode is set to ON with jog mode set to VINYL ON, turning the jog dial causes the rate of change of image and sound to increase to 4x the rate of changed normally produced (when the hyper jog mode is OFF).

19. JOG MODE select button DJ

Each time pressed, sets VINYL mode alternately ON/OFF.

VINYL mode ON: If surface of jog dial is touched during playback, playback stops, and if the dial is then rotated, image and sound are output in response to the amount of rotation. (if the angled surface of the jog dial is rotated without touching the top, pitch bend operation is enabled.)

- The current jog mode is memorized even when power is turned off.

VINYL mode OFF: The above operations are disabled even when the jog dial's surface is touched.

20. VINYL mode indicator DJ

Lights when the jog mode is set to VINYL mode.

21. TEMPO control range select button DJ

Each time this button is pressed, the **TEMPO** control slider's variable range changes (±6 %, ±10 %, ±16 %, WIDE).
When WIDE is selected, the variable range during DVD and MP3 playback is set to +70 % to –100 %, and during CD playback, ±100 %.

22. TEMPO control range indicator (±6, ±10, ±16, WIDE) DJ

- When control range is set to ±6 %, the ±**6** indicator lights.
- When control range is set to ±10 %, the ±**6** and ±**10** indicators light.
- When control range is set to ±16 %, the ±**6**, ±**10** and ±**16** indicators light.
- When control range is set to WIDE, the ±**6**, ±**10**, ±**16** and **WIDE** indicators light.

23. MASTER TEMPO button/indicator DJ

Each time this button is pressed, the master tempo function turns alternately ON/OFF.

24. TEMPO control slider DJ

When pulled forward (**+**), playback tempo is accelerated, and when pushed away(**–**), tempo is slowed.

25. TEMPO RESET indicator DJ

When lighted, indicates that the playback tempo is set to normal tempo "**0**", regardless of the position of the **TEMPO** control slider.

26. TEMPO RESET button DJ

Pressing this button instantly resets the playback tempo to "**0**" (normal tempo), regardless of the current position of the **TEMPO** control slider. Press the button once again to cancel the reset.

27. Jog dial display

28. Jog dial (+FWD/–REV) DJ

29. Disc loading slot

30. Forced eject hole

31. Memory card door and slot DJ

32. Memory card indicator DJ

This indicator lights with a memory card is loaded and the door is closed, and flashes during memory card access.

- Do not open the door or turn off power while the indicator is flashing.

33. PLAY/PAUSE indicator

Lights during playback, and flashes during pause mode.

34. PLAY/PAUSE (►/II) button

35. CUE indicator DJ

Lights to indicate a cue point has been set. Flashes during pause mode.

36. CUE button DJ

Cue point setting
Back cue
Cue point sampler

37. Manual search buttons (REV◄◄/FWD►►)

38. Track search buttons (PREVIOUS I◄◄/NEXT ►►I)

Use to return/advance playback by track (during CD, MP3) or chapter (during DVD).
During DVD playback, use to return to menu page or change page.

39. REV indicator DJ

Lights when DIRECTION FWD/REV select switch is set to reverse.

40. DIRECTION FWD/REV select switch DJ

Set to REV position for reverse playback.

41. HOT CUE REC MODE button DJ

Press to select the **HOT CUE** button's function (record/call).

- Defaults to call mode when power is switched on.

42. HOT CUE (A, B, C) buttons/indicators DJ

A, B, or C indicator lights red to indicate hot cue point record mode.
A, B, or C indicator lights green for hot cue point, and orange for hot loop point; when an indicator is lighted, call mode is enabled for that point; pressing the button initiates playback from the hot cue point. When indicator is not lighted, no hot cue point is recorded.

43. Title/folder search buttons (DVD TITLE/MP3 FOLDER⬅ / ➡)

During DVD playback, titles are forwarded or reversed in the direction corresponding to the button pressed.
During MP3 playback, goes in designated direction for folders in layered CD or DVD.

44. TEXT MODE select button

Each time this button is pressed, the display alternates between WAVE and TEXT (track name/album name/artist name).

45. TIME MODE/AUTO CUE button DJ

TIME MODE:

Each time the button is pressed, the time display switches alternately between playback elapsed time and remaining time (REMAIN). In the case of MP3, the remaining playtime (REMAIN) may not immediately be displayed, depending on the file.

- The time mode remains set in memory even when power is turned off.

AUTO CUE:

Hold depressed for one second or more to set/release the auto cue function.
Hold depressed for five seconds or more to switch the auto cue level .

- The auto cue ON/OFF setting and auto cue level remains set in memory even when power is turned off.

46. ANGLE button

Press to change the viewing angle during DVD playback (on supported discs only).

- In DJ mode, when the angle is changed both video and audio playback temporarily stop (due to writing to buffer memory).

47. SUBTITLE button NORMAL

During DVD playback, press to alternately turn subtitle display ON/OFF (on supported discs only).

- This function is disabled in DJ mode.

48. AUDIO button

During DVD playback, press this button to change language or audio channel (on supported discs only).

- In DJ mode, when the language/audio channel is changed, both video and audio playback temporarily stop (due to writing to buffer memory).

49. Display (**1 to 24**)

50. DISPLAY button

When pressed in DJ mode, turns the hot cue, cue point, and playback time guide display ON/OFF on the monitor connected to the preview video output connector .
When pressed in NORMAL mode, turns the disc information guide display ON/OFF.

- When power is switched ON, the display function defaults to ON when the unit is in the DJ mode, and OFF (no display) in the NORMAL mode.

51. DELETE button DJ

Press to delete cue points and loop points recorded in memory card.

52. MEMORY button DJ

Press to store cue points or loop points in memory card.

53. MENU button

Press to display DVD menu.
Also, during MP3 playback, displays MP3 navigator screen.

54. Top menu (T.MENU) button

Press to display a DVD's top menu level.

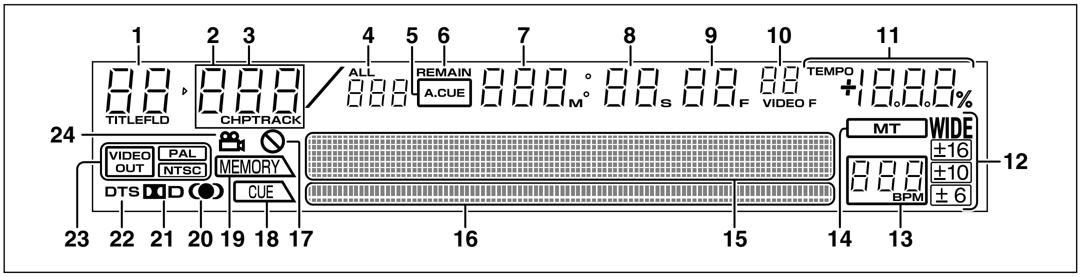
55. SETUP button

Press to display the setup menu.

56. RETURN button

When setup or other menus are displayed, press this button to return to the previous menu or item .

Display



1. Title/Folder number display (TITLE/FLD 00 to 99)
Indicates the DVD title number.
During MP3 playback, displays folder number.
Not displayed during CD playback.

2. Chapter number (CHP 000 to 999)
Indicates the DVD chapter number.
During CD/MP3 playback, "**CHP**" does not appear.

3. Track number (TRACK 00 to 99/000 to 999 (MP3))
Indicates CD or MP3 track number.
"**TRACK**" is not displayed during DVD playback.

4. Total track number (ALL)
When playing DVD, displays the total number of chapters within the title. When playing an audio CD, displays the total number of tracks on the disc. When playing MP3, displays the total number of tracks within the folder.

5. Auto cue (A.CUE) indicator
Lights when auto cue is ON. Does not light during NORMAL mode.

6. Remaining time (REMAIN) indicator
Indicates that the current numerical display is of a track's remaining time.

7. Minutes display (000 to 999 M)

8. Seconds display (00 to 59 S)

9. Frame display (00 to 74 F)
Display audio frame numbers. 75 frames are equivalent to one second of normal play. Not displayed in NORMAL mode.

10. Video frame display (00 to 29 VIDEO F)
Lights during DVD playback in DJ mode. 30 frames are equivalent to one second. Video frame display does not appear during CD playback and in NORMAL mode.
Since this unit controls video frames (1/30 second) based on the audio frame (1/75 second), combining the two will result in a maximum deviation of 1.

11. TEMPO indicator
Displays tempo variation produced when **TEMPO** control slider is operated.
Does not light in NORMAL mode.

12. Tempo control range display (±6, ±10, ±16, WIDE)
Lights to indicate the **TEMPO** control slider's variable range as selected with the tempo control range select button.
Does not light in NORMAL mode.

13. BPM counter display
Lights to indicate the beats per minute (BPM) of the currently playing track. Some tracks may not be measurable with the automatic BPM counter.
Does not light in NORMAL mode.

14. Master tempo indicator (MT)
Lights when master tempo function is ON.
Does not light in NORMAL mode.

15. Dot matrix display (100x7 dots)
Used for displaying various items, including TEXT, WAVE, guide, etc.
During CD TEXT display, shows up to 72 characters for disc title or up to 48 characters for track title; for MP3, displays up to 48 characters (display scrolls when 15 or more characters are to be shown).
In the case of WAVE display, the varying volume levels of the currently playing track are displayed, with the track sized to fit into the entire 100-dot display width.
For guide messages, "**Dot Matrix Guide Display Messages**".

16. Play address display
Displaysa bar graph to allow an immediate visual grasp of the elapsed and remaining playing time of the currently playing track. The full scale bar length indicates the full track length.

- ● The bar graph is off at the beginning of the track, and lights from left to right.
- ● The bar graph is lighted at the beginning of the track, and goes out from left to right.
- ● When the remaining playing time falls below 30 seconds, the bar graph flashes slowly; when less than 15 seconds are left, the bar graph flashes quickly.

17. Prohibited indicator (⦸)
Some DVD discs or players do not support certain functions or operations; if an effort is made to perform such operations, this indicator appears for about 2 seconds.

18. CUE point indicator
When a point is recorded in the **CUE** button or **IN/REALTIME CUE/IN ADJUST** button for the currently selected track, the "**CUE**" indicator lights and the point's relative starting position is shown in the display (2 lighted dots).
Does not light in NORMAL mode.

19. MEMORY display
If the currently selected track includes cue memory or loop memory, the "**MEMORY**" indicator lights and the MEMORY display indicates the relative starting position of the cue or loop. Two dots are used to display 1 point, and even if multiple starting positions are included within a single point, only one point is displayed.
Does not light in NORMAL mode.

20. SRS indicator ((●))
Lights when TruSurround function is selected. This function is disabled in DJ mode.

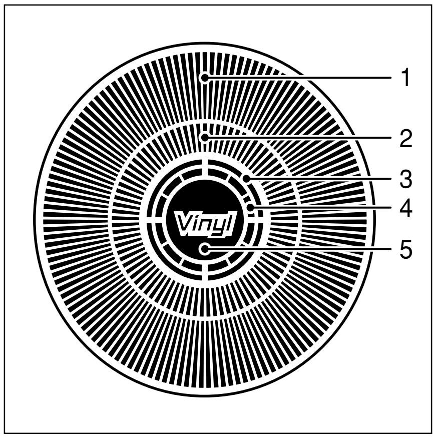
21. Dolby Digital indicator (□□ D)
Lights during playback when Dolby Digital audio has been selected.

22. DTS indicator
Lights during playback when DTS audio has been selected.
Not supported in DJ mode.

23. Video out indicators (VIDEO OUT/PAL/NTSC)
When the video output TV system is set to AUTO, only the "**VIDEO OUT**" indicator lights.
When NTSC has been selected, both the "**VIDEO OUT**" and "**NTSC**" indicators light.
When PAL has been selected, both the "**VIDEO OUT**" and "**PAL**" indicators light.

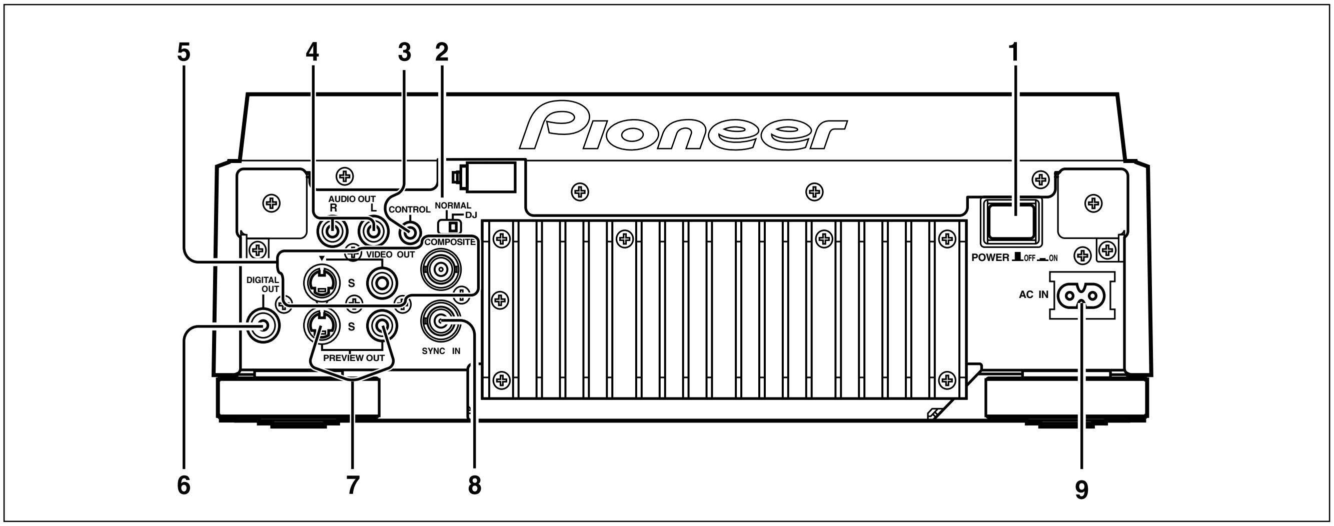
24. Angle indicator (🎥)
During DVD playback, this mark appears to indicate a scene with variable angle.

Jog dial display



1. **Operation display**
   Displays play position, with one revolution equivalent to 135 frames. The display rotates during playback, and stops during pause.
2. **Cue point position display**
   Displays the position of cue points.
3. **Audio/video memory status display**
   Flashes during writing to the audio/video memory. While indicator is flashing, it may not be possible to record real time cue points or hot cue points.
4. **Jog touch detector indicator**
   When VINYL mode is set to ON, this indicator lights when the top panel of the jog dial is touched.
5. **VINYL mode indicator (Vinyl)**
   Lights when VINYL mode is set to ON.

Rear panel



1. **POWER OFF (■)/ON (▬) switch**
2. **NORMAL/DJ switch**
   If this switch position is changed during playback, playback will stop, and then resume playback from the disc's beginning.
   DJ: For jog dial, tempo variation and other DJ operations. During DVD playback, subtitles and some other functions, operations, or playback points may not be supported.
   NORMAL: DJ functions are not supported. Pause mode is silent, not audible. During DVD playback, selected audio signals are output from the digital output connectors. During CD playback, digital data containing subcodes are output (does not support CD graphics).
3. **CONTROL connector**
   Using the supplied accessory control cord, this connector can be connected to a Pioneer DJ mixer (sold separately) to allow control of this unit from the DJ mixer. This facilitates the use of functions such as fader start play and back cue. Alternately, linking this connector to another DJ player allows automatic relay play.
4. **AUDIO OUT L/R connectors**
   RCA type analog audio output connectors.
5. **Main video output connectors (VIDEO OUT)**
   Includes RCA and BNC type connectors for outputting DVD playback video only (composite signals), as well as S-Video output connector.
6. **DIGITAL OUT connector**
   RCA type coaxial digital output, for connecting AV amplifier, Dolby Digital/DTS decoder, CD recorder, etc.
   When **NORMAL/DJ** switch is set to "**DJ**", during DVD playback the unit outputs 2-channel linear PCM digital data, regardless of the selected audio signal format. During CD playback, the unit outputs only audio data that do not include subcodes.
   When **NORMAL/DJ** switch is set to "**NORMAL**", during DVD playback the unit outputs digital data in the audio signal format selected. During CD playback, the unit outputs digital data including subcodes.
7. **Preview video output connectors (PREVIEW OUT)**
   RCA type connector (composite signals) and S-Video output connector. These connectors output monitor images used to aid the DJ during operation. Outputs various guide messages and displays.
8. **SYNC IN connector**
   BNC type input connector for inputting external sync signal . Use to connect optional dedicated sync signal generator.
9. **AC inlet (AC IN)**
   Use auxiliary power cord to connect to standard electrical outlet.

Jigs list

Name	Jig No.	Remarks
DVD Test Disc (DVD-Video)	GGV1025	Check of DVD-Video
DVD Data Disc	GGV1175	Diagnosis (ID data setting)
DVD Audio Disc	GGV1070	Check of DVD-Audio

Lubricants and Glues list

Name	Lubricants and Glues No.	Remark
Dyefree	GEM1036 (ZLX-ME413A)	Refer to "2.5 SLOT-IN MECHANISM SECTION"
Grease	GYA1001 (ZLB-PN397B)	Refer to "2.4 JOG SECTION", "2.5 SLOT-IN MECHANISM SECTION" and "2.6 TRAVERSE MECHA ASSY-S SECTION"
Grease	GYA1007 (ZLB-PN948P)	Refer to "2.6 TRAVERSE MECHA ASSY-S SECTION"
Lubricants	(ZLB-HRD1500)	Refer to "2.4 JOG SECTION"

Cleaning

• Before shipping out the product, be sure to clean the following positions by using the prescribed cleaning tools:

Position to be cleaned	Cleaning tools	Remark
Pickup leneses	Cleaning liquid : GEM1004 Cleaning paper : GED-008	Refer to "2.6 TRAVERSE MECHANISM ASSY-S SECTION" , "7.1.7 DISASSEMBLY SECTION".